# PRIME

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#### PRIME ENGINEERING HANDBOOK

for

PRIME P400, P500; PRIMOS IV, V

June, 1978

PRIME ENGINEERING HANDBOOK

This revision corresponds to PRIMOS REV 15.

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PE-T-500 REV 0

PE-T-500 REV 0

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#### 1 INTRODUCTION

#### OVERVIEW

This handbook provides a summary of information needed for the development and maintenance of Prime P400 and P500 hardware and software systems. While this book contains information useful to a general user community, the information is presented in very condensed form. It is assumed that the reader has had prior contact with this material and, therefore, that detailed descriptions are unnecessary.

Some of the information contained herein pertains only to the latest revision of PRIMOS. This information will be updated on a regular basis as new revisions are released. (Refer to the inside of the cover for the revision currently reflected in this version of the handbook.) Readers are strongly urged to report errors, discrepancies, and omissions as soon as they are noticed.

#### SYNTAX

The descriptions of commands and external programs use the following syntax:

#### Abbreviations

Uppercase letters represent minimal abbreviations for commands and options. (When actually typing the command or option, either uppercase or lowercase can usually be used. Exceptions are noted.) For example:

#### COMOutput

specifies the COMOUTPUT command. COMO, COMOU, COMOUTP, etc., also specify this command.

#### Command Line Variables

Greater-than (>) and lesser-than (<) signs surrounding a string indicate a variable for which specific information is to be substituted; for example:

#### <filename>

should be replaced with a valid filename. Notice, however, that the use of greater-than and lesser-than signs <u>in treenames</u> is literal.

#### INTRODUCTION

## Optional Parameters

Brackets enclose optional parameters for a command; for example:

SHutdn [ ALL ]

#### Alternative Operand Specification, Defaults

When an operand has more than one possible specification, choices are stacked. A default option, if any, is underscored; for example:

The OPRPRI command accepts a single parameter of 1 or  $\emptyset$ . If none is specified, the default parameter is  $\emptyset$ .

#### Repeated Operands

Ellipsis indicate an operand that may be repeated one or more times; for example:

The CLOSE command accepts one or more file unit specifications (separated by blanks or commas).

#### 2 CENTRAL PROCESSING UNIT

#### ARGUMENT POINTER (AP)

#### 16 1 B|BBB|I-Z|ZLS|---|P|PPP|PPW|WWW|WWW|WWW

BBBB Bit Number

Ι Indirect

Base Register: 00 - PB zz

Ø1 - SB

10 - LB

11 - XB

L Last AP in arglist

S Store

P..W Page/Word Disp from Base Reg

#### Typical values, first word:

100	PB Rel.	4100	PB Rel.	Indirect	
300	PB Rel. Last	4300	PB Rel.	Indirect	Last
500	SB Rel.	4500	SB Rel.	Indirect	
700	SB Rel. Last	4700	SB Rel.	Indirect	Last
1100	LB Rel.	5100	LB Rel.	Indirect	
1300	LB Rel. Last	5300	LB Rel.	Indirect	Last
1500	XB Rel.	5500	XB Rel.	Indirect	
1700	XB Rel. Last	57ØØ	XB Rel.	Indirect	Last

#### CHECKS

CHECK HEADER (4 WORDS): PBH, PBL, KEYS, MODALS

Power Fail 4/200

4/270 Memory Parity

4/300 Machine Check

Missing Memory Module 4/310

On entry to fault handler, mode=64V, MCM=0 for all but ECCC, for which MCM=MCM-at-check - 1.

MMOD interrupts any other check in progress.

MCHK and ECCU interrupt ECCU in progress if MCM=2 (QUIET).

#### CONCEALED STACK/QUEUE

(Valid only between time of fault and subsequent CALF instruction.)

PCB+'74 --> FIRST +Ø PBH PCB+'75 --> NEXT +l PBL PCB+'76 --> LAST +2 KEYS +3 FCODE +4 FADDRH +5 FADDRL

(PB, KEYS are those of procedure causing the fault.)

#### DIAGNOSTIC STATUS WORD (DSW)

DSWRMA: R34 DSWSTAT: R35 DSWPB: R36

#### DSWSTATH:

1 2 3 4 5-7	100000 04000 020000 010000 007000	MC - Machine Check MP - Memory Parity (ECC) MM - Missing Memory
		xx4xxx Peripheral Addr (BPA) Output
		xx5xxx RDX-BPD Input
		xx6xxx Memory Address (BMA)
		xx7xxx Register File (RF)
8	000400	Not RCM Parity (P500, XCS)
9	000200	ECCU ECC Uncorrectable Error
10	000100	ECCC ECC Correctable Error
11	000040	BUP Invalid RP Backup Count Invalid
12-14	000034	RP Backup Count Sub from DSWPB
15	000002	Check During DMX
16	000001	IO Bus DMX, PIO, u-code check

#### DSWSTATL:

1 6	174000	ECCC Syndrome*:	
1-3	1/4000	000xxx MB	100xxx MB
		004xxx MB	
		010xxx MB 014xxx 15	110000 3
		020xxx MB	120000 2
		024xxx 14	124xxx 2
		030xxx 13	130XXX 1
		Ø34xxx 9	134XXX C2
		Ø4Øxxx MB	140XXX 6
		044xxx MB	144xxx b
		050xxx MB	150xxx 5
		054xxx 12	
		Ø6Øxxx 16	160xxx 4
		Ø64xxx 11	164xxx C4
		070xxx 10	
		074xxx RP,Cl	174xxx NE
6	002000	OP Overall Pa	rity
7	001000	Unused	
8	999499	Mod # (L.O. addr	bit of module in err)
9	000200	RMA Invalid	
	000100		
1116	000100	U-Verify Test Nu	mber
11-10	DDDD / /	O Verri	
		*** U-Verify Tes	t Number ***
		to be supplied a	t next update
		co be puppared a	
* MD-1	m1+ihi+	RP,LP=Right/Left	Parity,
- MB-L	intribit,	Re , in Right/ Bere	

Cn=Check Bit n, NE=No Errors

See Micro Code Handbook for additional information.

#### DMQ

See DMX under Peripheral I/O.

# DESCRIPTOR TABLE ADDRESS REGISTER (DTAR)

1	16 1	16
SISSISS	S SSS AAA AAA A* A*AA AAA	AAA AAA AAA

S...S 1024 - # SDWs in Table.

A...A High Order 21 bits of Physical Addr of Table.
(Bit 22 taken as zero.)

\*: bits 1 and 2 of the second word must be equal.

#### ENTRY CONTROL BLOCK (ECB)

Ø	PROCEDURE	1
1	BASE	
2	STACK FRAME SIZE	1
3	SN OF STACK ROOT	(Ø => USE CURRENT)
4	DISP OF ARGLIST IN S.F.	1
5	NUMBER OF ARGUMENTS	1
6	LINK	
7	BASE	
10	KEYS	1

Locations 'll through 'l7 are set to  $\emptyset$ .

#### FAULTS

FCODEH: CRS 26H FADDR: CRS 27

FAULT	#	OFFSET	VECT	FCODEH	FADDR	RING	SAVED PB
RXM	Ø	Ø	62		addr	curr	backed
PROCESS	1	4	63	ABFLAGS		Ø	curr
PAGE	2	10	64		addr	Ø	backed
SVC	3	14	65			curr	backed
UII	4	20	66	cur PBL	addr	curr	backed
ILL	10	40	72	cur PBL	addr	curr	backed
ACCESS	11	44	73	code	addr	Ø	backed
ARITH	12	5Ø	74	code	addr	curr	curr
STACK	13	54	75	code	addr	Ø	backed
SEGMENT	14	60	76	code	addr	Ø	backed
POINTER	15	64	77	code	ptr addr	curr	backed

#### INSTRUCTION SET

See Section 5.

#### INDIRECT POINTER (IP)

1			16						16	
FRRE	SSS SSS	SSS	SSS	P	PPP	PPW	WWW	WWW	WWW	1

F 1 => Missing Pointer

RR Ring Number (00-11)

E 1 => Word 3 = bit number: BBBB-----

S..S Segment Number

P..W Page Number/Word Number

(As effective address in a base reg, F,E ignored.)

# KEYS, MODALS

(CRS 24 RFILE 124,164 CRASH 50,150)

#### KEYSH (Keys):

```
100000 C Bit
      040000 Double Precision
2
      020000 Link Bit (L)
3
      016000 Addressing Mode:
                 x00xxx 16S
                 xØ2xxx 32S
                 x04xxx 64R
                 x06xxx 32R
                 xl0xxx 32I
                 x14xxx 64V
      001000 FLEX (0 => Allow Fault)
7
      000400 IEX (Integer Exception)
8
       000200 CCLT (condition code)
9
      000100 CCEQ (condition code)
11-14 000074 Unused
       000002 ID (In Dispatcher)
15
       000001 SD (Save Done)
16
KEYSL (Modals):
```

1 2	100000 040000	<pre>ENB (1 =&gt; Enable Interrupts) VIM (1 =&gt; Vectored Int. Mode)</pre>
3-8	037400	Unused
9-11	000340	CRS: $xxx00x => Reg File 2$
		$xxx04x \Rightarrow Reg File 3$
12	000020	MIO (1 => Mapped $I/O$ )
13	000010	PXM (1 => Process Exchange Mode)
14	000004	SEG (1 => Segmentation Mode)
15-16	000003	MCM (Machine Check Mode):
		xxxxx0 None
		xxxxxl Memory Parity
		xxxxx2 Quiet
		xxxxx3 Record

#### MODALS

See KEYS (KEYSL).

#### PAGE MAPS (HMAP, LMAP)

Starts at 4/4000. HMAP, LMAP interleaved in 64-word chunks, thus 128 words/segment in system.

#### HMAP (Hardware Map):

1	100000	1 => Page Resident.
2	040000	1 => Page Referenced.
3	020000	<pre>Ø =&gt; Page Modified.</pre>
4	010000	<pre>1 =&gt; Shared Page (inhibits cache).</pre>
5-16	007777	H.O. 12 bits of physical page addr.
		(Bits 13-22 taken as zero.)

If non-resident, bits 3,5 software defined:

3,5 024000 Page status:
000000 Not resident, copy on disk
020000 Not resident, no copy on disk
004000 In transition, coming in
024000 In transition, going out

#### LMAP (Software Map -- HMAP+'100):

1-2	140000	Lock ctr $\emptyset$ = unlock, not $\emptyset$ = locked
3	020000	First Time (just paged in)
4	010000	Use alternate paging device
5-16	ØØ7777	Record index (1 val/8 pages)

SEGMENT	PAGE MAP LOCATION
_	
Ø	4000
1	4200
4	4400
5	4600
6	5000
7	5200
10	5400
11	5600
12	6000
6000	6200

#### PANEL

See MEMORY/REGISTER DISPLAY under OPERATIONAL PROCEDURES.

# PROCESS CONTROL BLOCK (PCB)

(See also PCBs under PRIMOS IV.)

~	TENTE (T) DELENI TION	40	CD7
	LEVEL (IN READY LIST)		
	LINK (NEXT PCB IN LIST)		
2	WAIT LIST SN (Ø=>READY)	42	FPØ
	WAIT LIST WORD NUMBER	43	
_	ABORT FLAGS		H .
_	RESERVED		н
6			FP1
		47	
10	ELAPSED TIMER (LOW)	5Ø	н
11	ELAPSED TIMER (HIGH)	51	n
12	DTAR2H	52	PBH
13	DTAR2L	53	PBL
14	DTAR3H	54	SBH
15	DTAR3L	55	SBL
16	INTERVAL TIMER	56	LBH
17	RESERVED	57	LBL
2Ø	SAVE MASK	60	XBH
21	KEYS	61	XBL
22	GRØ	62	FAULT VECTOR, RING Ø
23	11	63	m ,
24	GR1	64	FAULT VECTOR, RING 1
	"	65	
	GR2	66	RESERVED
27	11	67	11
	GR3		FAULT VECTOR, RING 3
	11	71	
	GR4	72	PAGE FAULT VECTOR
33	11	73	
	GR5		CONCEALED STACK FIRST
	II .		CONCEALED STACK NEXT
	GR6		CONCEALED STACK LAST
37	II		RESERVED
31		,,	TANDER A TITA

#### READY LIST

CPU

PPA current level/current PCB
PPB next level/next PCB
LEVELn --> FIRST PCB ON LEVELn
LEVELn+1 --> LAST PCB ON LEVELn
PCB+0 --> LEVEL THIS PCB IS ON
PCB+1 --> NEXT PCB, 0 IF LAST

Ready list in Segment 4, starting at 4/600:

#### LEVEL PCBS ON LEVEL

600	CLOCK PROCESS
602	SMLC PROCESS
604	AMLC PROCESS
606	MPC, MP2 PROCESSES
610	VERSATEC PROCESS
612	IPC PROCESS
614	RINGNET PROCESS
616	SPARE1, SPARE2 PROCESSES
620	SUPERVISOR PROCESS (USER 1)
622	PRIORITY 3 USER PROCESSES
624	PRIORITY 2 USER PROCESSES
626	PRIORITY 1 USER PROCESSES (NORMAL LEVEL)
63Ø	PRIORITY Ø USER PROCESSES
632	BACKSTOP PROCESS

#### RSAV FORMAT

Registers as saved/restored by the RSAV/RRST instructions.

Ø 1	SAVE MASK   FRN1
3	FRI
5	FRNØ
7	   FRØ
11	GR7
13	GR6
15	GR5
17	GR4
21	GR3
23	GR2
25	GR1
27	GRØ
31	X-BASE

## SAVE MASK (1=>REG SAVED):

SAV	אכואויו ב	(T-\VEG	SHVED
1-4	170000	Unused	3
5	004000	FRNl	
6	002000	FRl	
7	001000	FRNØ	
8	000400	FRØ	
9	000200	GR7	
10	000100	GR6	
11	000040	GR5	
12	000020	GR4	
13	000010	GR3	
14	000004	GR2	
15	000002	GR1	
16	000001	GRØ	
(XB	always	saved.)	

#### SECTOR Ø (P300 only)

```
X Register (Index Register)
         A Register (Arithmetic, Shifts, I/O)
  2
         B Register (Ext Arithmetic, Shifts)
 3
         Stack Pointer
         FLPH (Floating Point High)
         FLPL (Floating Point Low)
 6
         VSC (Visible Shift Counter)
         P Register (Program Counter)
10
         PMAR (Page Map Address Register)
11
         Microcode Scratch Location
12
         EAS (Effective Addr Save - ILL, UII, Ints)
13
         Microcode Scratch Location
14
         Y Register Save (Control Panel, DMA)
15
         M Register Save (Control Panel, DMA)
16
         Microcode Scratch Location
17
         Microcode Scratch Location
        DMA Range/Start Address Pairs
 20-37
 40-47
        Reserved for DMC Channel Pairs
 60
         PFI (Power Fail Int, Watchdog Timer)
 61
         RTCI (Real Time Clock Increment)
 62
         REVI (Restricted Execution Violation Int.)
 63
         Standard Interrupt (Compatible Mode)
 64
         Page Fault Interrupt
 65
         SVC Interrupt
 66
         UII (Unimplemented Instruction Interrupt)
         Memory Data Parity Error
 67
 7Ø
         Machine Check
 71
         Missing Memory Module
72
         Illegal Instruction Interrupt
 73
         Page Write Violation
74
         FLEX (Floating Point Exception)
         Procedure Stack Underflow (300 Only)
76-100 Debugging Scratch Area
101-177 Interrupt Vectors (Vectored Interrupt Mode)
200-777 General Cross Sector Links
```

#### SEGMENT DESCRIPTOR WORD (SDW)

1			16	1			16
PPP	PPP   P	PP   ØØ	000   000	FAA	A BBB	CCC PPP	PPP
7			22			1	6

```
P...Ø Page number/Word number of pagemap
       Fault, 1 => No segment or missing pagemap
AAA
      Access controls for Ring 1:
                                    000 No access
      Access controls for Ring 2
BBB
                                    001 Gate
CCC
      Access controls for Ring 3
                                    010 Read
                                    011 Read/Write
                                    100 Reserved
                                    101 Reserved
                                    110 Read/XEQ
                                    111 R/W/XEQ
```

#### REGISTERS

RFIL ADDR = Address in Register File CRASH ADDR = Disp in hardware register save area.

		CRASH	RFIL			CRASH
HIGH	LOW	ADDR	ADDR	HIGH	LOW	ADDR
TRØ	-	300	40			200
TRl	-	3Ø2	41			202
TR2	-	304	42			204
TR3	_	306	43			206
TR4	-	310	44			210
TR5	-	312	45			212
TR6	-	314	46			214
TR7 (PB)	-	316	47			216
GRMX1	-	320	5Ø			220
GRMX2	-	322	51			222
	RATMPL	324	52			224
RSGT1	_	326	53			226
RSGT2	-	33Ø	54			230
RECC1	-	332				232
RECC2	-	334	56			234
	REOIV		57			236
ZERO	ONE			(20)	(21)	240
PBSAVE	-					242
GRMX3	-	344		(22)	(23)	244
GRMX4	-	346				246
C377				(24)	(25)	250
						252
				(26)	(27)	254
						256
	-			(30)	(31)	260
	-					262
				(32)	(33)	264
	PCBB					266
	_			(34)	(35)	270
	-					272 .
	-			(36)	(37)	274
RSAVPTR	-	376	77			276
	TRØ TRI TR2 TR3 TR4 TR5 TR6 TR7 (PB) GRMX1 GRMX2 RSGT1 RSGT2 RECC1 RECC2 ZERO PBSAVE GRMX3 GRMX4 C377 PSWPB PSWKEYS PLA: PPA	TRØ	HIGH         LCW         ADDR           TRØ         -         300           TR1         -         304           TR2         -         306           TR4         -         310           TR5         -         312           TR6         -         314           TR7 (PB)         -         316           GRMX1         -         320           GRMX2         RATMPL         324           RSGT1         -         330           RECC1         -         334           RECC2         -         334           ZERO         ONE         340           PBSAVE         -         342           GRMX3         -         344           GRMX4         -         350           CS77         350         352           ABSAVE         -         354           GRMX4         -         354           GRMX5         -         356           PSWPB         -         360           PSWPB         -         360           PSWREYS         -         362           PLA: PPA         PCBA         364	HIGH         LCW         ADDR         ADDR           TRØ         -         300         40           TR1         -         302         41           TR2         -         304         42           TR3         -         306         43           TR4         -         310         44           TR5         -         312         45           TR6         -         314         46           TR7 (PB)         -         316         47           GRMX1         -         320         50           GRMX2         -         322         51           RSGT1         -         324         52           RSGT2         -         330         54           RECC1         -         332         55           RECC2         -         334         56           RECY         -         340         60           PBSAVE         -         342         61           GRMX3         -         344         62           GRMX4         -         346         63           C377         350         64           GPSWPB <td>HIGH         LCW         ADDR         ADDR         HIGH           TRØ         -         300         40         -           TR1         -         302         41         -           TR2         -         304         42         -           TR3         -         306         43         -           TR4         -         310         44         -           TR5         -         312         45         -           TR6         -         314         46         -           TR7(PB)         -         316         47         -           GRMX1         -         320         50         -           GRMX2         -         322         51         -           RSGT1         -         326         53         -           RSGT2         -         334         56         -           RECC1         -         332         55         -           RECC2         -         340         60         (20)           PBSAVE         -         344         62         (22)           GRMX3         -         34         62         (22</td> <td>HIGH         LOW         ADDR         ADDR         HIGH         LOW           TRØ         -         300         40</td>	HIGH         LCW         ADDR         ADDR         HIGH           TRØ         -         300         40         -           TR1         -         302         41         -           TR2         -         304         42         -           TR3         -         306         43         -           TR4         -         310         44         -           TR5         -         312         45         -           TR6         -         314         46         -           TR7(PB)         -         316         47         -           GRMX1         -         320         50         -           GRMX2         -         322         51         -           RSGT1         -         326         53         -           RSGT2         -         334         56         -           RECC1         -         332         55         -           RECC2         -         340         60         (20)           PBSAVE         -         344         62         (22)           GRMX3         -         34         62         (22	HIGH         LOW         ADDR         ADDR         HIGH         LOW           TRØ         -         300         40

# () indicate P300 address mapping

TR7 PB at machine halt
PSWPB PB at last interrupt
PSWKEYS Keys at last interrupt
PPA Current level/current PCB
PPB Next level/next PCB
RSAVPTR Reg save area ptr. Ø => regs saved.

See Micro Code Handbook for additional information.

CRS			RF2	CRASH	RF3	CRASH
ADDR	HIGH	LOW	ADDR	ADDR2	ADDR	ADDR3
Ø	GRØ:OLTL	_	100	Ø	140	100
1	GRl:PTS	-	101	2	141	102
2	GR2(1,A,LH)	-(2,B,LL)	102	4	142	104
3	GR3 (EH)	- (EL)	103	6	143	106
4	GR4	_	104	10	144	11Ø
5	GR5(3,S,Y)	-	105	12	145	112
6	GR6	_	106	14	146	114
7	$GR7(\emptyset,X)$	-	107	16	147	116
10	FAR1 (13)		110	20	150	120
11	FLRl	-	111	22	151	122
12	FAR2 (4)	<del>-</del> (5)	112	24	152	124
13	FLR2(6)	-	113	26	153	126
14	PB	- (Ø=>CRS) *		3Ø	154	130
15	SB (14)	<del>-</del> (15)	115	32	155	132
16	LB(16)	<b>-</b> (17)	116	34	156	134
17	XB	-	117	36	157	136
20	DTAR3 (10)	-	120	40	160	140
21	DTAR2	_	121	42	161	142
22	DTARl	-	122	44	162	144
23	DTARØ	-	123	46	163	146
24	KEYS	MODALS	124	5Ø	164	150
25	OWNER		125	52	165	152
26	FCODE (11)	-	126	54	166	154
27	FADDR	-(12)	127	56	167	156
3Ø	TIMER	-	13Ø	6Ø	17Ø	160
31			131	62	171	162
32			132	64	172	164
33			133	66	173	166
34			134	70	174	17Ø
35			135	72	175	172
36			136	74	176	174
37			137	76	177	176
				-		

<sup>\*</sup> Current PBL at halt in TR7L -- R7.

#### SEMAPHORES

- +0 # WAITS # NOTIFIES, e.g., <0 => notifies outstanding, >0 => processes waiting, =0 => wait list empty.
- +1 Pointer to first waiting PCB on queue.

#### STACK FRAME, STACK ROOT

```
Ø | Ø=>PCL l=>CALF|
 1 | SN of STK ROOT
       PB FOR
 3
       RETURN
 4 | CALLER'S SB
 6 | CALLER'S LB
7 1
10 | CALLER'S KEYS
         PBCL
11
    FCODE if CALF
                     <--- START OF AUTOMATIC
13 | FADDR if CALF
                          STORAGE IF PCL (*)
14 |
```

\*: First Argument Ptr pointed to by ECB+4.

#### STACK ROOT HEADER (WORD Ø OF SEGMENT)

- 0,1 FREE POINTER (SN/WN)
- 2,3 FIRST EXTENSION (SN/WN)

#### STACK EXTENSION HEADER (WORD Ø OF SEGMENT)

- 0.1 0/0
- 2,3 NEXT EXTENSION (SN/WN)

#### WAIT LIST

SEMAPHORE+0 = COUNT OF WAITING PCBS

SEMAPHORE+1 --> FIRST PCB

PCB+1(LINK) --> NEXT PCB ON WAIT LIST

LAST PCB+1 --> Ø

PCB+2,3 --> SEMAPHORE PROCESS IS WAITING ON

Note: wait list ordered by increasing ready list level, i.e., highest priority processes first in list.

#### 3 COMMANDS

```
ADDISK -- ADD DISKS TO SYSTEM
   ADdisk <dvno> ...
   System user only.
   Internal command.
AMLC -- SET AMLC LINE CHARACTERISTICS
   AMlc [ Tty ] line> [<config>] [<lword>]
        [ TRan ]
        [TTYHs ]
        [TRANHS]
        [TTYNop]
   If 2<USER<TRMUSR, <li>1ine>=USER-2.
   <config>:
     2033 - 100 BAUD
     2213 - 300 BAUD
     2313 - 1200 BAUD
     2413 - 9600 BAUD
   <lword>:
     bit
                  Meaning when on
     1 100000
                  Half duplex
      2 040000
                  No LF after CR
      3 020000
                  XOFF/XON Recognition
      4 010000
                  XOFF Received
    5-8 007400
                  Reserved
   9-16 000377
                  User number
   System user only.
   Internal command.
ASRCWD -- SET VIRTUAL ASR CONTROL WORD
   ASRcwd [ 0 ] Terminal (Port 1)
          [ 2 ] Centronics Printer #2 (Port 3)
          [ 4 ] Centronics Printer #1 (Port 2)
```

Internal command.

```
ASSIGN -- ASSIGN PERIPHERAL DEVICE
```

```
ASsign AMLC [<protocol>] line> [<config>] [<lword>]
          Cenpr [-WAIT]
          CE2pr [-WAIT]
          CArdr [-WAIT]
          Ptr
                 [-WAIT]
          PUnch [-WAIT]
          PRn
                 [TIAW-]
                            (where n=0,1)
          CRl
                 [-WAIT]
          MTn
                 [-WAIT]
                             (where n=0,7)
          SMLCnn [-WAIT]
                            (where nn=00,03)
                 [TIAW-]
          PLot
          Disk
                 <dvno> [-WAIT]
          SMLC
                 [-WAIT] <line>
   <config> and <lword> described under AMLC on previous
   page.
   cprotocol>:
      Tty
      TTYHS
      TRan
      TRANHS
      TTYNop
  Internal command.
ATTACH -- ATTACH TO UFD
  Attach [<ufdname>] [<passwd>] [<dvno>] [<option>]
                                  [100000]
                                          [ 2
                                  [177777] [ \bar{1}
                                           [ 177777 ]
  <dvno>:
  100000 => search all started devices,
  177777 => search MFD of current device
   <option>:
```

2 => set home UFD after attach to subUFD 1 => dont set home UFD after attach to subUFD 177777 => attach to UFD and dont set home

Internal command.

#### AVAIL -- TYPE DISK USAGE STATISTICS

AVAIL [<packname>]

[<currdisk>]

[ \* ] (All Started Disks)

[ ONE ] (Logical disk #1)

...

[SEVENTEEN ]

External command.

#### BASIC

BASIC [<filename>]

See BASIC Interpretive Language User Guide, MAN1813.

External command.

#### BASICV -- VIRTUAL MEMORY BASIC

BASICV [<filename>]

See the BASIC/VM Guide.

External command.

#### BASINP -- READ PAPER TAPE

BASINP <filename>

External command.

#### BINARY -- OPEN FILE UNIT 3 FOR BINARY OUTPUT

Binary <filename>

Internal command.

#### CHAP -- CHANGE USER PRIORITY

CHap - $\langle usrno \rangle$  [ $\langle level \rangle$ ] [ $\langle timeslice \rangle$ ] ALL [  $\frac{1}{2}$  ] [ 3 ]

<timeslice> is in tenths-of-a-second. Defaults taken
only for ALL option, else unchanged.

System user only. Internal command.

#### CLOSE -- CLOSE FILE UNIT(S)

Close <funit>... <filename>

Unit '21 (COMOUTPUT) must be closed explicitly.

Internal command.

#### CMPF -- COMPARE ASCII FILES

CMPF <treel> <tree2> [... <tree5>] [<option>...]
Options can be:
 -MINL [<n>] (default = 3)
 -BRief
 -REPORT <report-file-name>
External command.

#### CMPRES -- COMPRESS SOURCE FILE

External command.

#### CNAME -- CHANGE NAME OF FILE

CName <oldfilename> <newfilename>

Internal command.

#### CNVTMA -- CONVERT LOAD MAP FOR PMA

CNVTMA <in-file> <out-file>

Converts load map into format usable by PSD 'LS' command.

#### COBOL

```
COBOL <treename> [<option>...]
     or
COBOL [<option>...] -I <treename> [...<option>]
Options can be:
  -Binary
     Define binary file/device.
  -B <treename>
     Create binary file with specified treename.
     Do not create a binary file.
  -B YES
     Create binary file in current UFD.
  -EXPlist
     Generate an expanded listing file.
     Define input file/device.
  -I <treename>
     <treename> is source program.
  -Listing
     Define listing file.
  -L <treename>
     <treename> is listing file.
  -L NO
     Do not create a listing file.
  -L YES
     Create listing file in current UFD.
  -L TTY
     Print listing at terminal.
  -L SPOOL
     Spool listing file to line printer.
  -NOEXPLIST
     Do not generate expanded listing file.
     Generate relative-addressed code.
  -64V
     Generate segmented-addresssed code.
External command.
```

```
COMINP -- START COMMAND INPUT FILE
```

```
COminp <filename> [<ufdname>] [<funit>]
       [-]PAUSE
                             [ 6 ]
      [-]CONTIN
      [-]TTY
      -Start
      -End
'-S' = 'S' + 'CO CONTIN'. '-E' = 'TTY'.
```

COMOUTPUT -- CONTROL ROUTING OF TERMINAL OUTPUT

COMOutput [<filenamel>] <option>... Options can be: -Ntty -Contin -Tty -Pause -End

Internal command.

Internal command.

#### X.CONCAT -- CONCATENATE FILES

CONCAT [<outtreename>]

No name => unit 2 assumed open.

Follow with list of files to be cancatenated together. '=' starts line to be used as header. Null line terminates list (unit 2 left open if open on entry).

# COPY -- COPY DISK COPY Prompts: FROM PHYSICAL DISK= (Enter DVNO-see Disk Addresses.) 1.5M WORD PACK? (Enter Yes or No.) TO PHYSICAL DISK= (Enter DVNO) 1.5M WORD PACK? (Answer as to FROM...) FROM, TO, RECORDS = <from-dvno> <to-dvno> <rec-in-dec> PARAMETERS OK? (Yes or No. No => reenter all.) External command. CPMPC -- PUNCH FILE ON CARD PUNCH CPMPC <treename> [<option>...] Options can be: -PRINT -CRØ -CR1 External command. CReate <ufdname> Internal command. CRMPC <treename> [<option>...] Options can be: -PRINT -CRØ -CR1 External command.

```
CREATE -- CREATE SUBUFD IN CURRENT UFD
CRMPC -- READ CARDS
CRSER -- READ FROM SERIAL CARD READER
   CRSER <treename>
   External command.
```

```
CX -- SEQUENTIAL JOB MONITOR
   CX [<filename>] [<option>...]
   Options can be:
     -A
        list entire audit file.
        drop job numbered xx.
     -ON <dvno>
        submit CX job on <dvno> specified.
        list personal jobs in queue and audit file.
        list queue.
     -Sxx
        list status of job numbered xx.
   External command.
DATE -- PRINT DATE AND TIME
   DATE
   External command.
DELAY - SET TERMINAL DELAY CHARACTERISTICS
   DELAY [<min>] [<max>] [<width>]
         [6][12][72]
   Can issue prior to login.
   Internal command.
DELETE -- DELETE FILE
   DELETE <filename>
   Internal command.
DELSEG -- DELETE SEGMENT(S)
   DELSeg <segno>
           ALL
   segno > '2000 and not '6000
```

Internal command.

# DISKS -- SPECIFY ASSIGNABLE DISKS DIsks [NOT] <dvno> ... System user only. Internal command. ED -- EDITOR ED [<filename>] (no <filename> => new file) (<str> - text string) (/ = unique delimiter not in string) SUBCOMMANDS .CR. = INPUT TTY Append <str> Append to current line. Bottom Go to bottom of file. BRief Don't display changes. Change/ $\langle strl \rangle / \langle str2 \rangle [/] [\langle n \rangle] [G]$ Change <strl> to <str2> for first occurrence on line, for all occurrences if G present, for $\langle n \rangle$ lines if <n> present. Delete [<n>] Delete <n> (1) lines. Delete TO <str> Delete to line containing <str>. DUnload <frame> [<n>] Unload/delete <n> (1) lines. DUload <fname> TO <str> Unload/delete up to (not incl) line containing <str> to <fname>. Erase <char> Make <char> the erase character ("). FILe [<fname>] Write updated file to <fname>. Find <str> Find line starting with <str>. Gmodify <subcmnds> Modify line w/subcommands: A/<str>/ - Append B<n> - Back <n> chars C<c> - Copy up to (not inc) char <c> D<c> - Delete up to char <c> E<n> - Delete next <n> chars F - Copy to end of line I/<str>/ - Insert <str> at curr pos. M<n> - Copy <n> chars Nxx - Negate criteria of cmnd xx O/<str>/ - Overlay at current position R/<str>/ - Retype at current position S - Reset to start of line

```
Insert <str>
   Insert line (.NULL. => input mode).
INPUT [ASR] [PTR] [TTY]
   Input text from specified device.
Kill <char>
   Make <char> new kill character.
LINesz <n>
   Set max line size to <n> chars.
LOAd <fname>
   Insert contents of <fname>.
Locate <str>
   Locate line containing <str>.
MODE <arg>
   Set editor mode. <arg> can be:
        PRUPPER, PRALL, PRLOWER,
        PROMPT, NPROMPT,
        COUNT, NCOUNT,
        NUMBER, NNUMBER,
        COLUMN, NCOLUMN.
Modify \langle strl \rangle / \langle str2 \rangle [/] [G] [\langle n \rangle]
   Copy <str2> on top of <strl> starting with first
   char.
MOVe <bufl> <buf2>
   Move contents of <buf2> to <buf1>.
MOVe <bufl> <str>
   Move contents of <str> to <bufl>. Buffers are
   EDLIN (command line), INLIN (current line to be
   editted), STR.1, ..., STR.10.
Next [<n>]
   Advance <n> (1) lines.
NFind <str>
   Find line not starting with <str>.
OUTput [DISPLAY] [TTY]
  Send verification output to specified device.
Overlay <str>
  Overlay line with <str>. Blank leaves current
   char, WILD becomes blank.
PAuse
   Back to PRIMOS, restart w/'S'.
POint <n>
  Go to line <n>.
Print (<n>)
   Print <n> (1) lines
PSymbol
   Print symbols.
PUnch [<n>] [ASR] [PTP]
   Punch n lines on indicated device.
```

```
Ouit
  Exit without filing.
Retype <str>
  Replace line with <str>.
Symbol <name> <char>
  Define <name> symbol.
                            <name>:
                                        BLANK (#),
  CPROMPT (\S), COUNTER (@), DPROMPT (\&), ERASE
   ("), ESCAPE (\hat{}), KILL (\hat{}), SEMICO (\hat{}), TAB (\hat{}),
  WILD (!).
TAbset <tabl>...
  Set tab positions.
Top
  Go to top of file.
Unload <fname> [<n>]
  Unload <n> line into <fname>.
Unload <fname> TO <str>
  Unload lines up to (but not incl) <str> to
  <fname>.
Verify
  Display all changed lines.
  Print current line number.
Xeq <buff>
  Execute contents of buffer.
*[<n>]
  Repeat <n> (until bottom or forever) times.
```

External command.

```
EDB -- BINARY EDITOR
  EDB <intreename> [<outtreename>]
          PTR
                         PTR
  SUBCOMMANDS
    BRIEF
       No names printed.
    Copy <name>
       Copies up to (but not incl) <name>.
    Copy ALL
       Copies to end of file.
    Find <name>
       Position to <name>.
    Find ALL
       Position to end of file.
    Insert <treename>
       Insert <treename>.
    Newinf <name>
       Open new input file.
    OPEN < name>
       Open output file.
    Replac
       Replace <fname> with <treename>.
       Reset Force Load flag.
    SFL
       Set Force Load flag.
    TERSE
       Print 1st name in blocks.
       Top of input file.
    VERIFY
       Print all names.
  To replace <name>:
  EDB <oldlib> <newlib>
  R <name> <treename>
  C ALL
  ET
```

QUIT

#### ELIGTS -- SET ELIGIBILITY TIMESLICE

ELIGTS <tenths>  $\underline{3}$ 

Internal command.

#### EXPAND -- EXPAND (COMPRESSED) SOURCE FILE

EXPAND <intreename> <outreename>

External command.

#### FILMEM -- ZERO MEMORY

FILMEM [ALL]

No 'ALL' => '100 - '77777 excluding DOS zeroed.

External command.

#### FILVER -- COMPARE BINARY FILES

FILVER [<treenamel>] [<treename2>]

Prompts if no names entered.

External command.

#### FIXRAT -- FIX RECORD AVAILABILITY TABLE

FIXRAT [<option>...]

Prompts:

FIX DISK? (Enter Yes or No.)
PHYSICAL DISK DRIVE= (Enter DVNO-see Disk Addresses.)

If 'OPTIONS' specified:

TYPE DIRECTORIES TO LEVEL (Enter level.)

TYPE FILENAMES (Enter Yes or No.)

TYPE FILE CHAINS (Enter Yes for disk addrs.)

External command.

#### FTN -- FORTRAN

Option	Function
-64v -64R - <u>32r</u> -Big -NOBig	Generate 64V mode object code Generate 64R mode object code Generate 32R mode object code Dummy arrays may cross segment boundaries
-DClvar -NODclvar	Flag undeclared variables
-Debase -DYnm -ERRlist	allocate locxal storage in stack frame Generate errors-only listing
-ERRTty -NOErrtty	
-Explist - <u>Fp</u> -NOFp	Generate expanded listing Generate floating-pt skip instr
-Intl - <u>INTS</u>	
-SAve -Spo	Allocate local storage in linkage frame System programmer option
-Trace -Notrace	Generate code for trace output
-XREFL -Xrefs -NOXref	Generate cross reference listing Generate abbreviated cross ref listing

A-REG	<u> </u>	ON-OPTION	OFF-OPTION
1	100000	Spo	
2	040000	Explist	ERlist
2 3	020000	ERlist	Explist
4	010000	Trace	Notrace
5	004000	64r	32R,64V,DYnm,Big
6	002000	Debase	
7	001000	ERRTty	NOErrtty
8-10	000700	Input device (see b	elow)
11-13	000070	Explist, Erlist, Xref	s,XREFL (listing)
14-16	000007	Binary device	
B-REC			
1	100000	(Debug triad dump)	
1 2 3	040000	Unused	<del></del>
3	020000	SAve,DYnm	
4-7	017000	Unused	
8	000400	64V,DYnm,Big	64r, <u>32r</u> ,Debase
9	000200	Big	NOBig
10	000100	Intl	INTS
11	000040	Unused	
12	000020	Xrefs	XREFL, NOXref
13	000010	Xrefs,XREFL	NOXref
14	000004	Unused	
15	000002	NOFp	Fp
16	000001	Spo,DClvar	NODclvar
Devi	ce codes	for Input, Listing, E	Binary:

```
4 - Line Printer
Ø - None
1 - ASR
                  5 - Magtape
2 - PTR/PTP
                  6 - Cassette
3 - Card reader
                  7 - Disk
```

```
FUTIL -- FILE SYSTEM UTILITY
   FUTIL
   N.B.: command names must be in upper case.
   Attach <treename> ('*' => home ufd)
   CLEAN <prefix> [<level>]
   Copy <file> [<newname>] [,<file>[<newname>]] ...
   COPYSam <file> [<newname>] [,<file>[<newname>]] ...
COPYDam <file> [<newname>] [,<file>[<newname>]] ...
   CReate <ufdname> [<owner> [<non-owner>]]
   DELETE <file> [,<file>] ...
   FOrce ON or OFF
   From <treename>
                     ('*' => home ufd)
   Listf [<level>] [First] [LISTFIL] [PROtect] [Size]
          [Type] [Date] [Rwlock] [PAssw]
   LISTSave <filename> [<options as for Listf>]
   Protect <file> [<owner>] [<non-owner>]
   Quit
   Scan <file> [<options as for Listf>]
   SRwloc <file> <lockno>
   To <treename>
   TRECpy <ufd> [<newname>] [,<ufdname>[<newname>]]...
   TREDEL <ufdname> [,<ufdname>] ...
   TREPro <ufdname> [<owner> [<non-owner>]]
   TRESrw <ufdname> <lockno>
   Ufdcpy
   UFDDEL
   UFDPro [<owner> [<non-owner>]]
   UFDSrw <lockno> <level>
   <lockno>:
          use system read/write lock (SYS)
          n readers or 1 writer (W/NR)
          n readers and 1 writer (1WNR)
          n readers and n writer (NWNR)
   External command.
HPSD -- HIGH PSD
   HPSD
   SA, EA = 147760, 156552.
   Start of initial P counter = 150000.
   For internal commands, see PSD.
   External command.
```

```
INPUT -- OPEN FILE UNIT 1 FOR INPUT
   Input <filename>
   Internal command
LISTF -- LIST FILES IN CURRENT UFD
   Listf
   (Note: LISTING command with no name => LISTF.)
   Internal command.
LISTING -- OPEN FILE UNIT 2 FOR LISTING OUTPUT
   Listing <filename>
   (Note: <filename> omitted => LISTF.)
  Internal command.
LOAD
  LOAD
  ATtach [<ufd>] [<password>] [<ldisk>] [<key>]
             <key>=0=>don't set home, 1=>set home.
  AUtomatic [<n>]
                       Linkareas of length <n> around
             module. <n>=0 turns feature off.
  CHeck [<symbol>] [<parl ... par9>]
  COmmon <address>
                        Set COMMON TOP - 1
  DC [END]
  ENtire <treename>
  Error [\langle num \rangle]; \langle num \rangle = \emptyset, 1, or 2
  EXecute [<a>] [<b>] [<x>] Uses START entry
  FOrceload <treename> [<addr>] [<linkstart>]
             [nkrange>]
            Force prefix for FO, LO, LI commands.
  HArdware <definition>
            177700 Must be zero
            000040 l=>Prime 400 instruction set
            000020 Unused
            000010 1=>Double prec. fl. pt.
            000004 l=>Single prec. fl. pt.
            000002 1=>Prime 300 instruction set
            000001 1=>High speed arithmetic
  INitialize <treename> [<addr>] [<linkstart>]
            [<linklen>]
            Resets everything and loads <treename>.
  LIbrary [<treename>] [<addr>]
            [LIB>FTNLIB]
 LOad <treename> [<bareal>]...[<barea8>]
     <treename> [<bareal>]...[<barea9>]
     <treename> <symbol> [<bareal>]...[<barea9>]
```

```
MAp [<treename>] [<option>]
         [ \$F ] \langle option \rangle = \emptyset = \rangle full map, 1 = \rangle load state,
               2=>load state and link info, 3=>unresolved
               references, 4=>same as 0, 5=>system
               programmer map, 6=>sorted unresolved
               references, 7=>sorted full map, 10=>symbol
               map for PSD.
    MOde [D32R] [D64R] [D16S] [D32S] [D64V] [D32I]
   P/ Page boundary prefix for FO, LO, LI commands.
   PAuse
    PBrk [<symbol>] [<parl ... par9>]
                        <parl> [<par2 ... par9>]
   QUit
               Back to PRIMOS
   SAve \langle \text{fname} \rangle [\langle a \rangle] [\langle b \rangle] [\langle x \rangle] [\langle \text{keys} \rangle]
   SEtbase [<linkstart>] [<linklen>]
                      [end of sector] (*=>current sector)
   SS <symbol>
   SYmbol <symbol> <oldsym> [<parl ... par6>]
       <symbol> <addr> [<par2 ... par6>]
       <symbol> * [<parl ... par3>]
               parameters can contain + and - signs
   SZ [NO] or SZ YES
   VIrtualbase <linkstart> <tosector>
   XPunge \{\langle y \rangle\} \{\langle z \rangle\}
                                <y>: Ø=>all but undefined
               symbols, 1=>all but undefined and COMMON.
               \langle z \rangle: \emptyset = \rangle all defined base areas, 1 = \rangle all but
               sector \emptyset, 2=>return all.
   External command.
LOGIN -- LOGIN TO UFD
   LOGIN <ufdname> <passwd> [<dvno>] [-ON <nodename>]
   Internal command.
LOGOUT -- LOGOUT USER
```

LOgout [-<usrno>]

ALL(System user only)

<usrno> must have same login name as user unless issued by System user.

Internal command.

#### LOGPRT -- PRINT LOGREC

```
LOGPRT [<outfile>] [<option>...]
       [ LOGLST ] [ -Help ]
       [ Tty ] [ -From <mmddyy> ]
                   [ -Type Cold
                          Warm
                          Timdat
                          CHecks
                          Disk
                          DSKnam
                          Overfl
                          Shutdn
                          CHK300
                          Par300
                          Mod300
                          TYPE10-TYPE15 ]
                   [-Spool]
                   [ -Delete ]
                   [ -PURGE ]
```

Prompts for input treename, default (just .CR.) is CMDNCO>LOGREC.

External command.

#### LOOK -- MAP SEGMENT TO USER 1

```
LOOk [-<usrno>] [<segno>] [<access>] [<mapseg>] [- 1 ] [ 6000 ] [ 200 ] [ 4001 ]
```

System user only. OPRPRI 1. Internal command.

#### X.LS -- LIST FILE CHARACTERISTICS

```
LS [<filename>] [<option>]...
```

Filename can be a wild card name containing % and + signs:

- % matches any number of characters (including none) in the filename;
- + matches any one character in the filename.

Options can be:
-DIM -TYPE
-PR -RWLK

External command.

## MACHK -- TURNS ON MACHINE CHECK MODE

MACHK

COMMANDS

COMMANDS

MAGNET -- TRANSFER DATA TO AND FROM TAPE

MAGNET

PROMPTS RESPONSES OPTION:

POSITION or READ or WRITE or COPY

Depending on the option, MAGNET may prompt any of the following --

MTU # = #/7 or #/9; Ø<#<7

ABSOLUTE OR RELATIVE? A or R

FILE # = if # if A mode, #>1;

if R mode, # can be either

positive or negative

RECORD # = if A mode, #>1; if R mode,  $\#>\emptyset$ 

MT FILE # = #>Ø

< 10K bytes for PRIMOS II LOGICAL RECORD SIZE =

(DOS), IV, V or

< 2K bytes for PRIMOS III

BLOCKING FACTOR = # of line images in one

tape record

ASCII, BCD, BINARY

or EBCDIC? ASCII or

EBCDIC or BCD or BINARY

FULL OR PARTIAL RECORD

TRANSLATION? FULL or

PARTIAL

<filename> OUTPUT FILE NAME:

<filename> INPUT FILE NAME:

STARTING FILE # =

# FILES TO COPY =

External command.

MAGRST -- MAGTAPE RESTORE

MAGRST [-7TRK]

-9TRK

PROMPTS RESPONSES TAPE UNIT:

ENTER LOGICAL Ø -- tape already positioned

1 -- first logical tape TAPE NUMBER: 2 - second logical tape

etc.

READY TO RESTORE: Yes (yes)

> No (no) PA (partial)

\$I [<filename>] [<level>] (turn on indexing)

NW [<level>] (index only)

TREE NAME: <treename> (often partial) or

list of treenames, 1 per line, end with null line

#### MAGSAV -- MAGTAPE SAVE

MAGSAV [<option>...]

Options can be:

-LONG

1024 word records (default is 512).

use 7 track tape format (default is 9 track).

-INC

incremental save (only save files that have been modified since last save).

-UPDT

set dumped bit in the UFD entry (default is not to set the dumped bit).

PROMPTS TAPE UNIT:

0 -- tape already positioned ENTER LOGICAL TAPE NUMBER: 1 -- first logical tape 2 -- second logical tape

etc.

<6-character name> TAPE NAME:

MM DD YY or DATE:

.CR. for today's date (under

PRIMOS III and IV)

<an arbitrary integer> REV NO:

<filename> NAME:

\$A [<UFDname>] (attach)

\$Q (terminate tape and return

to PRIMOS)

\$R (terminate tape, rewind,

and return to PRIMOS)

\$I (<filename>) (<level>) (print index to

indicated level)

MFD (save entire disk)

\* (save current directory)

External command.

X.MAIL

MAIL (<ufdname>)

If no <ufdname> is specified, the mail command checks mail for the user issuing the command.

When sending mail to another user, end message with

External command.

MAKE -- FORMAT DISK

MAKE [OLD] [NEW]

RESPONSES

PHYSICAL DISK physical disk number

1.5 WORD PACK? yes or no

SPLIT DISK? yes or no

PAGING RECORDS (DECIMAL) number of records to be

used for paging (see paging

records tables, below)

<DISK NUMBER> <FILE RECORDS> <PAGING RECORDS>

yes or no

BADSPOTS ON DISK? yes or no

TRACK= track of badspot or

Ø to terminate

HEAD= head of badspot or

9 to terminate

prints list of badspot HEAD and TRACK numbers:

PARAMETERS OK? yes or no

VIRGIN DISK? yes or no

VERIFY DISK? yes or no

#### PAGING RECORDS TABLE

Disk	Decimal Records
Diskette	460
1.5 million word disk	32 <b>4</b> 8
3.0 million word pack	6496
30 million word disk	64960
128 K word fixed head disk (32	trk) 256
256 K word fixed head disk (64	
512 K word fixed head disk (12)	
1025 K word fixed head disk (2)	56 tk) 4096

#### RECORDS PARAMETERS FOR 30-MILLION WORD DISK

	Dev Addr 23	Dev Addr 21	
Partition	Disk Number	Disk Number	Records
2 hd (deflt)	XXØ25X	XXØØ5X	6496
2 hd (explicit	) XXØ65X	XXØ45X	6496
4 hd	XX125X	XX105X	12992
6 hđ	XX165X	XX145X	19488
8 hd	XX225X	XX2Ø5X	25984
10 hd	XX265X	XX245X	32480
12 hd	XX325X	XX3Ø5X	38976
14 hđ	XX365X	XX345X	45472
16 hd	XX425X	XX405X	51968
18 hd	XX465X	XX445X	58464
20 hd	XX525X	XX5Ø5X	6496Ø

External command.

# MAXSCH -- SET SCHEDULING CONSTANT

MAXSch <n>

System user only. Internal command.

## ! MAXUSR -- LIMIT NUMBER LOGGED-IN USERS

MAxusr [<number>]
[ 64 ]

System user only. Internal command.

#### MDL -- MEMORY DUMP/LOAD

MDL

MDL command requests parameters. Respond with the following data separated by spaces or commas and terminate with a CR or LF:

DEFINITION	
first location to be punched, must	
be $\geq$ '34.	
last location to be punched, must	
be <u>&lt;</u> '177777.	
auto-start address or $\emptyset$ .	
keys (default is 16S mode).	
bit 14:	
$\emptyset$ punch end of tape (EOT).	
1 omit EOT.	
bit 15:	
0 punch begin of tape (BOT).	
1 omit BOT.	
bit 16:	
Ø high speed punch.	
1 ASR punch	
loader address (default is sector in	
which MDL resides); if ASR, avoid	
folowing loader address:	
' 10400 thru ' 11600	
'110400 thru '111600	

Internal command.

#### MESSAGE -- SEND MESSAGE TO USER(S) OR SYSTEM

End command with .CR., enter message on next line, end message with .CR.

System user for all but  ${}^{1}M{}^{1}$  (user to operator). Internal command.

#### MRGF -- MERGE ASCII FILES

MRGF <tree1> <tree2> [...<tree5>] outtree [<opts>]

Options can be:

-MINL  $[\langle n \rangle]$  (default = 3)

-BRief

-FORCE

-REPORT <report-file-name>

External command.

#### NUMBER -- (RE)NUMBER BASIC FILE

NUMBER

External command.

#### OPEN -- OPEN FILE ON SPECIFIED UNIT

Open [<filename>] <unit> <key>

<key>: l-Read, 2-Write, 3-R/W, 4-Close, 5-Delete,
6-Exist, 7-Rewind, 10-Truncate + 0000-New SAM,
2000-New DAM, 4000-New SAM segment, 6000-New DAM
segment, 10000-New UFD. <filename> optional only for
Rewind and Truncate.

Internal command.

#### OPRPRI -- SET OPERATOR PRIORITY

OPRpri [ 1 ] [ Ø ]

System user only. Internal command.

#### PASSWD -- SET PASSWORDS ON CURRENT UFD

PASSWD [<owner-password>] [<non-owner-password>] [ blanks ] [ blanks ]

Internal command.

#### PHANTOM -- START PHANTOM USER

PHantom  $\langle \text{filename} \rangle [\langle \text{funit} \rangle]$  [  $\underline{6}$  ]

Phantom file should end with 'CO TTY' command.

Internal command.

#### PM -- PRINT USER REGISTER VECTOR

Pm

Internal command.

#### PMA -- PRIME MACRO ASSEMBLER

External command.

#### Options:

Errlist Errors-only listing EXplist Expanded listing

A-REG		ON-OPITON	OFF-OPTION
,	100000		
1	100000	Unused	
2	040000	Errlist	EXplist
3	020000	EXplist	Errlist
4-7	017000	Unused	
8-10	000700	Input device (see h	oelow)
11-13	000070	Errlist, EXplist (no	ormal listing)
14-17	000007	Binary device	

#### Device codes (Input, Listing Binary:

#### B-REG (PRIMOS IV BUILD):

11-13 000020 64-user version 000000 16-user version 16 000001 Large 16-user version

#### PMA ERROR CODES:

- C: INST IMPROPERLY TERMINATED.
- F: BAD MACRO EXPR TERMINATOR, ILLEGAL OP ON STACK PUSH/POP, FAIL PSEUDO-OP.
- G: GOTO ERROR WITHIN MACRO, END/ENDM IN 'GOTO' SKIP ARFA.
- I: GENERIC, I/O, OR SHIFT HAS TAG, TAG ON 321 FIELD INSTR, SHORT INSTR SPEC (#) IMPOSSIBLE, 64V LDX CLASS BAD TAG, 64V TAG ON BRANCH ILLEGAL, SEG EXT REF BAD INDIRECT OR INDEX, AP/IP, INDEX SPECIFIER INVALID, TAG ON 321 BRANCH.
- L: BAD LABEL, EXTERNAL VARIABLE IN LITERAL, BAD ARG IN EQU. SET, OR XSET,
- M: MULTIPLY DEFINED LABEL.
- N: 'END' WITHIN MACRO OR IF.
- O: BAD OPCODE, 64V MEMORY REFERENCE WHEN NOT IN 64V MODE, S/R MODE MEMORY REFERENCE NOT IN S/R MODE.
- P: MISMATCHED PARENTHESIS.
- Q: AP, NOT IN 64V/32I MODE, IP, NOT IN 64V/32I MODE, ENDM PSEUDO-OP NOT IN MACRO.
- R: STACK OVERFLOW, MULT DEF MACRO OR NAME EMPTY.
- S: 'LOAD' MODE, INSTR NEEDS DSECT, INDIRECT DAC IN C64R MODE.
- T: 32I MODE TAG MOD SYNTAX ERROR.
- U: UNDEF VAR IN ADR FIELD/EXP, UNDEFINED VARIABLE IN ORG/SETB.
- V: BIT FIELD OUT OF RANGE, UNRECOGNIZED OPERATOR IN EXPRESSION, FIELD ADR INST, OUT OF RANGE, I/O INSTR FIELD OUT OF RANGE, INST, SHIFT COUNT OUT OF RANGE, NO COMMA FOLLOWING FAR SPEC, 32I NO COMMA AFTER REGISTER #, 32I NO COMMA AFTER BIT #, 32I BAD DELIMITER, 32I SHIFT INSTR, BAD DELIM, BAD COUNT IN 32I SHIFT INSTR, BAD TAG MODIFIER IN 32I SHIFT, BAD DELIM AFTER REG # 321 PIO, OPEN PAREN MISSING ON DFTB ARG, CLOSE PAREN MISSING ON DFTB ARG, NO LAB IFTF, IFTT, IFVT, IFVF, NO NAME IFTF, IFTT, IFVT, IFVF, ABS/REL ILLEGAL IN SEG MODE, SEG/SEGR AFTER CODE GENED, PROC/LINK OUTSIDE OF SEG MODE, FIELD OUT OF RANGE ON DDM, BAD ARGUMENT FOLLOWING 'EXT', 'END' WITHIN MACRO, SYNTAX ERR IN 'DYNM' PSEUDO-OP, BAD ARG ON SUBR STATEMENT, VFD PSEUDO-OP, 16 BITS NOT DEFINED, UNTERMINATED CHARACTER STRING, EXPRESSION OVERFLOW ON FL PT NORMALIZE, EXPRESSION OVERFLOW ON FL RE-NORMALIZE, SCALED BINARY LOSS OF SIG, FL POINT NUMBER OUT OF RANGE, BCI REPEAT COUNT ERROR, BCI COUNT VARIABLE TYPE ERROR, MUNG IN ADDR FIELD OF CALL, BAD ADDR FIELD ON COMN, REPEAT COUNT ERROR, DEC/OCT PSEUDO OP HAS BAD OP, RLIT FOUND AFTER CODE GENED, NO LABEL ON DFTB.
- X: 32I MODE GPR SPEC ERROR.
- Y: PHASE ERROR.
- Z: ILLEGAL ABS REF IN SEG MODE, SEG MODE ABS REF NOT 0-7, AP/IP, ABSOLUTE REF INVALID, TOO MANY EXT NAMES IN EXPR, BAD EXPR MODE FOR INSTR, EXPRESSION MODE ERROR, >1 NON-ABS/REL OPERATOR, RIGHT-HAND OP NOT ABS/REL, EXTERNAL NAME NOT PERMITTED.

#### PRERR -- PRINT ERRVEC AND LAST ERROR MESSAGE

PRerr

Internal command.

#### PRMPC -- PRINT FILE ON LINE PRINTER

PRMPC <treename>

External command.

#### PROTECT -- SET PROTECTION ON FILE

PROtec [<owner-rights>] [<non-owner-rights>] [  $\underline{\emptyset}$  ] [  $\underline{\emptyset}$  ]

Ø-No access, l-Read, 2-Write, 3-R/W,
4-Delete/Truncate, 5-D/T/R, 6-D/T/W, 7-All.
Default on file creation equals 7 0.

Internal command.

#### PRSER -- PRINT FILE ON SERIAL LINE PRINTER

PRSER <treename>

External command.

#### PRVER -- PRINT FILE ON VERSATEC

PRVER <treename>

External command.

#### PSD -- PRIME SYMBOLIC DEBUGGER

PSD [<token>...]

(NOTE: VPSD <u>has</u>: segment, base register operations, does not have: symbols, trace.)

TERM	INATORS for 'A'	MODES	•
.CR.	*+1	:A	ASCII
,	*+1	:B	BINARY
^	*-l (uparrow)	:H	HEXIDECIMAL
.n	*+n	:0	OCTAL
n	*-n	:S	SYMBOLIC
<b>@</b>	Effective address	:D	DECIMAL
\	Back to last @	:P	AP
(	To contents of *	:L	LONG OCTAL INTEGER

COMMANDS

No. 1 to Just 3. Short	M7m
) Back to last defined (	MAp
= EA + contents, no update of *	Print load map symbols.
/ Return, do not close *	MO [D16S] [D32R] [D64R] [D64V] [D32S] [D32I]
? return, do not close *	Set address mode.
! Return, close *	Monitor [ <start>] [<a>] [<b>] <addr></addr></b></a></start>
	Trace obj prog for mem ref instr.
Expressions: Locations can be expressions including:	Not-equal <from> <to> <nmatch> [<mask>]</mask></nmatch></to></from>
-	Negative serarch.
* (current location)	Open <fname> <unit> <key></key></unit></fname>
[+]number-in-current-mode	Open unit.
>number-relative to relocation constant	PATCH <locl> <loc2></loc2></locl>
	Patch instr in <loc2> into <loc1>.</loc1></loc2>
	Print
SUBCOMMANDS	Print brkpt, contents, a, b, x,
SUBCOMMAND	keys, relocation.
3,000 (100)	PRoceed [ <newbrk>] [<a>] [<b>] [<x>] [<k>]</k></x></b></a></newbrk>
Access <loc></loc>	
Access location.	Set new brkpt and resume execution.
Breakpoint <loc></loc>	Quit
Set breakpoint (up to 10).	Quit.
BR	RElocate <reloc-val></reloc-val>
Print base registers.	Set relocation constant.
Copy <from> <to> <new-addr></new-addr></to></from>	Run_[ <loc>] [<a>][<b>][<x>][<keys>]</keys></x></b></a></loc>
Copy block of memory to new location.	Run program.
Define <sym> <val></val></sym>	SB <sn> <wn></wn></sn>
Define symmbol.	Set stack base.
Dump <from> <to> [<ncol>] [<mode>]</mode></ncol></to></from>	Search <from> <to> <match> [<mask>]</mask></match></to></from>
Dump contents of memory.	Search memory block.
Effective <from> <to> <match> [<mask>]</mask></match></to></from>	SN <sn></sn>
Search for effective address.	Set segment number.
Execute <addr> [<a>] [<b>] [<x>]</x></b></a></addr>	SY Ø
	Symbol mode off.
Search for effective address.	SY 1
EXecute	Symbol mode on.
Execute segmented program.	Trace [ <addr>] [<a>] [<b>] [<val>] [-1 <interval>]</interval></val></b></a></addr>
FAddress <fld-addr-reg-no></fld-addr-reg-no>	
Access field address register.	Trace program. Update <loc> <val></val></loc>
FLength <fld-len-reg-no></fld-len-reg-no>	•
Access field address register.	Update location.
Fill <from> <to> <val></val></to></from>	Verify <from> <to> <copy-addr></copy-addr></to></from>
Fill memory block with <val>.</val>	Verify block of memory.
GO [ <count>] [<a>] [<b>] [<x>]</x></b></a></count>	VErsion
Continue at breakpoint.	Print version, restart address.
Jumptrace [ <start>] [<a>] [<b>]</b></a></start>	Where
Execute obj prog and produce diagnostic listing.	Display brkpts and proceed counts.
Keys <value></value>	X <reloc-val></reloc-val>
Set keys to value.	Set relocation constant.
List <loc></loc>	XB <sn> <wn></wn></sn>
list location.	Set X base.
LB <sn> <wn></wn></sn>	XR <val></val>
Set link base.	Set X register.
IS	YR <val></val>
Load symbols (unit 1).	Set Y register.
THE DAMPOTO (MITT I).	Zero [ brk-loc>]
	Remove brkpt (current)
	remove prope (current)

#### PTBOOT -- PAPER TAPE BOOT

PTBOOT

External command.

#### PTCPY -- PAPER TAPE COPY

PTCPY

External command.

#### PUSS -- COMPARE SOURCE FILES

**PUSS** 

PROMPTS RESPONSES

DIFF FILE, OMISSIONS? name of difference file

followed by a space plus YES, NO, or carriage return

OLD-FILE TREE-NAME:

<treename>

NEW-FILE TREE-NAME:

<treename>

Obsolete — use CMPF. External command.

#### RESTORE -- RESTORE EXTERNAL PROGRAM

RESTore <filename> [<sa>] [<ea>] [] [<a>] [<b>] [<x>] [<k>]

Internal command.

#### RESUME -- RUN EXTERNAL PROGRAM

REsume  $\langle filename \rangle [\langle token \rangle...] [\langle sa \rangle] [\langle ea \rangle] [\langle p \rangle] [\langle a \rangle] [\langle b \rangle] [\langle x \rangle] [\langle k \rangle]$ 

Internal command.

#### RPG -- REPORT PROGRAM GENERATOR

RPG <filename> [<option>...]

Options can be:

-SEQCHK, -NOSEQCHK

-BANNER, -NOBANNER

-OBDATA, -NOOBDATA

-STATUS, -NOSTATUS

-ERRTTY, -NOERRTTY

-LISTING

-BINARY

#### Error Message Format:

\*\*\*\*ERROR, LINE (xxxx) COL yy-zz [contents]-message.
\*\*WARNING, LINE (xxxx) COL yy-zz [contents]-message.

#### RUNOFF -- TEXT FORMATTER

RUNOFF [<filename>]

#### Notes:

- When imbedded in text, all runoff command lines begin with a period; when issued at command level, runoff commands do not begin with a period.
- 2) In the table below, some runoff command actions are followed by brk, ejt, and/or deflt to indicate the command causes a break, ejects a page, and/or is the default. Also, if the runoff command has a default value, that value is specified.

(<str> = text string)

#### SUBCOMMANDS

.NULL.

Start processing (from command mode).

\* <str>

Comment line.

+ <str>

Enter verbatim string.

/-/-/-/

/Left/Center/Right/ strings.

> <str>

Center string.

Adjust

Enter adjust/fill modes (brk, deflt).

BLank <char>

Define blank substitute character (.NULL.).

BMargin <n>

Set bottom margin (brk, ejt, 5).

Break

Break (start new line).

CMargin <n>

Set column margin (brk, ejt, 5).

Column

Set number of columns (brk, ejt, 1).

D (str

Down Decimal level.

DDS <str>

Down decimal level, no decimal number.

Define <sym> <str>

Define symbol value.

DI <lev> <before> <after>

Set decimal indents.  $\emptyset \Rightarrow$  all levels

DLevel <n>

Go to decimal level <n> (1).

DLT <n

Set highest decimal level to appear in Table of Contents (all).

DN <str>

Next heading on current decimal level.

DNS <str>

Next heading on current decimal level, suppress

DR <n>

Reset number on decimal level <n>.

DS <lev> <before> <after>

Set decimal heading skip values.

Ø => all; -l => eject before

DU [<n>

Go up <n> decimal levels (1).

EFooter /-/-/

Define even-page footer.

EHeader /-/-/-/

Define even-page header.

Eiect

Page eject (brk, ejt).

ERase <char>

Define cmnd mode erase char.

ERRgo

Continue on error.

FILE <fn>

Specify output file.

Fill

Enter fill mode.

FLoat <fn>

Floating insert of <fn>.

FOoter /-/-/-/

Define footer for all pages.

FRom <n>

First page number to output.

Header /-/-/-/

Define header for all pages.

HYphen <char>

Define phantom hyphen char (.RUBOUT.).

Indent <n>

Indent left margin (5).

INDEX <str>

Write <str> and page number to index.

INS <fn> [(<parms>]

Insert <fn>.

INS <unit>

Insert from <unit>.

IXfile <fn>

Define index file (16).

Kill <char>

Define command line kill char (?).

Length

Specify physical page length (brk, ejt, 66).

NAdiust

Leave adjust mode (brk).

Need <n>

Eject if < <n> lines (1).

NERrgo

Stop on error encountered (deflt).

```
NFill
   Leave fill and adjust modes (brk).
NFILE
   No output to file.
NIXfile
   Stop output to index file.
NParagraph
  No paragraph indentation (deflt).
NPAUse
   No pause between pages (deflt).
NPErforate
   No perforation marks (deflt).
NIty
   No output to TTY (deflt).
OFooter /-/-/-/
   Define odd-page footer.
OHeader /-/-/-/
   Define odd-page header.
PAGen <n>
   Set page number (1).
Paragraph [<n>] [<m>]
   Start paragraph, skip <n>, indent <m>.
PAUse
   Pause between output pages.
PErforate
   Print perforation marks.
PIcture <n>
   Leave <n> lines together (1).
PUrge
   Force in outstanding floats.
Quit
   Exit RUNOFF (brk, ejt).
RBar [ON]
   Start revision bars.
RBar [OFF]
   Stop revision bars.
REturn <n>
   Return to prev input file (\emptyset).
Rindent <n>
   Indent right margin (5).
RUndent <n>
   Undent right margin (0).
Skip <n>
   Skip <n> lines (brk, 1).
SM <n>
   Specify side margins (brk, ejt, 7).
SO <n>
   Print <n>th source line # (1).
Space <n>
   Specify single/double, etc. spacing (1).
   Conditional .QUIT/.RETURN.
SYchar <char>
   Define symbol delimiter (%).
```

Tab <char> <nl> ... Set tab character and stops. TMargin <n> Specify top margin (brk, ejt, 7). TO <n> Specify last page to print (32767). TOFc <fn> <lim> Specify table of contents file. TOFc [<opt>] Close, stop, start table of contents for  $\langle opt \rangle = omitted, \emptyset, 1.$ TTOfc <str> Enter string in table of contents. TTy Output to TTY. UNDEFine <sym> Undefine symbol. Undent <n> Undent left margin. WIDOw <n> Specify allowable widow size  $(\emptyset)$ . Width <n> Specify paper width (brk, ejt, 85).

External command.

#### SAVE -- SAVE MEMORY IMAGE

SAve  $\langle \text{filename} \rangle [\langle \text{sa} \rangle] [\langle \text{ea} \rangle] [\langle \text{p} \rangle] [\langle \text{a} \rangle] [\langle \text{b} \rangle]$  $[\langle x \rangle]$   $[\langle k \rangle]$ 

Do not use SAve with 64V segmented files.

Internal command.

# X.SAVER -- COMPARE RUNFILES

SAVER [<treenamel>] [<treename2>]

Prompts if no names entered.

#### SEG -- SEGMENTED LOADER

SEG [<filename>]

#### SUBCOMMANDS

DELETE [<filename>]
deletes runfile.

HElp

print list of SEG commands.

LOad [<treename>]

define runfile and invoke loader for creation.

LOad \* [<treename>]

define runfile and invoke loader for appending.

ATtach [<UFDname>] [<password>] [<ldisk>] [<key>] attach to UFD.

A/SYmbol <sname> [<segtype>] <segno> <size> define a symbol in memory and reserve space

for it using absolute segment numbers.

COmmon [ABS] <seqno>

relocate COMMON using absolute segment

numbers.

COmmon REL <segno>

relocate COMMON using relative segment

assignment.

D/IL (D = Ditto = use parms of previous cmnd.)

D/LCad

D/LIbrary

D/FOrceload

D/PL or D/RL

load using previous parameters. D/ and F/ may be combined.

EXecute

save load to disk and execute program.

F/xx [<filename>] [<addr psegno lsegno>]

forceload all routines in object file.

IL (addr psegno lsegno)

load impure FORTRAN library.

INitialize [<treename>]

initialize and restart loader.

LIbrary [<treename>] [addr psegno lsegno]

load library file.

LO [<treename>] [addr psegno lsegno]

load object file.

MAp [<filename>] <option>

generate load map.

MIXUP [ON] or MIXUP OFF

mixes procedure and static data.

MV

moves portion of loaded file. Will prompt for

info.

Operator

relax/impose high-level restrictions

PL [addr psegno lsegno]

load pure FORTRAN library.

P/xx [<filename>] [<option>] [<psegno

lsegno>]

load on a page boundary.

OUi

return to PRIMOS command level.

REturn

return to SEG command level.

RL <treename> [<addr psegno lsegno>]

reload a routine.

R/SYmbol <sname> [<segtype>] <segno> <size>

define symbol in memory and reserve space for relative segment assignment.

SAve [a] [b] [x]

save load to disk.

SE <segno> <len>

create base area for desectorization.

Split <segno><addr>

<addr>

<addr><ssegno><saddr> <esegno>

break data into data and procedure portions

SS <sname>

save symbol.

STack <size>

change stack size.

SYmbol [<sname>] <segno> <addr>

define a symbol at specific location in

S/xx [<filename>] <addr> <psegno> <lsegno>

load a specific absolute segment.

XP <dsymbol> <dbase>

expunge symbols from symbol table and delete

base information.

MAp <filenamel> [<filename2>] <option>

ascending addr, ll=sym>

MOdify [<filename>] or SA [<filename>] invoke modification subprocessor. NEw <filename> write new copy of runfile to disk. modify save range of existing segment. REturn return to SEG command level. SK <ssize> or SK <seqno> <addr> alter stack size and/or location. STart <seqno> <addr> change program execution start address. WRite write all segments to disk. PArams [<filename>] display parameters of runfile. **PSd** invoke VPSD debugging utility. Quit return to PRIMOS command level. RESTore [<treename>] bring runfile into user memory. RESUme [<treename>] restore runfile and begin execution. SHare [<treename>] create R mode runfiles for segments below '4001. SIngle [<treename>] <segno> create R mode file image of single segment. TIme [<treename>] print time and date of last runfile modification. VERSION display SEG version number. External command. SETIME -- SET DATE AND TIME SEtime -<mmddyy> -<hhmm> Must be issued before user logins possible. System user only. Internal command. SFRWLK -- SET FILE READ/WRITE LOCK SFRWLK Prompts for filename, RWLOCK setting (0-3): 0 - use system lock, 1 - N readers or one writer, 2 - N readers and one writer, 3 - N readers and N writers.

External command.

SHARE -- SPECIFY SHARED SEGMENT SHAre [<filename>] <segno> [<access>] [ 600 Omitted <filename> => change access only. <access>: 000-no access, 200-read access, 600-read/execute access, 700-read, write, execute access. segno < '4000. System user only. OPRPRI 1 only. Internal command. SHUTDN -- SHUTDOWN DISK(S) OR SYSTEM SHutdn [<node>] [<dvno> ... ] [ ALL ] Do not shutdown logical device 0 -contains CMDNC0. System user only. Internal command. SIZE -- PRINT SIZE OF FILE SIZE <treename> External command. SLIST -- PRINT FILE TO TERMINAL SLIST [<treename>] External command. SORT SORT [ option1 ] [ option2 ] Options are: BRief, SPace, and MErge. Prompts for: input filename, output filename, number of pairs of starting and ending columns, input pairs of starting and ending columns, reverse order, and data type. If merge option: number of files to be merged followed by input files, one per line. External command.

#### SPOOL -- PRINT QUEUE MANAGER

```
SPOOL [ <treename> ] [<options>]
      [%<treename>] [-Lnum ]
                                Line Numbers
                     [ -Ftn ] FTN Forms Ctl
                     [ -Expand ] Expand
                      -Nofmt ] No Format Ctl
                      -Plot <nw> | Plot
                      -Defer [<time>]] Defer to time
                      -Home ] Local Printer
                      -FOrm <name> ] Forms type
                      -Open ] Open only
                      -FUnit <unit> ] From unit
                     [ -Tunit <unit> ] To unit (2)
         -CMD
       -CANCEL <filename> ]
       -LIST [ ALL ] ]
               OWN ]
              [DEFER]
              [ PLOT]
              [PRINT]
              [FORM <type>] (For default, say: ' ')
Prompts for defer time (-D), form type (-T) if not
entered. '-CMD' ('SYSTEM' only) allows internal
commands:
  USER <usrno-of-spooler>
           (Abort current file and restart later)
  DROP
           (Aborts and deletes)
           (Backs up 100 lines and restarts)
  BACK
  RESTART (Restart current file)
  FORM <type> (Specify new paper type)
           (Stop now)
           (Restart after HANG, FINISH)
  LENGIH <n> (Specify paper length)
           (Exit SPOOL environment)
  TIME <secs>
                 (To wait for phantom)
  CANCEL <filename>
  FINISH (Stop when current file done)
  LIST
```

External command.

#### START -- START EXECUTION

```
Start [\langle token \rangle...] [\langle p \rangle] [\langle a \rangle] [\langle b \rangle] [\langle x \rangle] [\langle k \rangle] Internal command.
```

```
STARTUP -- STARTUP DISK(S)
```

STARTUp [<nodename>] <dvno> ...

System user only. Internal command.

#### STATUS -- PRINT USER OR SYSTEM STATUS

STATus [ ALl ]
[USers]
[DIsks]
[UNits]
[ NEt ]

Internal command.

#### SVCSW -- SET USER SVC SWITCH (INTERNAL

SVcsw [ 1 ] [ <u>Ø</u> ]

1 => bounce (except class 5),  $\emptyset$  => don't bounce.

Internal command.

#### TA -- TREE ATTACH

TA <treename>

WARNING: Wipes out memory.

#### TAPXAM -- READ/WRITE MAGTAPE

#### TAPXAM

Allows examination of 7 or 9-track tapes at a single tape command level. Can be used to diagnose failing drive or controller. Prompts for drive number, 7 or 9 track indication, maximum record size to read or write. Defaults (just hit .CR.) are drive 0, 9 track, '1000 words. Displays command, status, and NW transferred after each operation. Internal commands:

BF or BT	Backspace one filemark (tapemark).
BS or BR	Backspace one record.
DS	Display Status. Interprets last
	status read in readable form.
Eol	Write MAGSAV-style End-Logical-Tape
	record. Must first read last record
	on tape (to get sequence number
	right).
FF or FT	Forward space to file mark.
FS or FR	Forward space one record.
In	Re-initialize (all prompts repeated).
Max [ <n>]</n>	Set max amt of data to display after
	reads $0$ -10 words, defaults to $0$ .
Psd	Enter PSD (to examine block just
	read). Prints addr of buffer, use 'Q'
	in PSD to return to TAPXAM.
Quit	Exit TAPXAM (tape is not rewound).
$R[C][1][\langle nw \rangle]$	Read [Binary] [1 char/word] or
	[Correct] [1 char/word].
RW	Rewind to load point.
St	Read current status.
$W[C][1] [\langle nw \rangle]$	Write [Binary] [1 char/word] or
	[Correct] [1 char/word].
WF or WT	Write file mark (tape mark).
1600BPI	Set density to 1600 bpi. (only
	applicable for software settable
	density)
6250BPI	Set density to 6250 bpi. (only
	applicable for software settable
	density)

External command.

```
TERM -- SET TERMINAL CHARACTERISTICS
```

```
TERM [ DISPLA ]
    [ ERASE <char> ]
    [ KILL <char> ]
    [ BREAK ON ]
    [ OFF ]
    [ HALF [NOLF] [XOFF] ]
```

External command.

#### TIME -- PRINT TIME STATISTICS

Time

HH'MM logged in, MM'SS CPU time, MM'SS I/O time.
Internal command.

#### TRAMLC -- AMLC I/O

TRAMLC TRANSMIT <filename> <line> [T]
TRAMLC RECEIVE <filename> <line> [T]

External command.

#### UNASSIGN -- RELEASE PERIPHERAL DEVICE

Unassign parameters-identical-to-ASSIGN-command>

Internal command.

#### UPCASE -- TRANSLATE FILE TO UPPER CASE

UPCASE <intreename> [<outreename>]
 [<file-open-on-unit\_2>]

External command.

#### USERS -- PRINT CURRENT NUMBER OF USERS

USErs

Internal command.

# USRASR -- CONNECT SYSTEM ASR TO USER

USRasr <usrno>

Must type full USRASR if not logged in.

System user only. Internal command.

#### VPSD -- VIRTUAL MODE PSD

**VPSD** 

Supports V-mode. For internal commands, see PSD.

External command.

# VRTSSW -- SET VIRTUAL SENSE SWITCHES

Internal command.

#### 4 FILE SYSTEM INTERNALS

The following describes the internal formats of all disk records for both the old and new file system partitions. All numbers are decimal unless preceded by a ':'. Where possible, field names are the same as those used by the internal file system routines.

#### DSKRAT FORMATS

#### DSKRAT Format -- Old Partitions

Ø	5	NUMBER OF WORDS IN DSKRAT HEADER = 5
1	RECSIZ	DISK RECORD SIZE (448 or 1040)
2	NMRECS	NUMBER RECORDS IN PARTITION
3	UNUSED	UNUSED
4	NHEADS	NUMBER HEADS IN PARTITION
5	DATA	START OF DKSRAT DATA (ONE BIT/RECORD)
	l l	

#### DSKRAT Format -- New Partitions

Ø 1 2	8     RECSIZ     NMRECS	NUMBER WORDS IN HEADER = 8 RECORD SIZE NUMBER RECORDS IN PARTITON (TWO WORDS)
4	NHEADS	NUMBER HEADS IN PARTITION
5	RESERVED	RESERVED
6	RESERVED	RESERVED
7	RESERVED	RESERVED
8	DATA	START OF DSKRAT DATA (ONE BIT/RECORD)

#### RECORD HEADER FORMATS

NOTE: record header formats are the same for new and old partitions. The format of a record header is a function of the physical record size.

#### Record Header Format -- 448-Word Records

Ø	REKCRA	RECORD ADDRESS (OF THIS RECORD)
1	REKBRA	RA OF DIRECTORY ENTRY OR FIRST RECORD
2	REKFPT	RA OF NEXT SEQUENTIAL RECORD
3	REKBPT	RA OF PREVIOUS RECORD
4	REKCNT	NUMBER DATA WORDS IN THIS RECORD
5	REKTYP	TYPE OF THIS FILE
6	REKLVL	INDEX LEVEL FOR NEW PART DAM FILES
7	RESERVED	RESERVED

#### Record Header Format -- 1040-Word Records

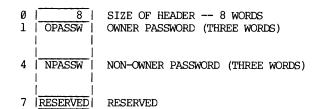
Ø	REKCRA	RECORD ADDR OF THIS RECORD (TWO WORDS)
2	REKBRA	BEGINNING RECORD ADDRESS (TWO WORDS) (BRA OF DIRECTORY IF FIRST RECORD)
4	REKCNT	NUMBER DATA WORDS IN THIS RECORD
5	REKTYP	TYPE OF THIS FILE
6	REKFPT	RA NEXT SEQUENTIAL RECORD (TWO WORDS)
	1	(Ø IF LAST)
8	REKBPT	RA OF PREVIOUS RECORD (TWO WORDS)
	1	(Ø IF FIRST)
10	REKLVL	INDEX LEVEL FOR NEW PART DAM FILES
11		
	RESERVED	RESERVED (FIVE WORDS)
	1	
15	<u> </u>	

#### Notes

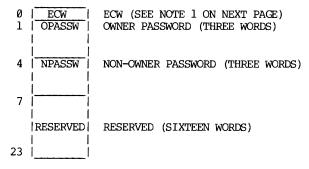
- 1) All disks except Storage Module have 448-word records. Storage Modules have 1040-word records.
- 2) The BRA of the first record in a file points to the beginning record address of the directory in which the file entry appears. In all other records, the BRA points to the first record of the file.
- 3) REKFPT contains the address of the next sequential record in the file or Ø if it is the last record in the file.
- 4) REKBPT contains the address of the previous record in sequence or Ø if it is the first record in the file.
- 5) REKTYP is valid only in the first record of a file. Legal values are:
  - SAM File
  - l DAM File
  - 2 SAM Segment Directory
  - 3 DAM Segment Directory
  - User File Directory (UFD)
- 6) If the file is the record zero bootstrap (BCOT) or the disk record availability table (DSKRAT or volume name) and the disk has a 1040 record size (Storage Module), bit 1 (:100000) of FILTYP will be set.
- 7) DAM files on new partitions are organized somewhat differently from the above -- see PE-T-276.

#### UFD HEADER AND ENTRY FORMATS

#### Old UFD Header Format



#### New UFD Header Format



#### Old UFD Entry Format

Ø 1	BRA   FILE	BEGINNING RECORD ADDRESS FILENAME (THREE WORDS)
	NAME	
4	SPACES	2 BLANKS FOR NAME EXPANSION (RESERVED)
5	PROTEC	PROTECTION (OWNER/NON-OWNER)

#### Notes

In an old UFD, the high-order eight bits of PROTEC are the owner rights stored in <u>complemented</u> form  $(\emptyset=)$ owner has right). The low-order eight bits are non-owner protection, stored in <u>true</u> form  $(\emptyset=)$ no right). On creation, PROTEC= $\emptyset$ . After a 'PROT 7  $\emptyset$ ', PROTEC=:174000.

#### New UFD Entry Format

Ø	ECW	ENTRY CONTROL WORD (TYPE/LENGTH)
1	BRA	BEGINNING RECORD ADDRESS (TWO WORDS)
	1	
3	RESERVED	RESERVED (THREE WORDS)
	! !	
_		PROPERTY (OF THE ALONE OF THE D)
6	PROTEC	PROTECTION (OWNER/NON-OWNER)
7	RESERVED	RESERVED FOR FUTURE USE
8	DATMOD	DATE LAST MODIFIED (YYYYYYMMMMDDDDD)
9	TIMMOD	TIME LAST MODIFIED (SECS-SINCE-MID./4)
10	FILTYP	FILETYPE
11	SCW	SUBENTRY CONTROL WORD FOR FILENAME
12	F	
	I	
	L	
	i Ei	
	1	FILENAME (1-16 WORDS, BLANK PADDED)
	N I	
	A	
	M	
N	E	

#### Notes

- 1) The Entry Control Word (ECW) consists of two eight-bit subfields. The top eight bits indicate the type of the following entry as follows:
  - Ø Old UFD Header
  - 1 New UFD Header
  - 2 Vacant Entry
  - 3 New UFD File Entry

The low-order eight bits give the size of the entry including the ECW itself.

- 2) The bits in PROTEC are stored in true form ( $\emptyset$ => no right) for both owner and non-owner fields.
- 3) The file type field is as before (see Old Record Header Format) with following additional bits:

#### BIT MEANING WHEN BIT IS ON

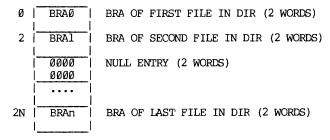
- File has 16-word hdr (DSKRAT, BOOT only).
- Special file (BOOT, DSKRAT, MFD, BADSPT).
- 4) The Subentry Control Word (SCW) consists of two eight-bit subfields. The top 8 bits are 0, indicating subentry type 0. The low-order 8 bits give the size of the subentry including the SCW itself.
- 5) N.B.: UFD entries are reused by the new file system. This means that a new entry will not necessarily appear at the end of the UFD.

### SEGMENT DIRECTORY FORMATS

### Old Segment Directory Format

Ø	BRAØ	BRA OF FIRST ENTRY IN DIRECTORY
1	BRA1	BRA OF SECOND FILE
2	0000	NULL ENTRY
N	BRAn	BRA OF LAST FILE IN DIRECTORY

#### New Segment Directory Format



#### Notes

The only difference between old and new directories is that each entry in a new directory is two words. A null entry in a new directory is a 32-bit  $\emptyset$ .

#### 5 INSTRUCTION SET

The following description applies only to P400 memory reference. The first number in the "opcode" column is the octal representation of instruction bits 3-6. The second number is the octal representation of bits 13-14 (bits 13-14 are inspected only if bits 6-11 are 11000, i.e., S=1 and -256 < D < -224).

The 'type' column indicates the format and/or function of the operation as follows.

AP:	Three-word operation, the last two words of	οf
	which are an AP address pointer.	

BR:	Two-word operation, the second word	of which
	is a word number within the current	procedure
	segment to which to branch.	

CON:	Single-word control operation
DA:	Decimal arithmetic operation.
FE:	Field and edit operation.
FLD:	Single-word field operation.

FOP: Single-word floating-point operation.
FSK: Single-word floating-point skip operation.
IG: Single-word integrity operation.
IO: Single-word input/output operation.
LOG: Single-word logicize operation.

MOD: Single-word mode operation.

MR: Memory-reference operation.

OPR: Single-word miscellaneous operation.
PIO: Programmed input/output operation.
SH: Single-word shift operation.

SKP: Single-word shift operation SKP: Single-word skip operation. VM: Virtual memory operation.

The  $^{\prime}\text{C}^{\prime}$  column indicates the effect of the operation on the C-bit and the L-bit as follows.

- -: C and L are unchanged by the operation.
- 1: C is unchanged, L is carry.
  2: C is overflow, L is carry.
- 3: C is overflow, L is indeterminant.
- 4: C is shift extension, L is indeterminant.
- 5: C is a result of op, L is indeterminant.
- 6: C and L are indeterminant.
- 7: C and L are loaded by the operation.
- 8: C is cleared, L is indeterminant.

The 'cc' column indicates the effect of the operation on the condition codes as follows.

-:	Cond.	codes	are	unchanged.
----	-------	-------	-----	------------

- 1,4: Cond. codes result of arith op or compare.
- 5: Cond. codes indeterminant.
- 6: Cond. codes loaded by operation.
- 7: Cond. codes indicate result of operation.

The 'avail' column indicates in which addressing modes the operation is available as follows.

- S: Available in 16S and 32S modes.
- R: Available in 32R and 64R modes.
- V: Available in 64V mode.
- I: Available in 32I mode.
- \*: Restricted to Ring Ø execution.

# PRIME 400 MEMORY-REFERENCE INSTRUCTIONS (WHEN IN 64V MODE).

# INSTRUCTION BITS 13-14 (if S=1 and -256<=D<-224)

(11	2-T all	u -230\-D\-	224)	
INSTRUCTION				
BITS 3-6	ØØ	Ø1	10	11
				•
0001	JMP	EAL .	XEC	-
0010	LDA	FLD	DFLD	LDL
0011	ANA	STLR	ORA	ANL
0100	STA	FST	DFST	STL
0101	ERA	LDLR	-	ERL
Ø11Ø	ADD	FAD	DFAD	$\mathtt{ADL}$
Ø111	SUB	FSB	DFSB	SBL
1000	JST	-	PCL	-
1001	CAS	FCS	DFCS	CLS
1010	IRS	MIA	EAXB	_
1011	IMA	MIB	EALB	_
1100	JSY	EIO	JSXB	-
1101*	STX	FLX	DFLX	-
1101**	LDX	LDY	STY	JSX
1110	MPY	FMP	DFMP	$\mathtt{MPL}$
1111	DIV	FDV	DFDV	DVL

Use column 00 if S (bit 7) is 0 or if D (bits 8-16) is not in the range  $-256 \le D \le -224$ .

<sup>\*:</sup> Use this row if bit 2 of instr. word is a 0.
These instructions cannot be indexed.

<sup>\*\*:</sup> Use this row if bit 2 of the instruction word is a 1. These instructions cannot be indexed.

				C		
MNEM	OPCODE	TYP	С	C	AVAI	L DESCRIPTION
A					I	ADD WORD.
AlA	141206	OPR	2	1	SRV	ADD 1 TO A REG. A+1=>A.
A2A	140304	OPR	2	1	SRV	ADD 2 TO A REGISTER. A+2=>A.
ABO	50134	OPR	_	7	I	ADD TO BOTTOM OF QUEUE.
	141716					ADD TO BOT OF Q. CCEQ => FULL
	141216					ADD CBIT TO A REG. CBIT+A=>A.
ADD	Ø6	MR	2	1	SRV	ADD. (A) + $[EA]16 \Rightarrow A$ .
ADL	Ø6 Ø3	MR	2	1	V	(A,B)+[EA]32=>A,B. (NO HOLE).
ADLL	141000	OPR	2	1	SRV	ADD LINK TO L REGISTER.
ADLR	50014	OPR	-	7	I	ADD LINK TO GR.
AH	12	MR	2	1	I	ADD HALFWD. RH+[EA16]=>RH.
ALFA	001301	FLD	6	5	SRV	ADD L TO FIELD ADDR REG. ZERO.
ALFA	001311		6	5	SRV	ADD L TO FIELD ADDR REG. ONE.
$_{ m ALL}$	Ø414XX	SH			SRV	A LEFT LOGICAL.
ALR	Ø416XX	SH	4	5	SRV	A LEFT ROTATE.
ALS	Ø415XX	SH	4	5	SRV	A LEFT SHIFT (SHORT INT ARITH).
ana	Ø3	MR			SRV	AND. (A) .AND. $[EA]16 \Rightarrow A$ .
ANL	Ø3 Ø3	MR	-	-	V	(A,B) .AND. [EA] 32 => A,B.
	50161					ADD GR TO FIELD ADDR REG.
	000605					ARG TRANSFER (USED WITH PCL).
	0404XX				SRV	A RIGHT LOGICAL.
	Ø4Ø6XX				SRV	A RIGHT ROTATE.
	Ø4Ø5XX				SRV	A RIGHT SHIFT (SHORT ARITH).
	50135					ADD TO TOP OF QUEUE.
_	141717					ADD TO TOP OF Q. CCEQ => FULL.
	141602				SRV	BRANCH ON CONDITION CODE .EQ.
	141605				SRV	BRANCH ON CONDITION CODE .GE.
	141601 141600				SRV	BRANCH ON CONDITION CODE .GT.
	141704				SRV	BRANCH ON CONDITION CODE .LE.
	141603				SRV SRV	BRANCH ON CONDITION CODE .LT.
	141705				SRV	BRANCH ON CONDITION CODE .NE. BRANCH ON CBIT RESET.
	141604				SRV	BRANCH ON CBIT SET.
	140734				SRV	BRANCH ON DECREMENTED X.
	140724				SRV	BRANCH ON DECREMENTED Y.
	140612				SRV	BRANCH ON A REGISTER .EO. Ø.
	50122					BRANCH ON FLTG REG EQ.
	141612				SRV	BRANCH ON FAC .EQ. Ø.
	50125					BRANCH ON FLTG REG NE.
	141615				SRV	BRANCH ON FAC .GE. Ø.
	50121					BRANCH ON FLTG REG LE.
	141611		_	4	SRV	BRANCH ON FAC .GT. Ø.
BFLE	50120	BR				BRANCH ON FLTG REG LT.
BFLE	141610	BR				BRANCH ON FAC .LE. Ø.
BFLT	50124	BR		4	I	BRANCH on FLTG REG GT.
BFLT	141614	BR	-	4	SRV	BRANCH ON FAC .LT. Ø.
BFNE	50123	BR	- 4	4	I	BRANCH ON FLTG REG GE.
	141613					BRANCH ON FAC .NE. Ø.
	140615					BRANCH ON A REGISTER .GE. Ø.
	140611					BRANCH ON A REGISTER .GT. 0.
	50144	BR		-		BRANCH ON HALF REG DEC BY 1.
BHD2	50145	BR	-	-	I	BRANCH ON HALF REG DEC BY 2.

BHD4	50146					BRANCH ON HALF REG DEC BY 4.
BHEQ	50112			4		BRANCH ON HALF REG EQ.
	50111					BRANCH ON HALF REG LE.
	50140					BRANCH ON HALF REG INCR BY 1.
BHI2	50141	BR	_	-	I	BRANCH ON HALF REG INCR BY 2.
BHI4	50142	BR	-	-	I	BRANCH ON HALF REG INCR BY 4.
BHLE	50110 50104	BR	_	4	I	BRANCH ON HALF REG LT.
BHLT	50104	BR	_	4	I	BRANCH ON HALF REG LT. BRANCH ON HALF REG GT.
BHNE	50113	BR	_	4	T	BRANCH ON HALF REG GE.
BHNE	50105	BR	_	4	T	BRANCH ON HALF REG NE.
RTX	141334	BR	_	_	SRV	BRANCH ON INCREMENTED X.
BIY	141324	BR	_	_	SRV	BRANCH ON INCREMENTED Y.
BLE	140610	DI.	_	1	CDV	BRANCH ON A REGISTER .LE. Ø.
DIEU	140702	DD	_	7	CDM	BRANCH ON L REGISTER .EQ. Ø.
DICE	140/02	ממ	_	1	ODV	BRANCH ON L REGISTER .EQ. 0.
DIGE	1402012	DK		4	DKV	
BLGI	140701 140700	BK	_	4	SRV	BRANCH ON L REGISTER .GT. Ø.
						BRANCH ON L REGISTER .LE. Ø.
BLLT	140614	BR	_	4	SRV	BRANCH ON L REGISTER .LT. Ø.
BLNE	140703	BR	_	4	SRV	BRANCH ON L REGISTER .NE. Ø.
	141707					BRANCH ON LINK RESET.
BLS	141706	BR	-	-	SRV	BRANCH ON LINK SET.
	140614					BRANCH ON A REGISTER .LT. Ø.
BMEQ	141602	BR	-	-	SRV	BRANCH ON MAGCOND. L,CC .EQ.
BMGE	141706	BR	_	_	SRV	BRANCH ON MAGCOND. L,CC .GE.
BMGT	141710	BR	_	-	SRV	BRANCH ON MAGCOND. L,CC .GT.
BMLE	141711	BR	_	_	SRV	BRANCH ON MAGCOND. L,CC .LE.
BMLT	141707	BR	_	_	SRV	BRANCH ON MAGCOND. L,CC .LT.
	141603					BRANCH ON MAGCOND. L,CC .NE.
BNE	140613	BR	_	4	SRV	BRANCH ON A REGISTER .NE. Ø.
	50040					BRANCH ON REG BIT RESET.
					_	
BGR1	50134	BR	_	_	Ť	BRANCH ON REG DEC BY 1.
BGR2	50135	BR		_	T	BRANCH ON REG DEC BY 2.
BCR4	50136	BR	_	_	_ T	BRANCH ON REG DEC BY 4.
BREO	50100	BR	_	Δ	Ť	BRANCH ON REG EO
BRCE	50105	BD	_	4	Ť	BRANCH ON REG NE
BDCT	50103	DD	_	1	T	BDANCH ON DEC IE
BDT1	50130	BB	_	_	T	RRANCH ON REG INCR BY 1
DDT2	50130	DD	_	_	T	BDANCH ON DEC THEN BY 2
DDT 4	20131	ממ		_	<b>T</b>	DRANCH ON DEC TACE BY A
DUIL DUIL	50100	ממ	_	_ A	T T	BRANCH ON REG BIT SET. BRANCH ON REG DEC BY 1. BRANCH ON REG DEC BY 2. BRANCH ON REG DEC BY 4. BRANCH ON REG EQ. BRANCH ON REG NE. BRANCH ON REG LE. BRANCH ON REG INCR BY 1. BRANCH ON REG INCR BY 2. BRANCH ON REG INCR BY 4. BRANCH ON REGISTER LT. BRANCH ON REGISTER GT.
BRLT	50100	ממ	_	4	T	BRANCH ON REGISTER LT.
	50103					21121011 011 12202022
				1		BRANCH ON REGISTER GE.
	61	MR				COMPARE R WITH [EA32].
						*CLEAR ACTIVE INTERRUPT.
						CLEAR A REG. LEFT BYTE.
						PROC CALL FROM FAULTING PROC.
	141044					CLEAR A REG. RIGHT BYTE.
	11	MR			SRV	SKIP $\emptyset, 1, 2$ IF (A) >,=.< [EA] 16.
CAZ	140214					SKIP $\emptyset$ ,1,2 INST. IF A >,=,< $\emptyset$ .
CEA	000111	OPR	-	_	SRV	A AS EA=>A. (USELESS IN 64V ).
CGT	50026					COMPUTED GOTO.
CGT	001314				SRV	COMPUTED GO TO.
CH	71	MR		1		COMPARE RH WITH [EA16].
CHS	50040	OPR	-	-	I	CHANGE SIGN $-(1) = > GR(1)$ .
CHS	140024	OPR				CHANGE SIGN OF A REGISTER.
CLS	11 Ø3	MR	1	1	V	SKIP $\emptyset,1,2$ IF $(A,B)>,=.<[EA]32.$

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CMA	140401	OPR	-	-	SRV	ONE'S COMPLEMENT A REGISTER.
CMH	50045	OPR	_	_	I	COMP HALF REG. GRH=>GRH.
	50044					COMP REG. GR=>GR.
CR	50056					CLEAR REG. Ø => GR.
CRA	140040					CLEAR A REGISTER. Ø=>A.
	140015					CLEAR B REGISTER. Ø=>B.
CRB						LEFT BYTE Ø=>GRH(1-8).
	50062					
CRBR						RIGHT BYTE Ø=>GRH(9-16).
CRE	141404					CLEAR E. Ø=>E.
CREP			-			(P) => [(S)+1]16, EA=>P.
CRL	140010					CLEAR L REGISTER. Ø=>L.
CRLE	141410	OPR	-	-	SRV	CLEAR L AND E. $\emptyset = >L$ , $\emptyset = >E$ .
	140320					CPY SIGN OF A. Al=>CBIT, $\emptyset$ =>Al.
CSR	50041	OPR	_	-	I	COPY & SAVE SIGN. 1=>C, 0=>GR1.
	001714		_	_	V	CONTROL EXTENDED CONTROL STORE.
D	62	MR		1		DIV. $(R,R+1)/[EA32=>R RMR=>R+1.$
DAD	Ø6 (DP)				SR	(A,B) + [EA] 32 => A,B.(W/HOLE).
DBL	000007					ENTER DOUBLE-PREC MODE.
	50106					CONV SINGLE TO DOUBLE FLTG PT.
						DECR HALFWD. [EA16]-1=>[EA16].
DECH			_			
DFA	15 17	MR		1		DBLE FLTG ADD. DFR+[EA64]=>DFR.
	Ø6 Ø2	MR			RV	(DFAC) + [EA]64 => DFAC.
	Ø5 Ø7	MR		1		DBLE FLTG COMP DRF TO [EA64].
	50144					DBL PRC FLTG COMPDFGR=>DFGR.
DFCM	140574	FOP	3	5	SRV	-DFAC=>DFAC.
	11 Ø2	MR	6	5	RV	SKP $\emptyset,1,2$ IF (DFAC)>,=.<[EA]64.
DFD	31,33	MR	3	1	I	DOUBLE FLTG DIVIDE.
DFDV	17 02	MR	3	5	RV	$(DFAC) / [EA]64 \Rightarrow DFAC.$
DFL	Ø1 Ø3	MR	_	_	I	DBLE FLTG LOAD. [EA643]=>DFR.
	Ø2 Ø2	MR			RV	[EA]64 => DFAC.
	15 Ø2	MR		_		LD DFLT INDEX. $4*[EA]16 \Rightarrow X$ .
DFM	25 27	MR		1		DBL FLTG MULT. DFR/[EA64]=>DFR.
	16 Ø2				RV	(DFAC) * [EA] 64 => DFAC.
		MR				DBLE FLTG SUB. DFR-[EA64]=>DFR.
DFS	21 23	MR		1		
	Ø7 Ø2	MR			RV	(DFAC) - [EA] 64 => DFAC.
	11,13	MR		1		DBLE FLTG STORE. DFR=>[EA64].
DFST	Ø4 Ø2	MR			RV	$(DFAC) \Rightarrow [EA] 64.$
DH	72	MR		1		DIV HW. R/[EA16]=>RH RM=>RL(2).
DHl	50130					DECR HALF REG BY 1. GRH-1=>GRH.
DH2	50131	OPR	2	1	I	DECR HALF REG BY 2. GRH-2=>GRH.
DIV	17	MR	3	5	V	(A,B)/[EA]16=>A,REM=>B.(NOHOLE)
DIV	17	MR	3	5	SR	(A,B)/[EA]16=>A;REM=>B.(W/HOLE)
DLD	Ø2 (DP)	MR	_	_	SR	DOUBLE LOAD. [EA] 32 => A,B.
DM	60	MR				DECR. $[EA32]-1=>[EA32]$ .
DMH	70	MR		1		DECR HALDWD. [EA16]-1=>[EA16].
DRl	50124					DECR REG BY 1. GR-1=>GR.
DR2	50125	ODD	2	ī	Ť	DECR REG BY 2. GR-2=>GR.
						DECREMENT X AND SKIP IF Ø.
DRX	140210					
DSB	Ø7 (DP)					$(A,B)-[EA]32 \Rightarrow A,B (W/HOLE)$ .
DST	Ø4 (DP)			_		DOUBLE STORE. (A,B) => [EA] 32.
DVL	17 Ø3	MR			V	(A,B,E)/[EA]32=>A,B REM=>EH,EL.
	000011					ENTER P300 16K SECTORED MODE.
E32I	001010	MOD	-	-	SRV	ENTER P500 321 MODE.
E32R	001013	MOD	-	-	SRVI	ENTER P300 32K RELATIVE MODE.
E32S	000013	MOD	-	-	SRVI	ENTER P300 32K SECTORED MODE.
E64R	001011	MOD	_	_	SRVI	ENTER P300 64K RELATIVE MODE.
E64V	000010	MOD	_	_	SRVI	ENTER P400 MODE.

EAA Ø1 Ø1 MR --R EFF. ADDR TO A-REG. EA => A. EAFA 001300 AP -- SRVI EFF. ADDR TO FIELD REG 0. EAFA 001310 AP -- SRVI EFF. ADDR TO FIELD REG 1. EAL 01 01 MR -- V LOAD EFFECTIVE ADDR. EA => L. EALB 42 MR - - I EFF ADDR to LB. EA=>LB. EALB 13 02 MR -- V EFF. ADDR TO LB. EA => LB. EAR 63 MR - - IEFF ADDR (EA32) TO R. EAXB 52 MR -- I EFF ADDR TO XB. EA=>XB. EAXB 12 02 MR -- V EFF. ADDR TO XB. EA => XB. MR - 2 I EIO 34 EXECUTE ADDR TO BASE REG. EIO 14 01 MR - 7 V\* EXEC EA AS I/O INSTR.CCEQ=>SKP. EMCM 000503 IG -- SRVI\*ENTER MACH CHK MODE. ENB 000401 IO - - SRVI\*ENABLE INTERRUPTS. ENTR Ø1 Ø3 MR -- R (S) = > [(S) - EA] 16, (S) - EA = > S.EPMJ 000217 VM - - SR ENT PAGE MODE AND JUMP (P300). EPMX 000237 VM -- SR ENT PAG MOD & JMP TO MICROCODE. MR - - SRV (A) .XOR.  $[EA]16 \Rightarrow A$ . ERL Ø5 Ø3 MR -- V (A,B) .XOR. [EA] 32 => A,B. ERMJ 000701 VM -- SR ENTER RESTR MODE & JUMP (P300). ERMX 000721 VM - - SR RESTR MOD & JUMP TO MICROCODE. ESIM 000415 MOD - - SRVI \*ENTER STANDAGR INTERRUPT MODE. EVIM 000417 MOD - - SRVI\*ENTER VECTORED INTERUPT MODE. EVMJ 000703 VM - - SR ENT VIRT MODE AND JUMP (P300). EVMX 000723 VM -- SR VIRT MOD & JUMP TO MICROCODE. 14,16 MR 3 1 I FLTG ADD. FR+[EA32]=>FR. FAD 06 01 MR 3 5 RV (FAC) + [EA] 32 => FAC.04,06 MR - 1 I FC FLTG COMPARE FR TO [EA32] FCM 50100 FOP 3 1 I FLTG COMP. -FGR=>FGR. FCM 140530 FOP 3 5 SRV FLOATING COMP. -FAC=>FAC. FCS 11 01 MR 6 5 RV SKIP  $\emptyset,1,2$  IF (FAC)>,=.<[EA]32. 30,32 MR 3 1 I FLTG DIV. -FR/[EA32]=>FR. FDBL 140016 FOP - - SRV FAC=>DFAC. FDV 17 Ø1 MR 3 5 RV  $(FAC) / [EA]32 \Rightarrow FAC.$ FLD 02 01 MR -- RV FLOATING LOAD. [EA] 32 => FAC. FLOT 140550 FOP 6 5 SRV FLOT(A,B)=>FAC. (A,B) W/HOLE 50105 FOP - - I CONV INT TO FLTG PT FLOAT. FLTA 140532 FOP 3 5 SRV FLOT(A) => FAC. FLTH 50102 FOP - - I CNV HALF WD INT TO FLTG PT. FLTL 140535 FOP 8 5 SRV FLOT(L)=>FAC. (L W/NO HOLE) FLX 15 Ø1 MR - - RV 2\*[EA]16 => X.24,26 MR 3 1 I FLTG MULT. FR\*[EA32]=>FR. FMP 16 Ø1 MR 3 5 RV (FAC) \* [EA] 32 => FAC.FRN 50107 FOP 3 1 I FLOATING ROUND UP. FRN 140534 FOP 3 5 SRV FLOATING ROUND UP. 20,22 MR 3 1 I FLTG SUB. FR-[EA32]=>FR. FSB 07 01 MR 3 5 RV  $(FAC) - [EA]32 \Rightarrow FAC.$ FSGT 140515 FSK - 4 SRV FLOATING SKIP IF .GT. 0. FSLE 140514 FSK - 4 SRV FLOATING SKIP IF .LE. 0. FSMI 140512 FSK - 4 SRV FLOATING SKIP IF .LT. 0. FSNZ 140511 FSK - 4 SRV FLOATING SKIP IF .NE. 0. FSPL 140513 FSK - 4 SRV FLOATING SKIP IF .GE. 0. FST 10,12 MR -- I FLTG STORE. FR=>[EA32]. FST 04 01 MR 6 6 RV (FAC) => [EA] 32.FSZE 140510 FSK - 4 SRV FLOATING SKIP IF .EO. 0. HLT 000000 CON - - SRVI \*HALT COMPUTER OPERATION. MR -- I INTERCHANGE R WITH [EA32]. IAB 000201 OPR - - SRV EXCHANGE A AND B A=>B & B=>A.

ICA 141340 OPR SRV INTERCHANGE BYTES OF A REG.	LCGT 50155 LOG I	IF .GT., 1=>GRH, ELSE Ø=>GRH.
ICBL 50065 OPR I GRH(1-8)=>GRH(9-16), $\emptyset$ =>R.	LCGT 141505 LOG SRV	IF CC.GT., $1=>A$ . ELSE $\emptyset=>A$ .
ICBR 50066 OPR I GRH (9-16) =>GRH (1-8), 0=>		IF .LE., 1=>GRH, ELSE Ø=>GRH.
		IF CC.LE.,1=>A. ELSE Ø=>A.
ICHR 50061 OPR I GRL=>GRH, 0=>GRL.	LCLT 50150 LOG I	IF .LT., $1=>GRH$ , ELSE $\emptyset=>GRH$ .
ICL 141140 OPR SRV EXCHANGE BYTES OF A, CLR LEFT.	LCLT 141500 LOG SRV	IF CC.LT., $1=>A$ . ELSE $\emptyset=>A$ .
ICR 141240 OPR SRV EXCHANGE BYTES OF A, CLR RIGHT.		IF .NE.,1=>GRH, ELSE Ø=>GRH.
IH 51 MR I INTERCHANGE RH WITH [EA16].		IF CC.NE., $l \Rightarrow A$ . ELSE $\emptyset \Rightarrow A$ .
IH1 50126 OPR 2 1 I GRH+l=>GRH.		LOAD A-REGISTER. [EA]16 => A.
IH2 50127 OPR 2 1 I GRH+2=>GRH.		
		LOAD ADDR REGISTER.
		LOAD CHAR TO GRH.
IM 40 I - 1 I INCR. $[EA31]+1=>[EA32]$ .	LDC 001312 FLD - 7 SRV	LOAD CHAR TO A REG PER FAR 1.
IMA 13 MR SRV EXCHANGE MEMORY AND A-REGISTER.	LDC 001302 FLD - 7 SRV	LOAD CHAR TO A REG VIA FAR Ø.
IMH $\emptyset$ MR - 1 I INCR HALF WD. [EA16]+1=>[EA16].	LDL 02 03 MR V	LOAD LONG. [EA] 32 => A,B.
INA 54 PIO SR* INPUT TO A-REGISTER.		LOAD LONG FROM RFILE LOC EA.
INBC 001217 AP 6 5 SRVI*NTFY IN INT, LIFO, DO CAI.		LOAD X-REGISTER. [EA]16 => X.
INBN 001215 AP 6 5 SRVI*NTFY IN INT, LIFO, NO CAI.		
INEC 001216 AP 6 5 SRVI*NTFY IN INT, FIFO Q, DO CAI.		LOAD Y-REGISTER. [EA]16 => Y.
INEC UNIZIO AP 6 5 SKVI NIFI IN INI, FIFO Q, DO CAI.		IF GR=0 TN 1=>GRH,ELSE 0=>GRH.
INEN 001214 AP 6 5 SRVI*NTFY IN INT, FIFO Q, NO CAI.	LEQ 140413 LOG - 4 SRV	IF A.EQ.0, $1=>A$ . ELSE $0=>A$ .
INH 001001 IO SRVI*INHIBIT INTERRUPTS.	LF 50016 LOG - 4 I	LOGICIZE .F. Ø=>GRH.
INK 50070 OPR I MOVE KEYS TO REG KEYS.	LF 140416 LOG - 5 SRV	LOGICIZE FALSE. Ø=>A.
INK 000043 OPR SRV INPUT P-300 KEYS INTO A REG.	LFEQ 50023 LOG - 4 I I	F FRS=0 TN 1=>GRH,ELSE 0=>GRH.
INT $50103 \text{ FOP } \text{ I}$ INT (FRS) =>GR.		IF FAC.EQ.Ø, 1=>A. ELSE Ø=>A.
INT 140554 FOP 3 5 SRV INT(FAC)=>A,B W/HOLE.		
		IF FRS>=0 T 1=>GRH,ELSE 0=>GRH.
INTA 140531 FOP 3 5 SRV INT(FAC)=>A.		IF FAC.GE. $\emptyset$ , $1=>A$ . ELSE $\emptyset=>A$ .
INTH 50101 FOP I INT(FRS)=>GRH.	LFGT 50025 LOG - 4 I	IF FRS>0 TN 1=>GRH,ELSE 0=>GRH.
INTL 140533 FOP 3 5 SRV INT(FAC)=>L.	LFGT 141115 LOG - 4 SRV	IF FAC.GT.0, l=>A. ELSE 0=>A.
IR1 50122 OPR 2 1 I INCR REG BY 1. $GR+1=>GR$ .	LFLE 50021 LOG - 4 I	IF FRS<=0 T 1=>GRH,ELSE 0=>GRH.
IR2 50123 OPR 2 1 I INCR REG BY 2. GR+2=>GR.		IF FAC.LE.0, $1=>A$ . ELSE $\emptyset=>A$ .
IRB 50062 OPR I BYTES GRH(1-8)=>GRH(9-16)		LOAD FIELD LEN REG IMM ONE.
The state of the s		
		LOAD FIELD LEN REG IMM ZERO.
IRS 12 MR SRV INC, REPLACE, AND SKIP IF ZERO.		IF FRS<0 T 1=>GRH,ELSE 0=>GRH.
IRTC 000603 CON 7 6 SRVI*INTERRUPT RETURN, DO CAI.	LFLT 141110 LOG - 4 SRV	IF FAC.LT.0, 1=>A. ELSE 0=>A.
IRTN 000601 CON 7 6 SRVI*INTERRUPT RETURN, NO CAI.	LFNE 50022 LOG - 4 I	IF FRS<>0 T 1=>GRH,ELSE 0=>GRH.
IRX 140114 OPR SRV INCREMENT X AND SKIP IF $\emptyset$ .	LFNE 141112 LOG - 4 SRV	IF FAC.NE.0, $1=>A$ . ELSE $0=>A$ .
ITLB 000615 CON SRVI*INVAL STLB ENTRY, L = VADDR.		IF GR>=0 TN 1=>GRH,ELSE 0=>GRH.
JDX 15 02 MR R JUMP ON DECREMENTED X-R ZERO.		IF A.GE. $\emptyset$ , $1=>A$ . ELSE $\emptyset=>A$ .
JEQ 02 03 MR $$ R IF (A) .EQ. 0, EA => P.		
JGE 07 03 MR R IF (A) .GE. 0, EA => P.		IF GR>0 TN 1=>GRH, ELSE 0=>GRH.
		IF A.GT.0, 1=>A. ELSE 0=>A.
JGT 05 03 MR R IF (A) .GT. 0, EA => P.		LOAD HALF WD. [EA16]=>RH.
JIX 15 03 MR R JUMP ON INCREMENTED X-REG ZERO.		IF GRH=0 TN 1=>GRH,ELSE 0=>GRH.
JLE 04 03 MRR IF (A) .LE. 0, EA $\Rightarrow$ P.	LHGE 50004 LOG - 4 I	IF GRH>=0 T 1=>GRH,ELSE 0=>GRH.
JLT $\emptyset 6 \ \emptyset 3 \ MR R$ IF (A) .LT. $\emptyset$ , EA => P.	LHGT 50015 LOG - 4 I	IF GRH>0 TN 1=>GRH,ELSE 0=>GRH.
JMP 51 MR I JUMP. EA=>RP.		SHIFTED 1 [EA16] .LS. 1=>GRH.
JMP 01 MR SRV UNCOND JUMP. EA => PB,P.		SHIFTED 2 [EA16] .LS. 2=>GRH.
(a) 1 (b) 1 (b) 1 (c)		
		IF GRH<=0 T l=>GRH,ELSE 0=>GRH.
		IF GR<0 TN 1=>GRH,ELSE 0=>GRH.
JST 10 MR SRV (P) $\Rightarrow$ [EA]16, EA+1 $\Rightarrow$ P.	LHNE 50012 LOG - 4 I	IF GRH<>0 T l=>GRH,ELSE 0=>GRH.
JSX 35 $\emptyset$ 3 MR RV (P) => X, EA => P.	LLE 50001 LOG - 4 I	IF GR<=0 TN 1=>GRH,ELSE 0=>GRH.
JSXB 61 MR I RP=>XB, EA=>RP.		IF A.LE.Ø, 1=>A. ELSE Ø=>A.
JSXB 14 02 MR $V$ (PB,P) $\Rightarrow$ XB, EA $\Rightarrow$ PB,P.		IF L.EQ.Ø, l=>A. ELSE Ø=>A.
JSY 14 MR $V$ (P) $\Rightarrow$ Y, EA $\Rightarrow$ P.		IF L.GE. $\emptyset$ , $1=>A$ . ELSE $\emptyset=>A$ .
- TOTA (P1221-\D		IF L.GT.0, 1=>A. ELSE 0=>A.
		LONG LEFT LOGICAL.
LCEQ 141503 LOG SRV IF CC.EQ.,1=>A. ELSE Ø=>A.		IF L.LE.Ø, 1=>A. ELSE Ø=>A.
LCGE 50154 LOG I IF .GE., l=>GRH, ELSE0=>GRH.		IF L.LT.0, 1=>A. ELSE 0=>A.
LCGE 141504 LOG SRV IF CC.GE.,1=>A. ELSE Ø=>A.	LLNE 141512 LOG - 4 SRV	IF L.NE.0, l=>A. ELSE Ø=>A.

LLR	Ø412XX	SH	4	5	SRV	LONG LEFT ROTATE.
LLS					SRV	LNG LSFT 64V=>LNG INT,ELSE HOLE
	50000				T	IF $GR < \emptyset$ TN 1=> $GRH$ , ELSE $\emptyset$ => $GRH$ .
T.T.T	140410	LOG	_	4	SRV	IF A.LT.0, $1=>A$ . ELSE $\emptyset=>A$ .
I.MCM	000501	TG	_	_	SRVT	LEAVE MACHINE CHECK MODE.
INE	50002	100	_	1	T	IF GR<>0 TN 1=>GRH, ELSE 0=>GRH.
LNE						IF A.NE.Ø, 1=>A. ELSE Ø=>A.
						LOAD PROCESS ID FROM A01-A12.
	000215					LEAVE PAGE MODE & JUMP (P300).
	000215					LVE PAG MOD & JMP TO MICROCODE.
LDCM	000233	V.D Ala	7	6	CDITI	LOAD PSW (SN,WN,KEYS,MODALS).
TESM	000711	CU	1	5	CDM.	LONG RIGHT LOGICAL.
						LONG RIGHT ROTATE.
TKK	040200	SII	4	5	CDV	LNG RSFT.64V=LNG INT, ELSE HOLE.
			_	)	DKV	LOGICIZE TRUE. 1=>A.
	001710				v I	LOAD WRITABLE CONTROL STORE.
M	42	MR	3	1	T CDVIII	MULT. R*[EA32]=>(R,R+1). *MEM DIAG ENABLE INTERLEAVE.
	001304					
	001305					MEM DIAGN INHIBIT INTERLEAVE.
	0013??		_	_	SKVI	*MEM DIAG WRT INTERLV. L=>[E].
	001306					*MEM DIAG READ SYNDROME BITS.
	001307					MEM DIAG LOAD WRITE CTL REG.
MH	52	MR				RH*[EA16]=>R.
MIA	64	MR	-	-	1	MICROCODE EXECUTE A.
MIA	12 Ø1	MR	_	-	V	MICROCODE ENTRANCE. MICROCODE EXECUTE B.
MIB	74	MR	_	_	I	MICROCODE EXECUTE B.
MIB	13 Ø1	MIR				MICROCODE ENTRANCE.
MPL	16 Ø3	MR				(A,B) * [EA]16 => A,B. (NOHOLE)
MPY	16	MR				(A) * [EA]16 => A,B. (NOHOLE).
MPY	16	MR	3	1	SR	(A) * [EA] 16 => A, B. (HOLE).
N	Ø3	MR				R .AND. [EA32]=>R.
NFYB	001211	AP	6	5	SRVI	*NOTIFY ON SEM AT AP. LIFO Q.
		AΡ	ь	5	SRVI	*NOTIFY ON SEM AT AP. FIFO Q.
NH	13		-			RH .AND. [EA16]=>RH.
NOP	000001					
NRM	000101	OPR	_	_	SRV	NORMALIZE A,B AS ON P-300.
0	23	MR PIO	_	_	CD*	R .OR. [EA32]=>R.
OCP						OUTPUT CONTROL PULSE.
OH		MR				RH .OR. [EA16] => RH.
ORA	Ø3 Ø2				v SR*	(A) .OR. [EA]16 => A.
OTA	74 50071					OUTPUT FROM A-REGISTER.
OTK						SET KEYS FROM REGISTER OUTPUT A TO 300 KEYS, SHFT CTR.
OTK	000405					
PCL	41	MR			V	PROCEDURE CALL. P-400 PROCEDURE CALL.
PCL	10 02					
PID	50052					GR=>GRN, $(1)=>GR(2-32)$ .
PID						SHORT INT TO DP INT W/HOLE.
	000115					SHORT TO LONG INT CONV. A=>L.  GRH=>GRL,GRH(1)=>GRH(2-16).
PIDH						
	000305 50050					CONVERT LONG INT TO 64 BIT INT. POS REG AFTER INT MULTIPLY.
PIM						DP INT WITH HOLE TO SHORT INT.
PIM						L=>A. IEX ON PREC LOSS.
	000015 50051					POS HALF REG AFTER INT MULT.
	000301					64BIT INT TO LONG INT. (L,E)=>L.
						PROCEDURE RETURN.
RBQ	50133					REMOVE FROM BOTTOM OF QUEUE.
MOÑ	סכדמכ	OFR	_	′	_	TENOVE FROM DOLLOW OF QUEOF.

RBQ						RMV BOT OF Q. EMP=>A=Ø, CCEQ
RCB	140200					RESET CBIT. $\emptyset = > \text{CBIT}$ .
RMC	000021	IG	-	-	SRVI *	RESET MACHINE CHECK FLAG.
	000717					REST REGS (GEN, FLT, XB).
RSAV	000715	AΡ	-	-	SRV	SAVE REGS (GEN, FLT, XB).
RTN	000105	OPR	-	-	SRV	RETURN FROM P-300 RECUR PROC.
RTQ	50132	OPR	_	7	I	REMOVE FROM TOP OF QUEUE.
RIQ	141714	AΡ	_	6	SRV	RMV TOP OF Q. EMP=>A=0, CCEQ.
S	22	MR	2	1	I	SUB. R-[EA32]=>R.
SlA	140110	OPR	2	1	SRV	SUB 1 FROM A REGISTER. A-1=>A.
S2A	140310	OPR	2	1	SRV	SUB 2 FROM A REGISTER. A-2=>A.
SAR	10026X					SKIP IF A REG. BIT N RESET.
SAS	101260	SKP	_	_	SRV	SKIP IF A REG. BIT N SET.
SBL	Ø7 Ø3	MR			V	$(A,B) - [EA]32 \Rightarrow A,B. (NOHOLE)$
SCA	000041	OPR				LOAD P-300 SHFT CTR INTO A REG.
SCB	140600	OPR	5	_	SRV	SET CBIT. 1=>CBIT.
SGL	000005					ENTER SINGLE-PRECISION MODE.
SGT	100220	SKP	_	_	SRV	SKIP IF A REGGT. Ø.
SH	32	MR				SUB HALF WD. RH-[EA16]=>RH.
SHA	15	MR				ARITHMETIC SHIFT.
SHL	Ø5	MR				LOGICAL SHIFT.
SHLl	50076					GRH .LS. 1 => GRH.
	50077					GRH .LS. 2 => GRH.
SHRl	50120					GRH .RS. 1 => GRH.
SHR2	50121					GRH .RS. 2 => GRH.
SKP	100000					SKIP ONE WORD.
SKS	34	PIO				SKIP IF CONDITION SET.
	50072					$GR . LS. 1 \Rightarrow GR.$
SL2	50072					GR .LS. 2 => GR.
SLE	101220					SKIP IF A REGLE. Ø.
SLN	101100					SKIP IF A REG. BIT 16 SET.
SLZ	100100					SKIP IF A REG. BIT 16 .EQ. Ø.
	100100					SKIP IF MACHINE CHECK RESET.
	101200					SKIP IF MACHINE CHECK SET.
SMI	101400					SKIP IF A REGLT. 0.
SMK	170020					SET INTERRUPT MASKS.
SNR						SKIP IF SENSE SWITCH N RESET.
	101247	SVL	_	_	SKA.	SKIP IF SENSE SWITCH N RESELT.
SNS						
SNZ	101040					SKIP IF A REG. NE. Ø.
SPL	100400					SKIP IF A REGGE. Ø.
SRl	50074					$GR .RS. 1 \Rightarrow GR.$
SR1						SKIP IF SENSE SWITCH 1 RESET.
SR2						GR .RS. 2 => GR.
SR2	TOOOTO	SKP	_	_	SRV*	SKIP IF SENSE SWITCH 2 RESET.
SR3	100004	SKP	_	-	SRV*	SKIP IF SENSE SWITCH 3 RESET.
SR4	100002	SKP	-	-	SRV*	SKIP IF SENSE SWITCH 4 RESET.
SRC						SKIP IF CBIT RESET.
SSl						SKIP IF SENSE SWITCH 1 SET.
SS2						SKIP IF SENSE SWITCH 2 SET.
SS3						SKIP IF SENSE SWITCH 3 SET.
SS4						SKIP IF SENSE SWITCH 4 SET.
SSC	101001					SKIP IF CBIT SET.
SSM	50042					SET SIGN. MINUS 1=>(1).
SSM	140500					SET SIGN OF A MINUS. 1=>A1.
SSP	50043					SET SIGN. PLUS Ø=>(1).
SSP	140100					
SSR	100036	SKP	-	-	SRV*	SKIP IF SSWI 1,2,3 AND 4 RESET.

SSS	101036	SKP	_	_	SRV*	SKIP IF SSWI 1,2,3 AND 4 SET.
$\operatorname{ST}$	21	MR	_	-	I	STORE. R=>[EA32].
STA	04	MR	_	_	SRV	STORE A-REG. (A) $\Rightarrow$ [EA]16.
STAC	001200	AΡ	_	7	SRV	ST A COND ON B=[EA16].CCEQ= OK.
STAR	54	MR	_	-	I	STORE ADDRESSED REGISTER.
STC	50166	FLD	_	7	I	STORE CHARACTER FROM GRH.
STC	001332					STORE CHAR FROM A (SEE FAR1).
STC	001322	FLD	_	7	SRV	STORE CHAR FROM A (SEE FAR®).
STCD	50137	OPR	_	7	Т	GR+1=[EA16].
STCH	50136	OPR	_	7	I	GRL=[EA16].
STEX	50027	OPR	-	7	Ι	STACK EXTEND.
STEX	001315	OPR	6	5	SRV	STK EXTEND. L REG HAS EXTENT.
	001330					
STFA	001320					STORE FIELD ADDR REG Ø.
STH	31	MR				STORE HALF WD. RH=>[EA16].
STL	Ø4 Ø3	MR	-	-	V	STORE LONG. $(A,B) \Rightarrow [EA]32$ .
	001204					ST L COND ON E=[EA32].CCEQ=OK.
STLR	Ø3 Ø1	MR				STORE LONG INTO RFILE LOC EA.
STX	15				SRV	STORE X-REG. $(X) \Rightarrow [EA]16$ .
STY		MR				STORE Y-REG. $(Y) \Rightarrow [EA]16$ .
SUB	Ø7	MR			SRV	SUBTRACT. (A) $-$ [EA]16 $\Rightarrow$ A.
SVC	000505					
SZE	100040	SKP	-	_	SRV	SKIP IF A REG .EQ. Ø.
TAB	140314					TRANSFER A TO B REG. A=>B.
TAK	001015					TRANSFER A TO KEYS.
TAX	140504					TRANS A REG TO X REG. A=>X.
TAY	140505					TRANS A REG TO Y REG. A=>Y.
TBA	140604					TRANS B REG TO A REG. B=>A.
	50047					-GR+1=>GR.
TCA	140407					TWO'S COMPLEMENT AA=>A.
	50047					-GRH+1=>GRH.
	141210					TWO'S COMPLEMENT LL=>L.
	001333					XFER FLD LEN REG TO L REG 1.
	001323					XFER FLD LEN REG TO L REG Ø.
	50163 001005	LIT	_	1	CD/A	TRANSFER FLD LENGTH REG TO GR. TRANSFER KEYS TO A.
TKA						TRANS L TO FLD LEN REG Ø.
	001321 001331					TRANSFER L TO FLD LEN REG 1.
IM	44	MR				REST MEM. [EA32]::0=>CC.
	50165					TRANSFER GR TO FLD LENGTH REG.
	50103					TEST QUEUE.
	141757					TEST Q. A=# ITEMS. CCEQ=> EMP.
	141034					TRANS X REG TO A REG. X=>A.
	141124					TRANS Y REG TO A REG. Y=>A.
VIRY	000311	IG	5	6	SRV*	EXECUTE VERIFICATION ROUTINE.
	000315					WAIT ON SEMAPHORE AT AP.
						WCS ENTRANCES. UII ON NO WCS.
X	43	MR				EXC OR. R .XOR. $[EA32] = > R$ .
XAD	001100				VI	ADD TWO DECIMAL FLDS.
	001145				VI	CONV BI DEC VALUE TO DEC FLD.
XCA	140104	OPR	_	_	SRV	EXCHG AND CLR A. $A=>B$ , $\emptyset=>A$ .
XCB	140204					EXCHG AND CLR B. B=>A, Ø=>B.
XEC	Ø1 Ø2	MR			RV	EXECUTE INSTRUCTION AT EA.
XCM	001102	DA	X	Х	VI	COMP TWO NUMERIC FLDS.
	001146				VI	CONV DEC FLD TO BI REG VALUE.
XDZ	001107	DA	X	Х	VI	DIV DEST FLD BY SOURCE FLD.
XEC	Ø1 Ø2	MR	-	-	RV	EX EFF ADDR CONT AS THIS INST.

XED	001112	DD	v	v	777	EDIT NUMERIC FIELD.
VED	DOTITZ	LE	Λ	Λ	VΙ	
XH	52	MR	-	_	I	RH .XOR. $[EA16] = > RH$ .
XMP	001104	DA	Х	Х	VI	MULTIPLY TWO DEC FLDS.
XMV	001101	DA	Х	Х	VI	MOVE NUM SOURCE FLD TO DST FLD.
ZCM	001117	CS	Х	Х	VI	COMP TWO CHAR STR FIELDS.
ZED	001111	FE	Х	Х	VI	EDIT CHAR STR FIELD.
ZFIL	Ø11116	CS	Х	Х	VI	FILL CHAR STR FLD WITH CHAR.
ZM	43	MR		-	I	ZERO MEM. $\emptyset = > [EA32]$ .
ZMH	53	MR	_	-	I	ZERO MEM. HALF WD. 0=>[EA16].
ZMV	001114	CS	Х	Х	VI	CPY FRM SOURCE FLD TO DEST FLD.
ZMVD	001115	-	Х	Х	VI	CPY FROM SOURCE FLD TO DEST
						FLD OF EQUAL LENGTH.
ZTRN	001110	CS	Х	Х	VI	TRANS SRCE STR FLD TO DEST FLD.
xxx	140014	OPR	-	_	SRV	OBSOLETE. CLRS B, LSW OF DFAC.

#### INSTRUCTION SET GROUPED BY FUNCTION

### Miscellaneous Operations

CGT 001314 - 6 5 SRV COMPUTED GO TO.

LFLI 001313 - - SRV LOAD FIELD LEN REG IMM ONE.

LFLI 001303 - - SRV LOAD FIELD LEN REG IMM ZERO.

WCS 0016XX - - SRV\* WCS ENTRANCES. UII ON NO WCS.

ZMVD 001115 - X X VI CPY FROM SOURCE FLD TO DEST

#### Address Pointer Operations

ABQ 141716 AP - 6 SRV ADD TO BOT OF Q. CCEQ => FULL. ATQ 141717 AP - 6 SRV ADD TO TOP OF Q. CCEQ => FULL. CALF 000705 AP 7 6 SRV PROC CALL FROM FAULTING PROC. EAFA 001300 AP -- SRVI EFF. ADDR TO FIELD REG 0. EAFA 001310 AP -- SRVI EFF. ADDR TO FIELD REG 1. INBC 001217 AP 6 5 SRVI\*NTFY IN INT, LIFO, DO CAI. INBN 001215 AP 6 5 SRVI\*NTFY IN INT, LIFO, NO CAI. INEC 001216 AP 6 5 SRVI\*NTFY IN INT, FIFO Q, DO CAI. INEN 001214 AP 6 5 SRVI\*NTFY IN INT, FIFO Q, NO CAI. LPSW 000711 AP 7 6 SRVI\*LOAD PSW (SN,WN,KEYS,MODALS). NFYB 001211 AP 6 5 SRVI\*NOTIFY ON SEM AT AP. LIFO Q. NFYE 001210 AP 6 5 SRVI\*NOTIFY ON SEM AT AP. FIFO Q. RBO 141715 AP - 6 SRV RMV BOT OF Q. EMP=>A=0, CCEQ RRST 000717 AP -- SRV REST REGS (GEN, FLT, XB). RSAV 000715 AP -- SRV SAVE REGS (GEN, FLT, XB). RTQ 141714 AP - 6 SRV RMV TOP OF Q. EMP=>A=0, CCEQ. STAC 001200 AP - 7 SRV ST A COND ON B=[EA16].CCEQ= OK. STFA 001330 AP -- SRVI STORE FIELD ADDR REG 1. STFA 001320 AP  $\,$  - - SRVI STORE FIELD ADDR REG 0. STLC 001204 AP - 7 SRV ST L COND ON E=[EA32].CCEQ=OK. TSTQ 141757 AP - 6 SRV TEST Q. A=# ITEMS. CCEQ=> EMP. WAIT 000315 AP -- SRV\* WAIT ON SEMAPHORE AT AP.

#### Branch Operations

BCGQ 141602 BR -- SRV BRANCH ON CONDITION CODE .EQ. BCGE 141605 BR -- SRV BRANCH ON CONDITION CODE .GE.

```
BCGT 141601 BR -- SRV BRANCH ON CONDITION CODE .GT.
 BCLE 141600 BR - - SRV BRANCH ON CONDITION CODE .LE.
 BCLT 141704 BR - - SRV BRANCH ON CONDITION CODE .LT.
 BCNE 141603 BR - - SRV BRANCH ON CONDITION CODE .NE.
 BCR 141705 BR - - SRV BRANCH ON CBIT RESET.
 BCS 141604 BR - - SRV BRANCH ON CBIT SET.
 BDX 140734 BR - - SRV BRANCH ON DECREMENTED X.
 BDY 140724 BR -- SRV BRANCH ON DECREMENTED Y.
 BEQ 140612 BR - 4 SRV BRANCH ON A REGISTER .EQ. 0.
 BFEQ 50122 BR - 4 I
                              BRANCH ON FLTG REG EQ.
 BFEQ 141612 BR - 4 SRV BRANCH ON FAC .EQ. Ø.
 BFGE 50125 BR - 4 I
                              BRANCH ON FLTG REG NE.
 BFGE 141615 BR - 4 SRV BRANCH ON FAC .GE. Ø.
 BFGT 50121 BR - 4 I
                              BRANCH ON FLTG REG LE.
 BFGT 141611 BR - 4 SRV BRANCH ON FAC .GT. Ø.
 BFLE 50120 BR - 4 I
                              BRANCH ON FLTG REG LT.
 BFLE 141610 BR - 4 SRV BRANCH ON FAC .LE. 0.
 BFLT 50124 BR - 4 I
                              BRANCH on FLTG REG GT.
 BFLT 141614 BR - 4 SRV BRANCH ON FAC .LT. Ø.
 BFNE 50123 BR - 4 I
                              BRANCH ON FLTG REG GE.
 BFNE 141613 BR - 4 SRV BRANCH ON FAC .NE. Ø.
 BGE 140615 BR - 4 SRV BRANCH ON A REGISTER .GE. 0.
 BGT 140611 BR - 4 SRV BRANCH ON A REGISTER .GT. 0.
 BHDl 50144 BR - - I
                              BRANCH ON HALF REG DEC BY 1.
 BHD2 50145 BR - - I
                              BRANCH ON HALF REG DEC BY 2.
 BHD4 50146 BR -- I
                              BRANCH ON HALF REG DEC BY 4.
 BHEQ 50112 BR - 4 I
                              BRANCH ON HALF REG EO.
 BHGT 50111 BR - 4 I
                              BRANCH ON HALF REG LE.
 BHI1 50140 BR -- I
                              BRANCH ON HALF REG INCR BY 1.
 BHI2 50141 BR -- I
                              BRANCH ON HALF REG INCR BY 2.
BHNE 50113 BR - 4 I BRANCH ON HALF REG GE.

BHNE 50105 BR - 4 I BRANCH ON HALF REG ME.

BIX 141334 BR - - SRV BRANCH ON INCREMENTED X.

BIY 141324 BR - - SRV BRANCH ON INCREMENTED Y.

BLE 140610 BR - 4 SRV BRANCH ON A REGISTER LE. 0.

BLEQ 140702 BR - 4 SRV BRANCH ON I REGISTER .EQ. 0.

BLIE 140700 BR - 4 SRV BRANCH ON I REGISTER .GE. 0.

BLIE 140700 BR - 4 SRV BRANCH ON I REGISTER .GE. 0.

BLIE 140700 BR - 4 SRV BRANCH ON I REGISTER .GE. 0.

BLIE 140700 BR - 4 SRV BRANCH ON I REGISTER .GE. 0.

BLIE 140700 BR - 4 SRV BRANCH ON I REGISTER .GE. 0.

BLIE 140700 BR - 4 SRV BRANCH ON I REGISTER .GE. 0.

BLIE 140700 BR - 4 SRV BRANCH ON I REGISTER .GE. 0.
 BLLE 140700 BR - 4 SRV BRANCH ON L REGISTER .LE. 0.
 BLLT 140614 BR - 4 SRV BRANCH ON L REGISTER .LT. 0.
 BLNE 140703 BR - 4 SRV BRANCH ON L REGISTER .NE. 0.
 BLR 141707 BR - - SRV BRANCH ON LINK RESET.
 BLS 141706 BR - - SRV BRANCH ON LINK SET.
BLT 140614 BR - 4 SRV BRANCH ON A REGISTER .LT. 0.
 BMEQ 141602 BR - - SRV BRANCH ON MAG.-COND. L,CC .EQ.
 BMGE 141706 BR - - SRV BRANCH ON MAG.-COND. L,CC .GE.
BMGT 141710 BR - - SRV BRANCH ON MAG.-COND. L,CC .GT.
BMLE 141711 BR - - SRV BRANCH ON MAG.-COND. L,CC .LE.
BMLT 141707 BR - - SRV BRANCH ON MAG.-COND. L,CC .LT.
BMNE 141603 BR - - SRV BRANCH ON MAG.-COND. L,CC .NE.
BNE 140613 BR - 4 SRV BRANCH ON A REGISTER .NE. 0.
BRBR 50040 BR - - I
                             BRANCH ON REG BIT RESET.
BRBS 50000 BR -- I
                             BRANCH ON REG BIT SET.
BGRl 50134 BR - - I
                             BRANCH ON REG DEC BY 1.
```

```
BRANCH ON REG DEC BY 2.
BGR2 50135 BR - - I
BGR4 50136 BR - - I
                        BRANCH ON REG DEC BY 4.
BREQ 50102 BR - 4 I
                       BRANCH ON REG EO.
BRGE 50105 BR - 4 I
                       BRANCH ON REG NE.
BRGT 50101 BR - 4 I
                        BRANCH ON REG LE.
BRI1 50130 BR -- I
                       BRANCH ON REG INCR BY 1.
BRI2 50131 BR - - I
                        BRANCH ON REG INCR BY 2.
BRI4 50132 BR -- I
                       BRANCH ON REG INCR BY 4.
                       BRANCH ON REGISTER LT.
BRLE 50100 BR - 4 I
BRLT 50104 BR - 4 I
                       BRANCH ON REGISTER GT.
BRNE 50103 BR - 4 I
                        BRANCH ON REGISTER GE.
```

#### Control Operations

```
ARGT 000605 CON - - SRV ARG TRANSFER (USED WITH PCL).
HIT 000000 CON - - SRVI*HALT COMPUTER OPERATION.
IRTC 000603 CON 7 6 SRVI*INTERRUPT RETURN, DO CAI.
IRTN 000601 CON 7 6 SRVI*INTERRUPT RETURN, NO CAI.
ITLB 000615 CON - - SRVI*INVAL STIB ENTRY, L = VADDR.
LPID 000617 CON - - SRVI*LOAD PROCESS ID FROM A01-A12.
PRTN 000611 CON 7 6 SRVI PROCEDURE RETURN.
SVC 000505 CON - - SRVI SUPERVISOR CALL.
```

#### Character String Operations

ZCM 001117 CS X X VI	COMP TWO CHAR STR FIELDS.
ZFIL Ø11116 CS X X VI	FILL CHAR STR FLD WITH CHAR.
ZMV 001114 CS X X VI	CPY FRM SOURCE FLD TO DEST FLD.
ZTRN 001110 CS X X VI	TRANS SRCE STR FLD TO DEST FLD.

```
XMV 001101 DA X X VI MOVE NUM SOURCE FLD TO DST FLD.
```

```
XED 001112 FE X X VI EDIT NUMERIC FIELD.
ZED 001111 FE X X VI EDIT CHAR STR FIELD.
```

#### Field Operations

```
ALFA 001301 FLD 6 5 SRV ADD L TO FIELD ADDR REG. ZERO.
      ALFA 001311 FLD 6 5 SRV ADD L TO FIELD ADDR REG. ONE.
      ARFA 50161 FLD - 7 I ADD GR TO FIELD ADDR REG.
      LDC 50162 FLD - 7 I
                                   LOAD CHAR TO GRH.
      LDC 001312 FLD - 7 SRV LOAD CHAR TO A REG PER FAR 1.
      LDC 001302 FLD - 7 SRV LOAD CHAR TO A REG VIA FAR 0.
STC 50166 FLD - 7 I STORE CHARACTER FROM GRH.
STC 001332 FLD - 7 SRV STORE CHAR FROM A (SEE FAR0).
STC 001322 FLD - 7 SRV STORE CHAR FROM A (SEE FAR0).
      TFLL 001333 FLD - - SRV XFER FLD LEN REG TO L REG 1.
```

```
TFLL 001323 FLD - - SRV XFER FLD LEN REG TO L REG 0.
TFLR 50163 FLD - 7 I
                       TRANSFER FLD LENGTH REG TO GR.
TLFL 001321 FLD - - SRV TRANS L TO FLD LEN REG 0.
TLFL 001331 FLD - - SRV TRANSFER L TO FLD LEN REG 1.
TRFL 50165 FLD - 7 I TRANSFER GR TO FLD LENGTH REG.
```

#### Floating-point Operations

#### Integrity Operations

#### Input/Output Operations

```
CAI 000411 IO -- SRVI*CLEAR ACTIVE INTERRUPT.
ENB 000401 IO - - SRVI*ENABLE INTERRUPTS.
INH 001001 IO -- SRVI*INHIBIT INTERRUPTS.
```

#### Logicize Operations

```
LCEQ 50153 LOG - - I IF .EQ., 1=>GRH, ELSE 0=>GRH.

LCEQ 141503 LOG - - SRV IF CC.EQ.,1=>A. ELSE 0=>A.

LCGE 50154 LOG - - I IF .GE.,1=>GRH, ELSE0=>GRH.

LCGE 141504 LOG - SRV IF CC.GE.,1=>A. ELSE 0=>A.

LCGT 50155 LOG - I IF .GT., 1=>GRH, ELSE 0=>GRH.
                                                                                                                                                                                                                                                                     LCGT 50155 LOG - - I IF .GT., 1=>GRH, ELSE 0=>GRH.
LCGT 141505 LOG - - SRV IF CC.GT.,1=>A. ELSE 0=>A.
                                                                                                                                                                                                                                                                      LCLE 50151 LOG - - I IF .LE., 1=>GRH, ELSE 0=>GRH.
                                                                                                                                                                                                                                                                      LHGT 50015 LOG - 4 I IF GRH>0 TN 1=>GRH,ELSE 0=>GRH.
                                                                                                                                                                                                                                                                      LHLE 50011 LOG - 4 I IF GRH<=0 T 1=>GRH,ELSE 0=>GRH.
                                                                                                                                                                                                                                                                      LHLT 50000 LOG - 4 I IF GR<0 TN 1=>GRH, ELSE 0=>GRH.
 LHLT 50000 LOG - 4 I IF GR<0 TN 1=>GRH, ELSE 0=>GRH.

CXCS 001714 IG - V CONTROL EXTENDED CONTROL STORE.

EMCM 000503 IG - SRVI*ENTER MACH CHK MODE.

LMCM 000501 IG - SRVI*LEAVE MACHINE CHECK MODE.

LMCM 000501 IG - SRVI*LEAVE MACHINE CHECK MODE.

LMCS 001710 IG - V LOAD WRITABLE CONTROL STORE.

MDEI 001304 IG - SRVI*MEM DIAG ENABLE INTERLEAVE.

MDII 001305 IG - SRVI*MEM DIAG ENABLE INTERLEAVE.

MDII 001305 IG - SRVI*MEM DIAG WRITISTENTER MODE.

MDIW CO13?? IG - SRVI*MEM DIAG WRITISTENTER MODE.

MDIW 001307 IG - SRVI*MEM DIAG READ SYNDROME BITS.

MDWC 001307 IG - SRVI*MEM DIAG LOAD WRITE CTL REG.

RMC 000021 IG - SRVI*RESET MACHINE CHECK FLAG.

VIRY 000311 IG 5 6 SRV* EXECUTE VERIFICATON ROUTINE.

LIHT 50000 LOG - 4 I IF GR<0 TN 1=>GRH, ELSE 0=>A.

LIHT 50000 LOG - 4 I IF GR<0 TN 1=>GRH, ELSE 0=>GRH.

LHLT 50000 LOG - 4 I IF GR<0 TN 1=>GRH, ELSE 0=>GRH.

LHLT 50000 LOG - 4 I IF GR<0 TN 1=>GRH, ELSE 0=>GRH.

LHLT 50000 LOG - 4 I IF GR<0 TN 1=>GRH, ELSE 0=>A.

LIHT 140410 LOG - 4 SRV IF L.L. 0, 1=>A. ELSE 0=>A.

LIHT 140410 LOG - 4 SRV IF A.L. 0, 1=>A. ELSE 0=>A.

LIHT 140410 LOG - 4 SRV IF A.L. 0, 1=>A. ELSE 0=>A.

LIHT 140410 LOG - 4 SRV IF A.L. 0, 1=>A. ELSE 0=>A.

LIHT 140410 LOG - 4 SRV IF A.L. 0, 1=>A. ELSE 0=>A.

LIHT 140410 LOG - 4 SRV IF A.L. 0, 1=>A. ELSE 0=>A.

LIHT 140410 LOG - 4 SRV IF A.L. 0, 1=>A. ELSE 0=>A.

LIHT 140410 LOG - 4 SRV IF A.L. 0, 1=>A. ELSE 0=>A.

LIHT 140410 LOG - 4 SRV IF A.L. 0, 1=>A. ELSE 0=>A.

LIHT 140410 LOG - 4 SRV IF A.L. 0, 1=>A. ELSE 0=>A.

LIHT 140410 LOG - 4 SRV IF A.L. 0, 1=>A. ELSE 0=>A.

LIHT 140410 LOG - 4 SRV IF A.L. 0, 1=>A. ELSE 0=>A.

LIHT 140410 LOG - 4 SRV IF A.L. 0, 1=>A. ELSE 0=>A.

LIHT 140410 LOG - 4 SRV IF A.L. 0, 1=>A. ELSE 0=>A.

LIHT 140410 LOG - 4 SRV IF A.L. 0, 1=>A. ELSE 0=>A.

LIHT 140410 LOG - 4 SRV IF A.L. 0, 1=>A. ELSE 0=>A.

LIHT 140410 LOG - 4 SRV IF A.L. 0, 1=>A. ELSE 0=>A.

LIHT 140410 LOG - 4 SRV IF A.L. 0, 1=>A. ELSE 0=>A.

LIHT 140410 LOG - 4 SRV IF A.L. 0, 1=>A. ELSE 0=>A.

LHT 140410 LOG - 4 SRV IF A.L. 0, 1=>A. ELSE 0=>A.

LHT 140410 LOG
                                                                                                                                                                                                                                                                      LNE 140412 LOG - 4 SRV IF A.NE.0, 1=>A. ELSE 0=>A.
                                                                                                                                                                                                                                                                     LT 140417 LOG - 5 SRV LOGICIZE TRUE. 1=>A.
```

#### Mode Operations

DBL 000007 MOD - - SRV ENTER DOUBLE-PREC MODE. E16S 000011 MOD - - SRV ENTER P300 16K SECTORED MODE.

```
E32I 001010 MOD - - SRV ENTER P500 321 MODE.
E32R 001013 MOD - - SRVI ENTER P300 32K RELATIVE MODE.
E32S 000013 MOD - - SRVI ENTER P300 32K SECTORED MODE.
E64R 001011 MOD - - SRVI ENTER P300 64K RELATIVE MODE.
E64V 000010 MOD - - SRVI ENTER P400 MODE.
ESIM 000415 MOD - - SRVI*ENTER STANDAGR INTERRUPT MODE.
EVIM 000417 MOD - - SRVI*ENTER VECTORED INTERUPT MODE.
SGL 000005 MOD - - SRV ENTER SINGLE-PRECISION MODE.
```

#### Memory-reference Operations

```
3 MCD - SINVI ENTER 3980 37K SECTORED MODE.

1 MCD - SINVI ENTER 3980 37K SECTORED MODE.

5 MCD - SINVI ENTER 3980 37K SECTORED MODE.

5 MCD - SINVI ENTER 8980 37K SECTORED MODE.

5 MCD - SINVI ENTER 8980 37K SECTORED MODE.

5 MCD - SINVI ENTER 8980 MODE.

7 MCD - SINVI ENTER 8980 MODE.

5 MCD - SINVI ENTER 8980 MODE.

6 MCD - SINVI ENTER 8980 MODE.

7 MCD - SINVI ENTER 8980 MODE.

7 MCD - SINVI ENTER 8980 MODE.

8 MCD - SINVI ENTER 8980 MODE.

9 MC - SINVI ENTER 89
ADD Ø6
ADL 06 03 MR 2 1 V
AH 12
ANA Ø3
ANL 03 03 MR -- V
С
               61
CAS 11
CH 71
CLS 11 03 MR 1 1 V
CREP 10 02 MR -- R
               62
DAD 06 (DP) MR 2 1 SR
DFA 15 17 MR 3 1 I
DFAD 06 02 MR 3 5 RV
DFC 05 07 MR - 1 I
DFCS 11 02 MR 6 5 RV
DFD 31,33 MR 3 1 I
DFDV 17 Ø2 MR 3 5 RV
DFL 01 03 MR -- I
DFLD 02 02 MR -- RV
DFLX 15 Ø2 MR -- V
DFM 25 27 MR 3 1 I
DFMP 16 Ø2 MR 3 5 RV
DFS 21 23 MR 3 1 I
DFSB Ø7 Ø2 MR 3 5 RV
DFST 11,13 MR 1 1 I
DFST 04 02 MR -- RV
DH 72
DIV 17
DIV 17
DLD 02 (DP) MR - - SR
DM 60
 DMH 70
             Ø7(DP) MR 2 1 SR
 DST Ø4 (DP) MR - - SR
 DVL 17 Ø3 MR 3 5 V
 EAA Ø1 Ø1 MR -- R
 EAL Ø1 Ø1 MR -- V
 EALB 42
 EALB 13 02 MR -- V
 EAR 63
 EAXB 52
 EAXB 12 02 MR -- V
 EIO 34
 EIO 14 01 MR - 7 V*
 ENTR 01 03 MR -- R
```

```
ERA Ø5
             MR -- SRV (A) .XOR. [EA]16 \Rightarrow A.
ERL Ø5 Ø3 MR -- V
                           (A,B) .XOR. [EA] 32 => A,B.
FA 14,16 MR 31 I
                           FLTG ADD. FR+[EA32]=>FR.
FAD 06 01 MR 3 5 RV
                           (FAC) + [EA]32 => FAC.
FC 04,06 MR - 1 I
                           FLTG COMPARE FR TO [EA32]
                           SKIP \emptyset,1,2 IF (FAC)>,=.<[EA]32.
                           FLTG DIV. -FR/[EA32]=>FR.
                           (FAC) / [EA] 32 => FAC.
                           FLOATING LOAD. [EA]32 => FAC.
                           2*[EA]16 => X.
                           FLTG MULT. FR*[EA32]=>FR.
                           (FAC) * [EA] 32 => FAC.
                           FLTG SUB. FR-[EA32]=>FR.
                           (FAC) - [EA]32 \Rightarrow FAC.
                           FLTG STORE. FR=>[EA32].
                           (FAC) => [EA]32.
                           INTERCHANGE R WITH [EA32].
                           INTERCHANGE RH WITH [EA16].
                           INCR. [EA32]+1=>[EA32].
             MR - - SRV EXCHANGE MEMORY AND A-REGISTER.
                           INCR HALD WD> [EA16]+1=>[EA16].
             MR - - SRV INC, REPLACE, AND SKIP IF ZERO.
                           JUMP ON DECREMENTED X-R ZERO.
                           IF (A) .EQ. \emptyset, EA => P.
                           IF (A) .GE. \emptyset, EA => P.
                          IF (A) .GT. \emptyset, EA => P.
                           JUMP ON INCREMENTED X-REG ZERO.
                          IF (A) .LE. \emptyset, EA => P.
                          IF (A) .LT. \emptyset, EA => P.
                           JUMP. EA=>RP.
             MR - - SRV UNCOND JUMP. EA => PB,P.
                           IF (A) .NE. \emptyset, EA => P.
                           RPL=>RH, EA32=>RP
             MR - - SRV (P) => [EA]16, EA+1 => P.
                          (P) => X, EA => P.
                           RP=>XB, EA=>RP.
                           (PB,P) \Rightarrow XB, EA \Rightarrow PB,P.
                           (P) \Rightarrow Y, EA \Rightarrow P.
                           LOAD. [EA32] = R.
            MR - - SRV LOAD A-REGISTER. [EA]16 => A.
                           LOAD ADDR REGISTER.
                           LOAD LONG. [EA] 32 \Rightarrow A_B.
                           LOAD LONG FROM RFILE LOC EA.
             MR - - SRV LOAD X-REGISTER. [EA]16 \Rightarrow X.
                           LOAD Y-REGISTER. [EA]16 => Y.
                           LOAD HALF WD. [EA16]=>RH.
                           SHIFTED 1 [EA16] .LS. 1=>GRH.
                           SHIFTED 2 [EA16] .LS. 2=>GRH.
                           MULT. R*[EA32] => (R,R+1).
                           RH*[EA16]=>R.
                           MICROCODE EXECUTE A.
                           MICROCODE ENTRANCE.
                           MICROCODE EXECUTE B.
                           MICROCODE ENTRANCE.
                           (A,B) * [EA]16 \Rightarrow A,B. (NOHOLE)
                           (A) * [EA]16 => A,B. (NOHOLE).
            MR 3 1 SR (A) * [EA]16 \Rightarrow A_B. (HOLE).
```

n Ø3	MR I	R.AND. $[EA32] = > R$ .	CSR 5	0041 OPR I	COPY & SAVE SIGN. $1=>C,\emptyset=>GR1$ .
NH 13	MR I	RH .AND. [EA16]=>RH.	DH1 50	0130 OPR 2 1 I	DECR HALF REG BY 1. GRH-1=>GRH.
O 23	MR I	$R \cdot OR \cdot [EA32] = > R \cdot$	DH2 50	Ø131 OPR 2 1 I	DECR HALF REG BY 2. GRH-2=>GRH.
OH 33	MR I	RH .OR. [EA16]=>RH.	DR1 50	Ø124 OPR 2 1 I	DECR REG BY 1. GR-1=>GR.
ORA Ø3 Ø2	MR V	(A) $.OR. [EA]16 \Rightarrow A.$	DR2 50	0125 OPR 2 1 I	DECR REG BY 2. GR-2=>GR.
PCL 41	MR I	PROCEDURE CALL.	DRX 149	0210 OPR SRV	DECREMENT X AND SKIP IF 0.
PCL 10 02	MR 76 V	P-400 PROCEDURE CALL.	IAB 000	0201 OPR SRV	EXCHANGE A AND B A=>B & B=>A.
s 22	MR 211	SUB. R-[EA32]=>R.	TCA 14	1340 OPR SRV	INTERCHANGE BYTES OF A REG.
SBL 07 03	MR 21V	$(A,B) - [EA] 32 \Rightarrow A,B. (NOHOLE)$	ICBL 50	0065 OPR I	GRH $(1-8) = >$ GRH $(9-16)$ , $\emptyset = >$ R.
SH 32	MR 21I	SUB HALF WD. RH-[EA16]=>RH.	ICBR 5	0066 OPR I	GRH $(9-16) = >$ GRH $(1-8)$ , $\emptyset = >$
SHA 15	MR 4 - I	ARITHMETIC SHIFT.	TCHL 50	0060 OPR I	GRH=>GRL, Ø=>GRH.
SHL Ø5	MR 4 - I	LOGICAL SHIFT.	TCHR 50	0061 OPR I	GRL=>GRH, Ø=>GRL.
ST 21	MR I	STORE, R=>[EA32].	ICI. 14	1140 OPR SRV	EXCHANGE BYTES OF A, CLR LEFT.
STA 04	MR SRV	STORE A-REG. (A) $\Rightarrow$ [EA]16.	TCD 14	1240 OPR SRV	EXCHANGE BYTES OF A, CLR RIGHT.
STAR 54	MR I	STORE ADDRESSED REGISTER.	IHI 50	0126 OPR 2 1 I	GRH+1=>GRH.
STH 31	MR I	STORE HALF WD RH=>[FA]6]	IH2 50	Ø120 OPR 2 1 I	GRH+2=>GRH.
STL 04 03	MR V	STORE LONG (A B) => $[EA]32$	TUZ 31	1414 OPR SRV	
STLR 03 01	MR V	CHOOL TONG. (A'D) > [TI] 22.	11.C 14.		EXCHANGE L AND E. L=>E & E=>L.
	MD CD7	STORE X-REG. (X) => [EA]16.	INK 50	0070 OPR I	MOVE KEYS TO REG KEYS.
STX 15	MR SRV	CMODE V-DEC (V) => [EA]16	INK 000	0043 OPR SRV	INPUT P-300 KEYS INTO A REG.
	MR V	SIORE 1-REG. (1) -> [EA]16 -> $\lambda$	IR1 50	Ø122 OPR 2 1 I	INCR REG BY 1. GR+1=>GR.
SUB Ø7	MR 21 SRV	SUBTRACT. (A) $-$ [EA]16 $\Rightarrow$ A.	IR2 50	Ø123 OPR 2 1 I	INCR REG BY 2. GR+2=>GR.
TM 44	MR - 1 I	REST MEM. [EA32]::0-/CC.	IRB 50	0062 OPR I	BYTES $GRH(1-8) = > GRH(9-16)$
X 43	MR I	EXC UR. R .XUR. [EA32]-/R.	IRH 50	0057 OPR I	GRH=>GRL, GRL=>GRH.
	MR RV	EXECUTE INSTRUCTION AT EA.	IRX 140	0114 OPR SRV	INCREMENT X AND SKIP IF 0.
XEC Ø1 Ø2	MR RV	EX EFF ADDR CONT AS THIS INST.	NOP 000	0001 OPR SRV	NO OPERATION.
XH 52	MR I	RH .XOR. $[EA16] = > RH$ .	NRM ØØ6	0101 OPR SRV	NORMALIZE A,B AS ON P-300.
ZM 43	MR I	ZERO MEM. $\emptyset = > [EA32]$ .	OTK 50	0071 OPR 7 S I	SET KEYS FROM REGISTER
zmH 53	MR I	ZERO MEM. HALF WD. $\emptyset = > [EA16]$ .	OTK ØØ	0405 OPR 7 6 SRV	OUTPUT A TO 300 KEYS, SHFT CTR.
		R .AND. [EA32]=>R. RH .AND. [EA32]=>R. R .OR. [EA32]=>R. RH .OR. [EA16]=>RH. (A) .OR. [EA]16 => A. PROCEDURE CALL. P-400 PROCEDURE CALL. SUB. R-[EA32]=>R. (A,B) - [EA]32 => A,B. (NOHOLE) SUB HALF WD. RH-[EA16]=>RH. ARITHMETIC SHIFT. LOGICAL SHIFT. STORE. R=>[EA32]. STORE A-REG. (A) => [EA]16. STORE ADDRESSED REGISTER. STORE HALF WD. RH=>[EA16]. STORE LONG. (A,B) => [EA]32. STORE LONG INTO RFILE LOC EA. STORE Y-REG. (Y) => [EA]16. SUBTRACT. (A) - [EA]16 => A. REST MEM. [EA32]::0=>CC. EXC OR. R .XOR. [EA32]=>R. EXECUTE INSTRUCTION AT EA. EX EFF ADDR CONT AS THIS INST. RH .XOR. [EA16]=>RH. ZERO MEM. 0=>[EA32]. ZERO MEM. HALF WD. 0=>[EA16].	PID 50	0052 OPR I	GR=>GRN, (1)=> $GR$ (2-32).
Miscellane	ous Operations		PID 000	0211 OPR SRV	SHORT INT TO DP INT W/HOLE.
			PIDA 000	0115 OPR SRV	SHORT TO LONG INT CONV. A=>L.
AlA 14120	6 OPR 2 1 SRV	ADD 1 TO A REG. A+1=>A. ADD 2 TO A REGISTER. A+2=>A.	PIDH 50	0053 OPR I	GRH=>GRL,GRH(1)=>GRH(2-16).
A2A 14030	4 OPR 2 1 SRV	ADD 2 TO A REGISTER. A+2=>A.	PIDL 000	0305 OPR SRV	CONVERT LONG INT TO 64 BIT INT.
ABO 5013	4 OPR - 7 I	ADD TO BOTTOM OF QUEUE.	PIM 50	0050 OPR 2 1 I	POS REG AFTER INT MULTIPLY.
ACA 14121	6 OPR 2 1 SRV	ADD CBIT TO A REG. CBIT+A=>A.	PIM 000	0205 OPR SRV	DP INT WITH HOLE TO SHORT INT.
ADLL 14100	Ø OPR 2 1 SRV	ADD 2 TO A REGISTER. A+2=>A. ADD TO BOTTOM OF QUEUE. ADD CBIT TO A REG. CBIT+A=>A. ADD LINK TO L REGISTER. ADD LINK TO GR. ADD TO TOP OF QUEUE. CLEAR A REG. LEFT BYTE. CLEAR A REG. RIGHT BYTE. SKIP Ø,1,2 INST. IF A >,=,< Ø. A AS FA=>A (USELESS IN 64V).	PIMA 000	0015 OPR 3 5 SRV	L=>A. IEX ON PREC LOSS.
ADLR 5001	4 OPR - 7 I	ADD LINK TO GR.	PIMH 50	0051 OPR 2 1 I	POS HALF REG AFTER INT MULT.
ATQ 5013	5 OPR - 7 I	ADD TO TOP OF QUEUE.	PIML 000	0301 OPR 3 5 SRV	64BIT INT TO LONG INT. (L,E)=>L.
CAL 14105	Ø OPR SRV	CLEAR A REG. LEFT BYTE.	RBQ 56	0133 OPR - 7 I	REMOVE FROM BOTTOM OF QUEUE.
CAR 14104	4 OPR SRV	CLEAR A REG. RIGHT BYTE.	RCB 146	0200 OPR 5 - SRV	RESET CBIT. Ø=>CBIT.
CAZ 14021	4 OPR 1 1 SRV	SKIP $\emptyset.1.2$ INST. IF A >.=.< $\emptyset$ .	RTN 000	0105 OPR SRV	RETURN FROM P-300 RECUR PROC.
CEA 00011	1 OPR SRV	A AS EA=>A. (USELESS IN 64V).	RTQ 56	0132 OPR - 7 I	REMOVE FROM TOP OF QUEUE.
CGT 5002	6 ODD - 7 T	COMPUTED COTO	C17 146	0110 OPR 2 1 SRV	SUB 1 FROM A REGISTER. A-1=>A.
CHS 5004	Ø OPR T	COMPUTED GOTO.  CHANGE SIGN - (1) =>GR(1).  CHANGE SIGN OF A REGISTER.  ONE'S COMPLEMENT A REGISTER.  COMP HALF REG. GRH=>GRH.  COMP REG. GR=>GR.  CLEAR REG. Ø => GR.	S2V 146	0310 OPR 2 1 SRV	SUB 2 FROM A REGISTER. A-1->A.
CHS 14002	1 ODD CD11	CHANGE SIGN OF A REGISTER.	SCA MAG	0010 OFR 2 1 SRV 0041 OPR SRV	LOAD P-300 SHFT CTR INTO A REG.
CMN 14002	1 OFK = - SKV	ONE COMPLEMENT A RECISTER	SCB 146		
CMA 14040	1 OFK 5KV	COMD HAIR DEC CDH=/CDH	CD 148	0600 OPR 5 - SRV	SET CBIT. 1=>CBIT.
CMH 5004	7 ODD 1	COMP DEC CD=\CD	SULT OF	0076 OPR 4 1 I	GRH .LS. 1 => GRH.
CMR 5004	4 OPR I	CLEAD DEC A -> CD	SHL2 50	0077 OPR 4 1 I	GRH .LS. 2 => GRH.
CR 5005	0 OFK I	CLEAR REG. V = 2 GR.	SHRl 50	0120 OPR 4 1 I	GRH .RS. 1 => GRH.

CRA 140040 OPR - - SRV CLEAR A REGISTER.  $\emptyset = > A$ .

CRB 140015 OPR - - SRV CLEAR B REGISTER. 0=>B.

CRL 140010 OPR - - SRV CLEAR L REGISTER. 0=>L.

CRLE 141410 OPR - - SRV CLEAR L AND E.  $\emptyset = >L$ ,  $\emptyset = >E$ .

CSA 140320 OPR 5 - SRV CPY SIGN OF A. Al=>CBIT, 0=>Al.

LEFT BYTE  $\emptyset = > GRH(1-8)$ .

CLEAR E.  $\emptyset = > E$ .

RIGHT BYTE  $\emptyset = > GRH(9-16)$ .

CRBL 50062 OPR - - I

CRBR 50063 OPR - - I

CRE 141404 OPR - - SRV

SHR2

 $\operatorname{SLl}$ 

SL2

SRl

SR2

SSM

SSM

SSP

50121 OPR 4 1 I

50072 OPR 4 1 I

50073 OPR 4 1 I

50074 OPR 4 1 I

50075 OPR 4 1 I

50042 OPR - - I

50043 OPR - - I

GRH .RS. 2 => GRH.

GR .LS. 1 => GR.

 $GR .LS. 2 \Rightarrow GR.$ 

 $GR .RS. 1 \Rightarrow GR.$ 

 $GR .RS. 2 \Rightarrow GR.$ 

140500 OPR - - SRV SET SIGN OF A MINUS. 1=>A1.

SET SIGN. MINUS  $1 \Rightarrow (1)$ .

SET SIGN. PLUS  $\emptyset => (1)$ .

```
SSP 140100 OPR - - SRV SET SIGN OF A PLUS. 0=>ABIT1.
STCD 50137 OPR - 7 I GR+1=[EA16].
STCH 50136 OPR - 7 I
                       GRL=[EA16].
STEX 50027 OPR - 7 I
                       STACK EXTEND.
STEX 001315 OPR 6 5 SRV STK EXTEND. L REG HAS EXTENT.
TAB 140314 OPR - - SRV TRANSFER A TO B REG. A=>B.
TAK 001015 OPR 7 6 SRV TRANSFER A TO KEYS.
TAX 140504 OPR - - SRV TRANS A REG TO X REG. A=>X.
TAY 140505 OPR - - SRV TRANS A REG TO Y REG. A=>Y.
TBA 140604 OPR - - SRV TRANS B REG TO A REG. B=>A.
TC 50047 OPR 3 1 I
                       -GR+1=>GR.
TCA 140407 OPR 2 1 SRV TWO'S COMPLEMENT A. -A=>A.
TCH 50047 OPR 3 1 I -GRH+1=>GRH.
TCL 141210 OPR 2 1 SRV TWO'S COMPLEMENT L. -L=>L.
TKA 001005 OPR - - SRV TRANSFER KEYS TO A.
TSTO 50104 OPR - 7 I
                       TEST OUEUE.
TXA 141034 OPR - - SRV TRANS X REG TO A REG. X=>A.
TYA 141124 OPR - - SRV TRANS Y REG TO A REG. Y=>A.
XCA 140104 OPR - - SRV EXCHG AND CLR A. A=>B, \emptyset=>A.
XCB 140204 OPR - - SRV EXCHG AND CLR B. B=>A, \emptyset=>B.
xxx 140014 OPR - - SRV OBSOLETE. CLRS B, LSW OF DFAC.
```

#### Programmed I/O Operations

```
INA 54 PIO - - SR* INPUT TO A-REGISTER.

OCP 14 PIO - - SR* OUTPUT CONTROL PULSE.

OTA 74 PIO - - SR* OUTPUT FROM A-REGISTER.

SKS 34 PIO - - SR SKIP IF CONDITION SET.

SMK 170020 PIO - - SR* SET INTERRUPT MASKS.
```

#### Shift Operations

```
ALL 0414XX SH 4 5 SRV A LEFT LOGICAL.

ALR 0416XX SH 4 5 SRV A LEFT ROTATE.

ALS 0415XX SH 4 5 SRV A LEFT SHIFT (SHORT INT ARITH).

ARL 0404XX SH 4 5 SRV A RIGHT LOGICAL.

ARR 0406XX SH 4 5 SRV A RIGHT ROTATE.

ARS 0405XX SH 4 5 SRV A RIGHT SHIFT (SHORT ARITH).

LLL 0410XX SH 4 5 SRV LONG LEFT LOGICAL.

LLR 0412XX SH 4 5 SRV LONG LEFT ROTATE.

LLS 0411XX SH 4 5 SRV LONG RIGHT LOGICAL.

LRL 0400XX SH 4 5 SRV LONG RIGHT LOGICAL.

LRR 0402XX SH 4 5 SRV LONG RIGHT LOGICAL.

LRR 0402XX SH 4 5 SRV LONG RIGHT LOGICAL.

LRS 0401XX SH 4 5 SRV LONG RIGHT ROTATE.

LRS 0401XX SH 4 5 SRV LONG RIGHT LOGICAL.

LRS 0401XX SH 4 5 SRV LONG RIGHT ROTATE.
```

#### Skip Operations

```
SAR 10026X SKP - - SRV SKIP IF A REG. BIT N RESET.

SAS 101260 SKP - - SRV SKIP IF A REG. BIT N SET.

SCT 100220 SKP - - SRV SKIP IF A REG. .GT. 0.

SKP 100000 SKP - - SRV SKIP ONE WORD.

SLE 101220 SKP - - SRV SKIP IF A REG. .LE. 0.

SIN 101100 SKP - - SRV SKIP IF A REG. BIT 16 SET.

SLZ 100100 SKP - - SRV SKIP IF A REG. BIT 16 .EQ. 0.

SMCR 100200 SKP - - SRV SKIP IF MACHINE CHECK RESET.

SMCS 101200 SKP - - SRV SKIP IF MACHINE CHECK SET.

SMI 101400 SKP - - SRV SKIP IF A REG. .LT. 0.
```

```
SNR 10024X SKP - - SRV* SKIP IF SENSE SWITCH N RESET.
SNS 101240 SKP - - SRV* SKIP IF SENSE SWITCH N SET.
SNZ 101040 SKP - - SRV SKIP IF A REG. NE. 0.
SPL 100400 SKP - - SRV SKIP IF A REG. .GE. 0.
SR1 100020 SKP - - SRV* SKIP IF SENSE SWITCH 1 RESET.
SR2 100010 SKP - - SRV* SKIP IF SENSE SWITCH 2 RESET.
SR3 100004 SKP - - SRV* SKIP IF SENSE SWITCH 3 RESET.
SR4 100002 SKP - - SRV* SKIP IF SENSE SWITCH 4 RESET.
SRC 100001 SKP - - SRV SKIP IF CBIT RESET.
SSI 101020 SKP - - SRV* SKIP IF SENSE SWITCH 1 SET.
SS2 101010 SKP - - SRV* SKIP IF SENSE SWITCH 2 SET.
SS3 101004 SKP - - SRV* SKIP IF SENSE SWITCH 3 SET.
SS4 101002 SKP - - SRV* SKIP IF SENSE SWITCH 4 SET.
SSC 101001 SKP - - SRV SKIP IF CBIT SET.
SSR 100036 SKP - - SRV* SKIP IF SSWI 1,2,3 AND 4 RESET.
SSS 101036 SKP - - SRV* SKIP IF SSWI 1,2,3 AND 4 SET.
SZE 100040 SKP - - SRV SKIP IF A REG .EQ. 0.
```

#### Virtual-memory Operations

```
EPMJ 000217 VM - - SR ENT PAGE MODE AND JUMP (P300).

EPMX 000237 VM - - SR ENT PAG MOD & JMP TO MICROCODE.

ERMJ 000701 VM - - SR ENTER RESTR MODE & JUMP (P300).

ERMX 000721 VM - - SR RESTR MOD & JUMP TO MICROCODE.

EVMJ 000703 VM - - SR ENT VIRT MODE AND JUMP (P300).

EVMX 000723 VM - - SR VIRT MOD & JUMP TO MICROCODE.

LPMJ 000215 VM - - SR LEAVE PAGE MODE & JUMP (P300).

LPMX 000235 VM - - SR LVE PAG MOD & JMP TO MICROCODE.
```

#### 6 OPERATIONAL PROCEDURES

#### BOOT PROCEDURES

Master clear, select LOAD, raise data switches 1-16
as follows ('-' => don't care):

```
16
A AAA AAA AAA --- 000 Start at 'AAAAAAAAAA000000
|S|SSS|SSS|DDP|---|001| ASR Paper Tape
|S|SSS|SSS|DDP|---|010| High Speed Paper Tape
|H|HHH|HHH|H--|--0|011| Option B FHD
|H|HHH|HHH|H--|-01|011| Option B' FHD, DA='21
|H|HHH|HHH|H--|-11|011| Option B' FHD, DA='23
|H|HHH|HHH|H--|0-0|100| Option B Upper MHD
|H|HHH|HHH|H--|1-0|100| Option B Lower MHD
|H|HHH|HHH|H-0|001|100| Option B' Upper MHD, DA='21
|H|HHH|HHH|H-0|101|100| Option B' Lower MHD, DA='21
|H|HHH|HHH|H-0|011|100| Option B' Upper MHD, DA='23
|H|HHH|HHH|H-0|111|100| Option B' Lower MHD, DA='23
|H|HHH|HHH|H-1|-01|100| Storage Module, DA='26
|H|HHH|HHH|H-1|-11|100| Storage Module, DA='27
|N|NNN|NNN|RRS|CT-|101| Magtape
|-|---|---|110| Diskette (Floppy)
|-|---|---|111| Unused
```

- A..A Addr/'100 to start at
- S..S Sector for boot loader relocation
- DD Displacement for boot loader relocation
- P 1 => suppress auto-start
- C 1 => halt to allow baud rate change
- H..H DOS select:
  - 00000000 Highest that will fit
    - Ø10---- \*DOS16
    - Ø11---- \*DOS64
    - 100---- \*DOS32
- N..N File number to load (0=>prompts)
- RR Relocation of boot program to ending address of:
  - 00 end of physical memory
  - Ø1 16K
  - 10 32K
  - 11 48K
- T Ø 9-track
  - 1 7-track

#### BOOT TERMINAL SPEED SELECTION

Parameter 4	(B Register
'110 for	110 BAUD
'1010 for	300 BAUD
'2010 for	1200 BAUD
'3410 for	9600 BAUD

Parameter 5	(X Register)
'27 for	110 BAUD
'76 for	300 BAUD
'373 for	1200 BAUD
'3735 for	9600 BAUD

```
Parameter 6 (Keys)

'740** for 110 BAUD

'340** for 1200 - 9600 BAUD
```

\*\* = number of delays after .CR.

#### TYPICAL SWITCH SETTINGS FOR DISK BOOTS

DVNO	Switches
Ø	0004
1	0044
10	0003
20	0006
3Ø	0014
31	0054
40	0014
<del></del> -050	0014
Ø51	0054
<del></del> 25Ø	0034
251	0074
460	0114

Hit START to initiate load sequence. Select RUN after load has started.

#### COLD START (PRIMOS IV,V)

- Boot in PRIMOS II (see 'BOOT' above), startup disk containing UFD with PRIMOS IV (typically PR4.64, PR4.16, PR4L16). Attach to UFD containing PRIMOS IV.
- 2) Type 'R PRIMOS'. If there is a C\_PRMO file in the current CMDNCØ the configuration will be taken from that file. Else enter CONFIG command:

#### CONFIG -- SET SYSTEM CONFIGURATION PARAMETERS

CONFIG [<node>] <ntusr> <pagdev> <comdev>
[<memsiz>] [<altdev>] [<namlc>] [<npusr>]
[<nrusr>] [<slmcon>]

-or-

CONFIG -DATA <config-filename>

Commands for latter form documented in PE-T-412.

Cold start command only.

- 3) If PRIMOS IV halts at 1507 (1510 in address lights), it has encountered bad memory during initial memory scan (see HALTS). Hit start to map out the bad page and continue.
- 4) After the introductory messages, enter the date and time:

SEtime -<mmddyy> -<hhmm>

Users may now log into the system.

#### ON MACHINE HALT:

HALTS

1) Select STOP/STEP mode (rotary switch).

- 2) Place ADDRESS/DATA switch on ADDRESS and note the address displayed in lights. To determine the segment number of the halt, select FETCH, depress DATA CLEAR, set data switches to '14, raise switches 1 and 4, depress START, note displayed segment number.
- 3) Refer to latest load map of PRIMOS IV and/or the following list of load map entries to determine subsequent actions. (NOTE: the address displayed at the halt will always be one location higher than the corresponding halt location address.)

#### PRIMOS IV HALT LOCATIONS

(Addresses marked with a '+' are those most likely to change when PRIMOS is reloaded. The letters in parentheses refer to subsequent actions that should be taken and are described following the list.)

AMICI +6/12714 Bad AMIC Interrupt.	(D,W)
BDMEM $4/1507$ Parity error during cold start.	(M,C)
BOOT0 +6/10634 Halt after SHUTDN ALL command.	(C)
IFLTB <u>+4/115204</u> Fault in interrupt handler.	(D,C)
INTRT +4/115357 Too many PRTNs.	(D,C)
IPAGE <u>+4/115321</u> Page flt in interrupt process.	(D,C)
MCHK $4/305$ Machine check.	(D,W)
$MEMH\overline{2}$ 4/317 Halt after mapout of bad page.	(W*)
MEMPA_ 4/276 Uncorrected mem parity error.	(M,W*)
MEMPA_ 4/276 Uncorrected mem parity error.  MMOD_ 4/315 Missing memory module.	(M,W*) (D,C)
MMOD_ 4/315 Missing memory module.	(D,C)
MMOD 4/315 Missing memory module. PAGFB 6/3577 Illegal page fault.	(D,C) (D,C)
MMOD_ 4/315 Missing memory module. PAGFB_ 6/3577 Illegal page fault. REFLØ_ 6/3663 Illegal FLEX, UII, PSU.	(D,C) (D,C) (D,C)
MMOD 4/315 Missing memory module.  PAGFB 6/3577 Illegal page fault.  REFLØ 6/3663 Illegal FLEX, UII, PSU.  RMCFØ 6/3545 Illegal restricted mode fault.	(D,C) (D,C) (D,C) (D,C)

#### ACTIONS TO TAKE AFTER HALT CONDITION IS DETERMINED

- D Note down register set (if RSAVPTR.NE.0) and take a tape dump.
- C -- Cold start.
- W -- Warm start.
- W\* -- Warm start is possible only if a <u>user</u> page got the parity error.
- M -- Map out bad page.

#### MEMORY PARITY ERRORS

On halt (at MEMPA\_): X = user number, A = physical page number, B = word number. Hit start to automatically map out bad page.

c(MMAP+PPN) --> HMAP for page ( $\leq$  '6200 => supervisor page).

c(PTUSEG+(PHMAP.RS.6-'40)=user number

Manual mapout: MMAP+PPN<--- 1, HMAP <-- 0

#### MEMORY/REGISTER DISPLAY

- 1) Select FETCH Y on rotary switch (this stops the machine).
- Select ADDRESS on ADDRESS/DATA toggle, hit DATA CLEAR, and depress address or register number in switches as shown below.
- After switches depressed/set, select DATA and depress START. The lights now show contents of selected location on register.

For memory references, raised switches are in top row, dialed switches in bottom row.  $^{\prime}\mathrm{H/L^{\prime}}$  selects high (raised) or low (middle position) side of register.

CURR. REG. SET:  $\frac{1}{|100\text{H/L}|000|000|0\text{RR}|RRR}}$ REG. #

Virtual addresses (but not absolute memory or registers) can be displayed while PRIMOS IV is running. Place ADDRESS/DATA toggle on DATA and enter segment number/word number as follows ('D' => depress switch):

DEPRESS SEGNO: | 1 5 16 | | D | 000 | DDD | DDD | DDD | DDD | DDD | C---SEG NUM--->

NOTE: If referenced page is not in memory, zeroes will be displayed (i.e., page faults are ignored).

#### MEMORY SCAN

- 1) Master clear, load '777 into the PC (location 7), select RUN, place ADDRESS/DATA switch on DATA, data switches in neutral position, hit START. (PRIMOS IV must be in memory.)
- 2) When the data lights change, a bad memory location has been found. The display with all switches neutral is the word number within the page. To display the physical page number, raise switch 15. To display the contents of the location, raise switch 14 (drop switch 15).
- 3) To continue the memory scan, type any character on the system console (ASR).
- 4) The scan will halt when the end of memory is reached. Hitting START will restart the scan.

#### TAPE DUMP

- Mount non-write-protected tape on first magtape controller, drive 1. Ensure only one unit dialed to 1. Tape should be at load point and online. (PRIMOS IV or V must be in memory.)
- 2) Master clear, set PC (location 7) from '1000 to '776, select RUN, hit START.
- 3) When done, the tape dump program will rewind the tape and halt. Perform WARM or COLD START as appropriate.

### WARM START

Master clear, select RUN, hit START twice.

NOTE: Warm start is possible only on CPUs with REV 10 microcode or later, otherwise an immediate halt at WARMH $\_$  will occur.

## 7 PERIPHERAL I/O

### ADDRESSES

00	Polling	40	PRIMAD (AIS)
Ø1	Paper Tape Reader	41	Digital Input 1
Ø2	Paper Tape Punch	42	Digital Input 2
Ø3	Unit Record Controller 1	43	Digital Output 1
04	TTY	44	Digital Output 2
Ø5	Unit Record Controller 2	45	Analog Output
Ø6	Interproc. Channel (IPC)	46	Computer Prod. IF
Ø7		47	CAMAC Interface
10		5Ø	HSSMLC 1
11	<del></del>	51	HSSMLC 2
12	Diskette	52	AMLC 3
13	Magtape Controller 2	53	AMLC 2
14	Magtape Controller 1	54	AMLC 1
15	RIOX I/O Bus Switch	55	MACI Autocall
16	RIOX MPS	56	SMLC
17		57	
20	Panel, Real Time Clock	60	Gen. Purp. IF Board
21	Disk (4002 Controller)	61	Ringnet Controller
22	Fixed Head Disk	62	GPIB
23	30 Megabyte Disk	63	GPIB
24	Writeable Control Store	64	GPIB
25	Moveable Head Disk	65	GPIB
26	Storage Module 1	66	GPIB
27	Storage Module 2	67	GPIB
30	IOC l (Parallel I/O)	7Ø	GPIB Test
31	ICC 2	71	ADAGE GP/400 IF
32		72	
33	VERSATEC	73	
34	VERSATEC	74	
35	AMLC 4	75	
36	ELFBUS Controller 1	76	
37	ELFBUS Controller 2	77	I/O Bus Test

## AMLC

OTA	01	 Set	Line	Config	uration

	004000 002000	Line Number Unused Data Set Control Loop Line
8-10	000700	
		xxx1xx - 134.5
		xxx2xx - 300
		xxx3xx - 1200
		xxx4xx - Pgmed Clock
		xxx5xx - Pgmed Clock
		xxx6xx - Pgmed Clock
		xxx7xx - Pgmed Clock
11	000040	Unused
12	000020	$\emptyset \Rightarrow 1$ Stop Bit, $1 \Rightarrow 2$
13	000010	l => Disable Parity
14	000004	$\emptyset \Rightarrow Odd Parity, 1 \Rightarrow Even$
15-16	000003	Char Len: xxxxx0 - 5 Bits
		xxxxxl - 7 Bits
		xxxxx2 - 6 Bits
		xxxxx3 - 8 Bits

## OTA 02 -- Set Line Control

1-4	170000	Line Number
5 <b>-</b> 1Ø	007700	Unused
11	000040	1 => Enable Char Time Interrupt
12	000020	Unused
13	000010	l => Enable Transmit
14	000004	l => Enable Echo Back
15	000002	<pre>1 =&gt; Receive Off, Report Break</pre>
16	000001	1 => Enable Receive

## ASR

BAUD	OPTION-A	SOC CTL 1	SOC CTL 2
110	110	27	740**
300	1010	76	340**
1200	2010	373	340**
9600	3410	3735	340**

\*\* = number of delays used by BOOT, PRIMOS

#### DISK CONTROLLERS

#### Disk Channel Program Definitions

	Οp	Executio Time	n		
Mnem	<u>Code</u>	(u-s)	Order	<u>Fields</u>	
DHLT SFORM	Ø 2	6	Halt Format	Rec Size Track Addr # Records Head Addr	13-16 23-32 33-40 44-48
SSEEK	3	7.5	Seek	Restore Clear Track Addr	17 18 23-32
DSEL SREAD	<b>4</b> 5	7.5	Select Read	MHD Rec Size Offset SR Track Addr Rec Addr Head Addr	29-32 13-16 17-20 21 23-32 33-40 44-48
SWRITE	6			Rec Size Track Addr Rec Addr Head Addr	13-16 23-32 33-40 44-48
DSTALL	. 7	210	Stall		
DSTAT	9	9	Input Status	Mem Addr	17-32
SSTOR	A	9	Store	Diag Addr Mem Addr	16 17-32
DOAR	В	9	Input OAR	Mem Addr	16
SLOAD	С	9	Load	Diag Addr Mem Addr	16 17-32
SDMA	D	6	Channel Address	Chain Chan Addr	13-16 17-32
DINT DTRAN	E F	6+CPU 6	Interrupt Transfer	Vect Addr Trans Addr	17-32 17-32

### 1 4|5 10 | op | | | ... | code | mask |

bit  $5 = \emptyset$ , do not execute inst if: bit 5 = 1, execute inst if:

### <u>set</u> bit

PERIPHERAL IO

- No function but reserved for "selected diskfile is write protected."
- 6 Last read or write record inst caused a DMA overrun, check error, controller parity error or header check failure (status word bits 2,4,5, or 6 set).
- 8 Selected MHD is seeking.
- 9 Selected diskfile has an error condition (status word bits 14, 15, or 16 are set).
- 10 For dual port operation only. Selected
   diskfile is busy servicing the "other"
   controller.

#### Disk Device Numbers (DVNO)

1-4	170000	(Offset to First Head)/2
5-8	007400	(Number of Heads)/2
9	000200	Ø=>Controller 1, 1=>Controller 2
10-13	000170	Type of Controller:
		xxx00x 4000 MHD
		xxx01x 4000 FHD
		xxx02x Diskette
		xxx03x 4003 8 Sectors/Track
		xxx04x 4003 FHD
		xxx05x 4003 32 Sectors/Track
		xxx06x 4004 Storage Module
		xxx07x-xxx17x Undefined
14-15	000006	Unit (Inc. bit 16 for Diskette)
16	000001	Diskette: Low Bit of Unit
		4003 Controller: 0 => Top, 1 => Bottom
		Storage Module: LSB of Number Heads

### Disk Errors

### Option B (4000 Controller)

177777	Bad Record Identifier
177776	Device Not Ready
100000	Data Transfer Complete
040000	R/W Past End of Record
020000	Unused
010000	Stack Available
004000	Seek Complete OK
002000	Write Protect Violation
001000	Not Ready
000400	Command Error
000200	Checksum Error
000100	DMX Overrun
000040	Stack Overflow
000036	Unused
000001	Not Ready (Software)
000000	Redundant Int. (Warm Start)
	177776 100000 040000 020000 010000 004000 001000 001000 000400 000200 0000100 000040

### Option B-Prime (4001 Controller)

	1 77777	Dea Desera Tarreleion
	177777	Bad Record Identifier
	177776	Device Not Ready
	177775	Memory Parity Error During DMX
1	100000	Bit 1 Always Set
2	040000	DMX Overrun
3	020000	Write Protect Status
4	010000	Checksum Error
5-9	007600	Unused
10	000100	Unit 1 Seeking
11	000040	Unit 2 Seeking
12	000020	Unit 3 Seeking
13	000010	Unit 4 Seeking
14	000004	Illegal Seek
15	000002	Malfunction Detected
16	000001	Not Ready (Software)
	000000	Redundant Int. (Warm Start)

### Diskette Controller

	177777	Bad Record Identifier
	177776	Device Not Ready
1	100000	Normal End of Instruction
2	040000	Sector Not Found
3	020000	Checksum Error on Sector ID
4	010000	Track Error (head misposition)
5	004000	Bad OTA or Not Ready
6	002000	Deleted Data Mark Read
7	001000	DMX Overrun
8	000400	Chksum err, Write Prot. Violation,
		Inoperable on Write or Format
<b>9-</b> 15	000376	Unused
16	000001	Not Ready
	000000	Redundant Int. (Warm Start)

### Storage Module (4004 Controller)

	177777	Bad Record Identifier
	177776	
	177775	Memory Parity Error During DMX
1	100000	
2	040000	DMA Overrun
3	020000	Write Protect
4	010000	Read Check
5	004000	Data Parity Error
6	002000	Header Check
7-10	001700	Unused
11	000040	Busy (Dual Port Only)
12	000020	Unused
13	000010	Seeking
14	000004	Illegal Seek
15	000002	Select Error
16	000001	Not Ready
	000000	Redundant Int. (Warm Start)

### Disk Sizes

\*\*\* Disk types and sizes \*\*\*
to be supplied at next update

#### DMX CONTROL WORDS

#### DMA

```
1 12 13
Ø | -WORD COUNT 0000 |
1 | STARTING ADDRESS |
```

OTA '14dd: | N | NNN | Ø CHAN ADDRESS

NNNN = Number of channels -1.

#### DMC

OTA '14dd: |N|NNN|1 CHAN ADDRESS

NNNN = Number channels - 1.

#### DMQ

	1 16	;
Ø	$ \mathbf{T} \mathbf{T}\mathbf{T}\mathbf{T} \mathbf{T}\mathbf{T}\mathbf{T} \mathbf{T}\mathbf{T}\mathbf{T} \mathbf{T}\mathbf{T}\mathbf{T}$	(Read Pointer)
1	B BBB BBB BBB BBB BBB	(Write pointer)
	Ø   PPP PPI	
3	M MMM MMM MMM MMM MMM	(mask)

mask = len-1 of Q len = 2\*\*K,  $4\le K\le 10$ (Queue must be on 2\*\*K boundary.)

INPUT: End of Range if no room.

OUTPUT: EOR if empty (not w/last entry).

#### DMT

Device Defined.

#### MAGTAPE

#### Command Bit Definitions

1	100000	Select Transport (bits 9-12)
2	040000	Ø=>File Operation, l=>Record Op
3	020000	<pre>Ø=&gt;Read/Write Op, 1=&gt;Spacing Op</pre>
4	010000	1=>9-Track Read and Correct
5	004000	<pre>Ø=&gt;Binary, 1=&gt;BCD (7-track only)</pre>
6	002000	Ø=>7-Track Transport, 1=>9-Track
7	001000	Unused
8	000400	1=>2 Characters per Word
9	000200	l=>Forward Motion (bits 10,11=0)
10	000100	l=>Reverse Motion (bits 9,11,12=0)
11	000040	l=>Rewind (bits 9,10,12=0)
12	000020	l=>Write Order
13	000010	Select Transport Ø
14	000004	Select Transport 1
15	000002	Select Transport 2
16	000001	Select Transport 3
		<u>-</u>

#### Magtape Commands

```
100000 Select Transport (7 and 9 track)
000040 Rewind to BOT (7 and 9 track)
022100 Backspace File Mark, 9-track
020100 Backspace File Mark, 7-track
062100 Backspace Record, 9-track
060100 Backspace Record, 7-track
022220 Write File Mark, 9-track
020220 Write File Mark, 7-track
062200 Forward Space Record, 9-track
060200 Forward Space Record, 7-track
022200 Forward Space File Mark, 9-track
020200 Forward Space File Mark, 7-track
042220 Write Record One Char/Word, 9-track
042620 Write Record Two Char/Word, 9-track
042200 Read Record One Char/Word, 9-track
042600 Read Record Two Char/Word, 9-track
052200 Read/Correct Record One Char/Word, 9-track
052600 Read/Correct Record Two Char/Word, 9-track
040220 Write Binary Record One Char/Word, 7-track
040620 Write Binary Record Two Char/Word, 7-track
044220 Write BCD Record One Char/Word, 7-track
044620 Write BCD Record Two Char/Word, 7-track
040200 Read Binary Record One Char/Word, 7-track
040600 Read Binary Record Two Char/Word, 7-track
044200 Read BCD Record One Char/Word, 7-track
044600 Read BCD Record Two Char/Word, 7-track
```

#### Magtape Status

1	100000	Parity Error
2	040000	Runaway Tape
3	020000	
4	010000	LRC Error
5	004000	Low DMX Range
6	002000	
7	001000	Read-After-Write (RAW) Error
8	000400	File Mark Detected
9	000200	Ready
10	000100	Online
11	000040	End of Tape Detected
12	000020	Rewinding
13	000010	Beginning of Tape (at Load Point)
14	000004	Tape is Write-Protected
15	000002	
16	000001	Rewind Complete
		•

(000300 or 000304 - Normal Completion)

### PROGRAMMED I/O (PIO)

### OCP -- Output Control Pulse

03FFDD FF=Function, DD=Device Address

### SKS -- Skip on Condition

13CCDD CC=Condition, DD=Device Address

## INA -- Input to A-Register

07FFDD FF=Function, DD=Device Address

No skip for device '20

Always skips if status register input.

## OTA -- Output from A=Register

17FFDD FF=Function, DD=Device Address

No skip if device '20.

### Standard Functions

FF OCP	SKS	INA	OTA
ØØ	Ready	Data Reg	
Ø1	Not Busy		
Ø2			
Ø3			
Ø4	Not Interr	upting	
Ø5		• ,	
Ø6			
Ø7			
10			
11		Input ID	
12 Normal M	lode	-	
13 Diagnost	ic Mode		
14 Ack Inte			DMX Channel
15 Set Int	-		
16 Reset Ir			Int Vect Addr
17 Initiali			THE VECE MAGE
1 / 1111 CTGT	L 2 C		

#### 8 PRIMOS IV

#### ABORT FLAGS

PCB+4, ABSAVE at 6000/10

100000	MINALM	ONE MINUTE ABORT FLAG
040000	SMLALM	SLMC ALARM
020000	NETALM	NETWORK ALARM
004000	WRMALM	WARM START ALARM
000200	MTIALM	MTDONE, CONTROLLER 1
000100	MT2ALM	MTDONE, CONTROLLER 2
000020	LOGALM	FORCED LOGOUT ALARM
000010	DISALM	DISCONNECT ALARM
000004	TMOALM	TIMEOUT ALARM
000002	QUTALM	QUIT ALARM
000001	TSEALM	TIME SLICE END (FIRMWARE)

#### COMMONS

\*\*\* List of COMMONS \*\*\*
to be supplied at next update

#### ERRVEC

In PUDCOM at 6000/106

ERRVEC(1) ALTVAL
ERRVEC(2) ALTVAL(2)
ERRVEC(3) NAME (0=>NC NAME)
ERRVEC(4) "
ERRVEC(5) "
ERRVEC(6) "

ERRVEC(7) WORD NUMBER OF MSG OR Ø ERRVEC(8) LENGTH (IN CHARS) OF MSG ERRVEC(9) SEGMENT NUMBER OF MSG

V-mode error messages saved in ERRVEC:

MESSAGE	ERRVEC (1-2)
ACCESS VIOLATION	PB of instr causing violation
ILLEGAL PAGE REF	32-bit ptr into ill page
ILLEGAL SEGNO	32-bit ptr into ill seg
POINTER FAULT	PB of instr causing fault
NO AVAIL SEGMENTS	32-bit ptr to seg that system
	attempted to create
UNDEFINED GATE	32-bit ptr into gate seg

#### FIGCOM

Starts at 4/700

700 LOUTOM 1000 INACTIVE MINUTES TO AUTO LOGOUT 701 RWLOCK 1 SYSTEM READ/WRITE LOCK: 0 - 1 READER OR 1 WRITER 1 - N READERS OR 1 WRITER 3 - N READERS AND 1 WRITER 5 - N READERS AND N WRITERS 702 DONSTP 0 0=>LOGOUT PHANTOMS ON WARM START 1=>CONTINUE PHANTOMS ON WARM START 0=>LOGOUT PHANTOMS ON WARM START 1=>FORCE LOGOUT ON DISCONNECT 1	TOC	NAME	DFLT	DEFINITION
701 RWLOCK 1  SYSTEM READ/WRITE LOCK:  0 - 1 READER OR 1 WRITER  1 - N READERS OR 1 WRITER  3 - N READERS AND 1 WRITER  5 - N READERS AND N WRITERS  702 DONSTP 0  0=>LOGOUT PHANTOMS ON WARM START  1=>CONTINUE PHANTOMS ON WARM START  1=>CONTINUE PHANTOMS ON WARM START  1=>FORCE LOGOUT ON DISCONNECT  1=>FORCE LOGOUT ON DISCONNECT  DEFAULT ERASE CHARACTER = "  705 DEFKIL 277 DEFAULT KILL CHARACTER = ?  706 PRI500  707 VERSIO  PRIMCS REVISION ID (ASCII)  720 NLGPRT  1=>INHIBIT LOGIN WHILE LOGGED IN				
0 - 1 READER OR 1 WRITER 1 - N READERS OR 1 WRITER 3 - N READERS AND 1 WRITER 5 - N READERS AND N WRITERS 6 - N READERS AND N WRITERS 702 DONSTP 0 0=>LOGOUT PHANTOMS ON WARM START 1=>CONTINUE PHANTOMS ON WARM START 0=>IGNORE DISCONNECT 1=>FORCE LOGOUT ON DISCONNECT 1=>FORCE LOGOUT ON DISCONNECT DEFAULT ERASE CHARACTER = " 705 DEFKIL 277 DEFAULT KILL CHARACTER = ? 706 PRI500 1=>PS00 707 VERSIO PRIMCS REVISION ID (ASCII) 720 NLGPRT 1=>INHIBIT LOGIN WHILE LOGGED IN	700	LOUTOM	1000	INACTIVE MINUTES TO AUTO LOGOUT
1 - N READERS OR 1 WRITER 3 - N READERS AND 1 WRITER 5 - N READERS AND N WRITERS 702 DONSTP 0 0=>LOGOUT PHANTOMS ON WARM START 1=>CONTINUE PHANTOMS ON WARM START 0=>LOGOUT PHANTOMS ON WARM START 1=>CONTINUE PHANTOMS ON WARM START 1=>FORCE LOGOUT ON DISCONNECT 1=>FORCE LOGOUT ON DISCONNECT DEFAULT ERASE CHARACTER = " 705 DEFKIL 277 DEFAULT KILL CHARACTER = ? 706 PRI500 1=>PS00 707 VERSIO PRIMCS REVISION ID (ASCII) 720 NLGPRT 1=>INHIBIT LOGIN WHILE LOGGED IN	7Ø1	RWLOCK	1	SYSTEM READ/WRITE LOCK:
3 - N READERS AND 1 WRITER 5 - N READERS AND N WRITERS 702 DONSTP 0 0=>LOGOUT PHANTOMS ON WARM START 1=>CONTINUE PHANTOMS ON WARM START 0=>IGNORE DISCONNECT 1=>FORCE LOGOUT ON DISCONNECT 1=>FORCE LOGOUT ON DISCONNECT DEFAULT ERASE CHARACTER = " 705 DEFKIL 277 DEFAULT KILL CHARACTER = ? 706 PRI500 1=>PS00 707 VERSIO PRIMCS REVISION ID (ASCII) 720 NLGPRT 1=>INHIBIT LOGIN WHILE LOGGED IN				0 - 1 READER OR 1 WRITER
5 - N READERS AND N WRITERS  702 DONSTP 0 0=>LOGOUT PHANTOMS ON WARM START 1=>CONTINUE PHANTOMS ON WARM START  703 DLOGOT 0 0=>IGNORE DISCONNECT 1=>FORCE LOGOUT ON DISCONNECT  704 DEFERA 242 DEFAULT ERASE CHARACTER = "  705 DEFKIL 277 DEFAULT ERASE CHARACTER = ?  706 PRI500 1=>P500  707 VERSIO PRIMCS REVISION ID (ASCII)  720 NLGPRT 1=>INHIBIT LOGIN WHILE LOGGED IN				1 - N READERS OR 1 WRITER
702 DONSTP 0 0=>LOGOUT PHANTOMS ON WARM START 1=>CONTINUE PHANTOMS ON WARM START 703 DLOGOT 0 0=>IGNORE DISCONNECT 1=>FORCE LOGOUT ON DISCONNECT 704 DEFERA 242 DEFAULT ERASE CHARACTER = " 705 DEFKIL 277 DEFAULT KILL CHARACTER = ? 706 PRI500 1=>P500 707 VERSIO PRIMCS REVISION ID (ASCII) 720 NLGPRT 1=>INHIBIT LOGIN MESSAGES 721 LOGOUT 1=>CAN'T LOGIN WHILE LOGGED IN				3 - N READERS AND 1 WRITER
1=>CONTINUE PHANTONS ON WARM START  1=>CONTINUE PHANTONS ON WARM START  0=>IGNORE DISCONNECT  1=>FORCE LOGOUT ON DISCONNECT  DEFAULT ERASE CHARACTER = "  DEFAULT ERASE CHARACTER = "  DEFAULT KILL CHARACTER = ?  DEFAULT KILL CHARACTER = ?  PRIMOS REVISION ID (ASCII)  RUGPRT 1=>INHIBIT LOGIN MESSAGES  LOGOUT 1=>CAN'T LOGIN WHILE LOGGED IN				5 - N READERS AND N WRITERS
703 DLOGOT 0 0=>IGNORE DISCONNECT  1=>FORCE LOGOUT ON DISCONNECT  704 DEFERA 242 DEFAULT ERASE CHARACTER = "  705 DEFKIL 277 DEFAULT KILL CHARACTER = ?  706 PRI500 1=>P500  707 VERSIO PRIMCS REVISION ID (ASCII)  720 NLGPRT 1=>INHIBIT LOGIN MESSAGES  721 LOGOUT 1=>CAN'T LOGIN WHILE LOGGED IN	7Ø2	DONSTP	Ø	Ø=>LOGOUT PHANTOMS ON WARM START
1=>FORCE LOGOUT ON DISCONNECT  704 DEFERA 242 DEFAULT ERASE CHARACTER = "  705 DEFKIL 277 DEFAULT KILL CHARACTER = ?  706 PRI500 1=>P500  707 VERSIO PRIMOS REVISION ID (ASCII)  720 NLGPRT 1=>INHIBIT LOGIN MESSAGES  721 LOGOUT 1=>CAN'T LOGIN WHILE LOGGED IN				1=>CONTINUE PHANTOMS ON WARM START
704 DEFERA 242 DEFAULT ERASE CHARACTER = " 705 DEFKIL 277 DEFAULT KILL CHARACTER = ? 706 PRI500 1=>P500 707 VERSIO PRIMCS REVISION ID (ASCII) 720 NLGPRT 1=>INHIBIT LOGIN MESSAGES 721 LOGOUT 1=>CAN'T LOGIN WHILE LOGGED IN	7Ø3	DLOGOT	Ø	Ø=>IGNORE DISCONNECT
705 DEFKIL 277 DEFAULT KILL CHARACTER = ? 706 PRI500 1=>P500 707 VERSIO PRIMCS REVISION ID (ASCII) 720 NLGPRT 1=>INHIBIT LOGIN MESSAGES 721 LOGOUT 1=>CAN'T LOGIN WHILE LOGGED IN				1=>FORCE LOGOUT ON DISCONNECT
706 PRI500 1=>P500 707 VERSIO PRIMCS REVISION ID (ASCII) 720 NLGPRT 1=>INHIBIT LOGIN MESSAGES 721 LOGOUT 1=>CAN'T LOGIN WHILE LOGGED IN		DEFERA	242	DEFAULT ERASE CHARACTER = "
707 VERSIO PRIMCS REVISION ID (ASCII) 720 NLGPRT 1=>INHIBIT LOGIN MESSAGES 721 LOGOUT 1=>CAN'T LOGIN WHILE LOGGED IN		DEFKIL	277	DEFAULT KILL CHARACTER = ?
720 NLGPRT 1=>INHIBIT LOGIN MESSAGES 721 LOGOUT 1=>CAN'T LOGIN WHILE LOGGED IN		PRI500		1=>P500
721 LOGOUT 1=>CAN'T LOGIN WHILE LOGGED IN				PRIMCS REVISION ID (ASCII)
1 SCAN I LOGIN WHILE LOGGED IN		NLGPRT		1=>INHIBIT LOGIN MESSAGES
722 LRQUOT 10000 LOGREC QUOTA				
	722	LRQUOT	10000	LOGREC QUOTA

### INTERNAL CALLING SEQUENCES

AINIT VERSIO, MEMSIZ ALCONF A,B ALONF BNO AMINIT ---ASRDIM --BADDSK DISK (LOG FCN) BFDEQU BUFCON, NW (FCN) BFENQU BUFCON, NW BFGETR BUFCON, NW (FCN) BFRELS BUFCON BOOT --BRPDIM ---BRPONF FLAG BRPOTA CHAR (LOG FCN) BUFCHK BNO, NUMCHARS (LOG FCN) BUFCLR BNO CE2DIM --CENDIM ---CGETBK SIZE, ADDR, COND, ALTRIN CLNLUN USRBLK, LUNADR, STAT CLOSE\$ UNIT CNFLCT KEY, PDEV (LOG FCN) COMXIT --COPYUP --CRDONF FLAG DATE\$ XXX (FCN) DELAY A,B,C,ALTRIN DELBKQ QNM, BKN, ALTRIN DEMOTE ---DEQUE NUM, ADDR, COUNT, ALTRIN

DEVCHK USR (FCN)	MODTS DELTA
DEVONF DEVNDX,FLAG	MOV32P LOC32 (FROM) , LOC32 (TO) , NW
DISMSG USRBLK, LUNADR	MOVNAM INAME, ILEN, ONAME, OLEN, TRULEN
DOSSUB	
DSKEQV DISKA, DISKB (LOG FCN)	MOVS2S FRMSEG,LOC16 (FROM), TOSEG,LOC16 (TO), NW
	MOVUTU FRMUSR, FRMSEG, LOC16 (FROM), TOUSR, TOSEG,
	LOC16 (TO), NW
ENQUEB NUM, ADDR, COUNT	MPINIT
ENQUET NUM, ADDR, COUNT	MSGOUT KEY, USR, LOC32 (BFR) (FCN)
ERRST\$ LCODE, KEY, TEXT, TXTLEN, MYNAME, MYLEN	MTDONE CTRLR-INDEX
EXPNAM INNAME, NAMLEN, OUTNAM	NETALM
F\$HT CODE	
FAMERR	NETMAN
FAMMSG FUNNO, FAMNO, RA, LOC32(A1), N1, LOC32(A2), N2,	NETMSG FLGW, LUNADR
10020/32 N3 10020/34) N4 10020/35) NE	NETXEC
LOC32 (A3) ,N3,LOC32 (A4) ,N4,LOC32 (A5) ,N5,	NEWDAM DRWP, UNIT, NRAL, CODE
LOC32 (A6), N6	NOTIFY OPTION, SEMAPHORE
FMLIOB BNO, CHAR (LOG FCN)	OERRIN ALTVAL, ALTRIN, CODE, TEXT, TXTLEN, NAME, NAMLEN
FNDLUN USRBLK,LSRCID,LUNADR,ALTRIN	PABORT ABORT-FLAGS
FREEBL	PAGTUR LOC32 (VIRTADR)
GCHAR LOC32 (ARRAY), CHARPTR	PHINIT
GET LOC32 (WORD) (FCN)	
GETBLK SIZE, ADDR, ALTRIN	PNDNAM SYSN, NETABL, ALTRIN
	PRCFP ABORT-FLAGS
GETCHT SIZE, COUNT	PRIPC
GETLUN USRBLK,SW,LUNADR,ALTRTN	PRSMLC
GETRBK ALTRIN	PRWERR ALTVAL, CODEV
GETREC RA32, DVNO, CODE (INT*4 FCN)	PTRAP INSTRUCTION
GETREG TVEC	PTRDIM
GETSBK BLKAD	PTRINA CHAR (LOG FCN)
GETSEG USR, SEGNO	· · · · · · · · · · · · · · · · · · ·
GETTBK BLKAD, ALTRIN	PTRONF FLAG
	QCHECK NUM, ELQUED
GETUBK USRBLK, ALTRIN	QTRVRS NUM, BLKN, ADDR, COUNT, ALTRIN
GROSS	QUITON (SHORT CALL)
GTSTAT XXX (FCN)	RMANLZ INST, FT, OP, ACT, DEV
HCRONF FLAG	ROUTER RINGBK, HDROFF, MSGBLK, COUNT
HDFILL TGIN, SRCN, TGTU, MSGBLK, LINTAB, NETHDR, FLGW	RTNBLK ADDR
HPRONØ FLAG	RINREC RA32, DVNO
HPRON1 FIAG	RINSEG SEGNO
TNICYTAAT	KINDEG DEGNO
TIVE IPIN	DUNING DAMPIN AND DESCRIPTION
INEIMN ININET	RUNUSR FAMBLK, NW, ERVEC
ININET	SCHAR LOC32 (ARRAY), CPTR, CHAR
ININET INUSRC	
ININET INUSRC IPCPTM LINEN	SCHAR LOC32 (ARRAY), CPTR, CHAR
ININET INUSRC IPCPTM LINEN ISPREM A,B,C (FCN)	SCHAR LOC32(ARRAY), CPTR, CHAR SCHED RESET-VAL [, QUEUE-SEMAPHORE]
ININET INUSRC IPCPTM LINEN ISPREM A,B,C (FCN) ITIBNZ	SCHAR LOC32(ARRAY), CPTR, CHAR SCHED RESET-VAL [, QUEUE-SEMAPHORE] SDWNDX USR, SEGNO (FCN) SEEK CYL, DVNO
ININET INUSRC IPCPTM LINEN ISPREM A,B,C (FCN)	SCHAR LOC32(ARRAY), CPTR, CHAR SCHED RESET-VAL [, QUEUE-SEMAPHORE] SDWNDX USR, SEGNO (FCN) SEEK CYL, DVNO SEMTN SEMAPHORE, INT1, INT2, CODE
ININET INUSRC IPCPTM LINEN ISPREM A,B,C (FCN) ITLBNZ LGET LOC32 (WORD) (INT*4 FCN)	SCHAR LOC32(ARRAY), CPTR, CHAR SCHED RESET-VAL [,QUEUE-SEMAPHORE] SDWNDX USR, SEGNO (FCN) SEEK CYL, DVNO SEMTN SEMAPHORE, INT1, INT2, CODE SEND USR, LUNADR, ALTRIN
ININET INUSRC IPCPTM LINEN ISPREM A,B,C (FCN) ITIBNZ	SCHAR LOC32(ARRAY), CPTR, CHAR SCHED RESET-VAL [,QUEUE-SEMAPHORE] SDWNDX USR, SEGNO (FCN) SEEK CYL, DVNO SEMTN SEMAPHORE, INT1, INT2, CODE SEND USR, LUNADR, ALTRTN SETABT KEY, USR, ALARM
ININET INUSRC IPCPTM LINEN ISPREM A,B,C (FCN) ITLBNZ LGET LOC32 (WORD) (INT*4 FCN) LINTST LINDFN,ALTRIN LISTF	SCHAR LOC32(ARRAY), CPTR, CHAR SCHED RESET-VAL [,QUEUE-SEMAPHORE] SDWNDX USR, SEGNO (FCN) SEEK CYL, DVNO SEMTN SEMAPHORE, INT1, INT2, CODE SEND USR, LUNADR, ALTRTN SETABT KEY, USR, ALARM SETREG TVEC, PARFLG
ININET INUSRC IPCPTM LINEN ISPREM A,B,C (FCN) ITLBNZ LGET LOC32 (WORD) (INT*4 FCN) LINTST LINDFN,ALTRIN LISTF LOCATE KEY,RA32,LDEV	SCHAR LOC32 (ARRAY), CPTR, CHAR SCHED RESET-VAL [,QUEUE-SEMAPHORE] SDWNDX USR, SEGNO (FCN) SEEK CYL, DVNO SEMTN SEMAPHORE, INT1, INT2, CODE SEND USR, LUNADR, ALTRTN SETABT KEY, USR, ALARM SETREG TVEC, PARFLG SHUTDN —
ININET INUSRC IPCPTM LINEN ISPREM A,B,C (FCN) ITLBNZ LGET LOC32 (WORD) (INT*4 FCN) LINTST LINDFN,ALTRIN LISTF LOCATE KEY,RA32,LDEV LOCK KEY,SEMAPHORE (LOG FCN)	SCHAR LOC32 (ARRAY), CPTR, CHAR SCHED RESET-VAL [,QUEUE-SEMAPHORE] SDWNDX USR, SEGNO (FCN) SEEK CYL, DVNO SEMTN SEMAPHORE, INT1, INT2, CODE SEND USR, LUNADR, ALTRTN SETABT KEY, USR, ALARM SETREG TVEC, PARFLG SHUTDN — SLABRT LPN
ININET INUSRC IPCPTM LINEN ISPREM A,B,C (FCN) ITLBNZ LGET LOC32 (WORD) (INT*4 FCN) LINTST LINDFN,ALTRIN LISTF LOCATE KEY,RA32,LDEV LOCK KEY,SEMAPHORE (LOG FCN) LOCKFS (SHORT CALL)	SCHAR SCHED RESET-VAL [,QUEUE-SEMAPHORE] SDWNDX USR,SEGNO (FCN) SEEK CYL,DVNO SEMTN SEMAPHORE,INT1,INT2,CODE SEND USR,LUNADR,ALTRTN SETABT KEY,USR,ALARM SETREG TVEC,PARFLG SHUTDN — SLABRT LPN SICLDB —
ININET INUSRC IPCPTM LINEN ISPREM A,B,C (FCN) ITLBNZ LGET LOC32 (WORD) (INT*4 FCN) LINTST LINDFN,ALTRIN LISTF LOCATE KEY,RA32,LDEV LOCK KEY,SEMAPHORE (LOG FCN) LOCKFS (SHORT CALL) LOCKPG USR,KEY,PTR32,NW	SCHAR LOC32 (ARRAY), CPTR, CHAR SCHED RESET-VAL [,QUEUE-SEMAPHORE] SDWNDX USR, SEGNO (FCN) SEEK CYL, DVNO SEMTN SEMAPHORE, INT1, INT2, CODE SEND USR, LUNADR, ALTRTN SETABT KEY, USR, ALARM SETREG TVEC, PARFLG SHUTDN — SLABRT LPN SLCLDB — SLCCNF FLAG
ININET INUSRC IPCPTM LINEN ISPREM A,B,C (FCN) ITIBNZ LGET LOC32 (WORD) (INT*4 FCN) LINTST LINDFN,ALTRIN LISTF LOCATE KEY,RA32,LDEV LOCK KEY,SEMAPHORE (LOG FCN) LOCKFS (SHORT CALL) LOCKPG USR,KEY,PIR32,NW LOGEVI MSG	SCHAR SCHED RESET-VAL [,QUEUE-SEMAPHORE] SDWNDX USR,SEGNO (FCN) SEEK CYL,DVNO SEMTN SEMAPHORE,INT1,INT2,CODE SEND USR,LUNADR,ALTRTN SETABT KEY,USR,ALARM SETREG TVEC,PARFLG SHUTDN — SLABRT LPN SICLDB —
ININET INUSRC IPCPTM LINEN ISPREM A,B,C (FCN) ITIBNZ LGET LOC32 (WORD) (INT*4 FCN) LINTST LINDFN,ALTRIN LISTF LOCATE KEY,RA32,LDEV LOCK KEY,SEMAPHORE (LOG FCN) LOCKFS (SHORT CALL) LOCKPG USR,KEY,PIR32,NW LOGEV1 MSG LOGEV2	SCHAR LOC32 (ARRAY), CPTR, CHAR SCHED RESET-VAL [,QUEUE-SEMAPHORE] SDWNDX USR, SEGNO (FCN) SEEK CYL, DVNO SEMTN SEMAPHORE, INT1, INT2, CODE SEND USR, LUNADR, ALTRTN SETABT KEY, USR, ALARM SETREG TVEC, PARFLG SHUTDN — SLABRT LPN SLCLDB — SLCCNF FLAG
ININET INUSRC IPCPTM LINEN ISPREM A,B,C (FCN) ITIBNZ LGET LOC32 (WORD) (INT*4 FCN) LINTST LINDFN,ALTRIN LISTF LOCATE KEY,RA32,LDEV LOCK KEY,SEMAPHORE (LOG FCN) LOCKFS (SHORT CALL) LOCKPG USR,KEY,PIR32,NW LOGEV1 MSG LOGEV2 LOGIN KEY,UNAME,NAMLEN,USNAME,LDEV	SCHAR LOC32 (ARRAY), CPTR, CHAR SCHED RESET-VAL [,QUEUE-SEMAPHORE] SDWNDX USR, SEGNO (FCN) SEEK CYL, DVNO SEMTN SEMAPHORE, INT1, INT2, CODE SEND USR, LUNADR, ALITRTN SETABT KEY, USR, ALARM SETREG TVEC, PARFLG SHUTDN — SLABRT LPN SLCLDB — SLCONF FLAG SLERF KEY
ININET INUSRC IPCPTM LINEN ISPREM A,B,C (FCN) ITIBNZ LGET LOC32 (WORD) (INT*4 FCN) LINTST LINDFN,ALTRIN LISTF LOCATE KEY,RA32,LDEV LOCK KEY,SEMAPHORE (LOG FCN) LOCKFS (SHORT CALL) LOCKPG USR,KEY,PIR32,NW LOGEV1 MSG LOGEV2	SCHAR LOC32 (ARRAY), CPTR, CHAR SCHED RESET-VAL [,QUEUE-SEMAPHORE] SDWNDX USR, SEGNO (FCN) SEEK CYL, DVNO SEMTN SEMAPHORE, INT1, INT2, CODE SEND USR, LUNADR, ALTRTN SETABT KEY, USR, ALARM SETREG TVEC, PARFLG SHUTDN — SLABRT LPN SLCLDB — SLCONF FLAG SLERF KEY SMLCEX SVCF, LPN SRWREC KEY, PBAV, NWV, NCH, RA32, DVNO, ALTRTN
ININET INUSRC IPCPTM LINEN ISPREM A,B,C (FCN) ITLBNZ LGET LOC32 (WORD) (INT*4 FCN) LINTST LINDFN,ALTRIN LISTF LOCATE KEY,RA32,LDEV LOCK KEY,SEMAPHORE (LOG FCN) LOCKFS (SHORT CALL) LOCKPG USR,KEY,PTR32,NW LOGEV1 MSG LOGEV2 LOGIN KEY,UNAME,NAMLEN,USNAME,LDEV LSTORE LOC32 (WORD),DATA	SCHAR LOC32 (ARRAY), CPTR, CHAR SCHED RESET-VAL [,QUEUE-SEMAPHORE] SDWNDX USR, SEGNO (FCN) SEEK CYL, DVNO SEMTN SEMAPHORE, INT1, INT2, CODE SEND USR, LUNADR, ALTRTN SETABT KEY, USR, ALARM SETREG TVEC, PARFLG SHUTDN — SLABRT LPN SLCLDB — SLCONF FLAG SLERF KEY SMLCEX SVCF, LPN SRWREC KEY, PBAV, NWV, NCH, RA32, DVNO, ALTRTN STAC NEWVAL, OLDVAL, VAR (LOG FCN)
ININET INUSRC IPCPTM LINEN ISPREM A,B,C (FCN) ITLBNZ LGET LOC32 (WORD) (INT*4 FCN) LINTST LINDFN,ALTRIN LISTF LOCATE KEY,RA32,LDEV LOCK KEY,SEMAPHORE (LOG FCN) LOCKFS (SHORT CALL) LOCKPG USR,KEY,PTR32,NW LOGEV1 MSG LOGEV2 LOGIN KEY,UNAME,NAMLEN,USNAME,LDEV LSTORE LOC32 (WORD),DATA MAPIO LOC32 (USRBFR),NW,PAGE-MAP-ENTRY (INT*4 FCN)	SCHAR LOC32 (ARRAY), CPTR, CHAR SCHED RESET-VAL [,QUEUE-SEMAPHORE] SDWNDX USR, SEGNO (FCN) SEEK CYL, DVNO SEMTN SEMAPHORE, INT1, INT2, CODE SEND USR, LUNADR, ALITRIN SETABT KEY, USR, ALARM SETREG TVEC, PARFLG SHUTDN — SLABRT LPN SLCLDB — SLCONF FLAG SLERF KEY SMLCEX SVCF, LPN SRWREC KEY, PBAV, NWV, NCH, RA32, DVNO, ALITRIN STAC NEWVAL, OLDVAL, VAR (LOG FCN)
ININET INUSRC IPCPTM LINEN ISPREM A,B,C (FCN) ITLBNZ LGET LOC32 (WORD) (INT*4 FCN) LINTST LINDFN,ALTRIN LISTF LOCATE KEY,RA32,LDEV LOCK KEY,SEMAPHORE (LOG FCN) LOCKFS (SHORT CALL) LOCKPG USR,KEY,PTR32,NW LOGEV1 MSG LOGEV2 LOGIN KEY,UNAME,NAMLEN,USNAME,LDEV LSTORE LOC32 (WORD),DATA MAPIO LOC32 (USRBFR),NW,PAGE-MAP-ENTRY (INT*4 FCN) MAPNDX USR,SEGNO (FCN)	SCHAR LOC32 (ARRAY), CPTR, CHAR SCHED RESET-VAL [,QUEUE-SEMAPHORE] SDWNDX USR, SEGNO (FCN) SEEK CYL, DVNO SEMTN SEMAPHORE, INT1, INT2, CODE SEND USR, LUNADR, ALTRIN SETABT KEY, USR, ALARM SETREG TVEC, PARFLG SHUTDN — SLABRT LPN SLCLDB — SLCONF FLAG SLERF KEY SMLCEX SVCF, LPN SRWREC KEY, PBAV, NWV, NCH, RA32, DVNO, ALTRIN STAC NEWVAL, OLDVAL, VAR (LOG FCN) STIMER TENTHS STORE LOC32 (WORD), DATA
ININET INUSRC IPCPTM LINEN ISPREM A,B,C (FCN) ITLBNZ LGET LOC32 (WORD) (INT*4 FCN) LINTST LINDFN,ALTRIN LISTF LOCATE KEY,RA32,LDEV LOCK KEY,SEMAPHORE (LOG FCN) LOCKFS (SHORT CALL) LOCKPG USR,KEY,PTR32,NW LOGEV1 MSG LOGEV2 LOGIN KEY,UNAME,NAMLEN,USNAME,LDEV LSTORE LOC32 (WORD),DATA MAPIO LOC32 (USRBFR),NW,PAGE-MAP-ENTRY (INT*4 FCN)	SCHAR LOC32 (ARRAY), CPTR, CHAR SCHED RESET-VAL [,QUEUE-SEMAPHORE] SDWNDX USR, SEGNO (FCN) SEEK CYL, DVNO SEMTN SEMAPHORE, INT1, INT2, CODE SEND USR, LUNADR, ALITRIN SETABT KEY, USR, ALARM SETREG TVEC, PARFLG SHUTDN — SLABRT LPN SLCLDB — SLCONF FLAG SLERF KEY SMLCEX SVCF, LPN SRWREC KEY, PBAV, NWV, NCH, RA32, DVNO, ALITRIN STAC NEWVAL, OLDVAL, VAR (LOG FCN)

TEXTO\$ NAME, NAMLEN, TRULEN, TEXTOK TEXTOK FNAMEBER TIME USR, TIM TIME\$ XXX (FCN) TODEC USR, NUM, LEAD-CHAR/FIELDWIDTH TOLIOB BNO, CHAR TPIOS KEY, PBA, PN, RA32, ALTRTN TRNMSG LINTAB, MSGBLK, CNT, MSGVEC TRUNC\$ UNIT, CODE TRWRAT KEY, LDEV UMAPIO LOC32 (USRBFR), NW UNLKFS ---(SHORT CALL) UNLOCK KEY, SEMAPHORE UPDATE KEY, RA32, LDEV UPUSR LOC32 (USRADDR), DATA, NWD VALID TNAME, NETTAB, ALTRIN, NODE VGONF FLAG VGINIT --

\*\*\*\*\*\*\*\*\*\*\*\*\*

WAIT SEMAPHORE

XEOUSR ---

WREC PBA, NW, NCH, CRA32, DVNO, ALTRIN

LOCKS, LCKCOM

Locks are semaphores used to control access to serially reusable resources. Located in LCKCOM (SEG 4), source file N1LOCK.

\*\*\* Addition LOCK Information \*\*\*
to be supplied at next update

#### MMAP (MEMORY MAP)

One entry per physical page of memory.

MMAP + n:

< 0 => Page 'n' unavailable

= 0 => Page 'n' available

> 0 => In use, --> HMAP entry for page

MAXPAG = number pages memory in use.

#### PAGE MAPS

See under Section on CPU.

#### PTUSEG

PTUSEG (2, KSEG) (SEG 4)

PTUSEG(1,N) Owner of Page Map N
PTUSEG(2,N) Segment Number for Page Map N

### PUDCOM

SUPSTK	SN FOR SUPERVISOR STACK
STKSIZ	ONE PAGE OF STACK
PUDCOM SU	PFRE (2), SUPEXT (2)
CUSR	1 CURRENT USER NUMBER
LUSR	1 USERCOM INDEX
VRTSSW	1 VIRTUAL SENSE SWITCHES
INHPRF	1 INHIBIT-PROCESS-FAULT COUNTER
ABSAVE	1 SAVED ABORT FLAGS
HILOCK	1 ADR HIGHEST OWNED ORDERED LOCK
LCKOWN	
OWNES	1 NUMBER ORDERED LOCKS OWNED
QUITF	1 QUIT FLAG, INHIBIT COUNT
ASRCWD	1 ASR CONTROLS
ERASCH	1 CHARACTER ERASE CHARACTER
KILLCH	1 LINE DELETE CHARACTER
TKNSAV	1 RDTK\$\$ SAVEAREA
COMPAR	40 COMMAND LINE INPUT BUFFER
COMSWI	1 COMMAND INPUT SWITCH
COMUNI	1 COMMAND INPUT UNIT
COUSWI	1 COMMAND OUTPUT SWITCH
COUPTR	1 COULIN CHARACTER POINTER
COULIN	10 COMMAND OUTPUT BUFFER
ERRVEC	9 ERRVEC
XSAVE	1 TEMP SAVE FOR X IN FAULT RTES
RVSA	1 START ADDRESS IN RVEC
RVEA	1 END ADDRESS IN RVEC
HMAPPP	
BUFNEW	
	START OF FIRST RING Ø STACK FRAME
	2 PBH AND PBL
	1 KEYS
	1 LOC(PCL)+2 ON RING Ø PCL ENTRY
	29 REGISTER SAVEAREA
	1 SAVE MASK IN RVEC
PUDEND	END OF PUDCOM
	PUDCOM SUCUSR LUSR VRTSSW INHPRF ABSAVE HILOCK LCKOWN OWNFS QUITF ASRCWD ERASCH KILICH TKNSAV COMPAR COMSWI COMUNI COUDTR COULIN ERRVEC XSAVE RVSA RVEA HMAPPP BUFNEW USREIM SUPSF RVPB RVKEYS RVPBCL

## SEGMENT USAGE BY PRIMOS

SEGMENT	CONTENTS
Ø	LOC '61 (OPTION-A memory increment cell) DMC channels for AMLC, SMLC, MAG TAPE AMLC buffers DISK driver (DVDISK) Disk I/O windows (4 pages) Mag tape I/O windows (6 pages) Mag tape dump window (1 page) IPC I/O windows (2 pages) SMLC I/O windows (12 pages)
1	Associative bfrs for file system (64 pages)
2,3	MOVU2U segment windows
4	Interrupt catchers (phantoms) Check catchers Semaphores Ready PCB list (loc '600) Configuration common (FIGCOM) (loc '700) Crash 9 trk magtape dump program (loc '776) Memory parity scanner (loc '777) WARM restart routine (loc '1000) COLD start routine (loc '1400) Memory usage map (MMAP) Page maps (HMAP, LMAP) Segment tables Process control blocks (PCBs) Interrupt fault table Interrupt stack
5	Gate segment for direct-entrance PCLs
6	TMAIN, including: Supervisor and user fault catchers SVC front-ends Supervisor locked data (SUPCOM) Clock process Kernel procedures
7	User terminal buffers
10	Per-user unlocked data (USRCOM)
11	File system procedures
12	Network data and procedures SMLC data and procedures
6000	Ring $\emptyset$ stack segment (one per user)

### SEMAPHORES (SEMCOM)

```
*** Refer to SEMAPHORES in Section II ***

*** Addition SEMAPHORE Information ***

to be supplied in next update
```

#### SVC INTERLUDE

```
ENTRY DAC **

SVC

OCT CODE

1 100000 1 => interlude call
2 040000 1 => bounce
3-4 030000 Unused
5-16 00**** SVC number
```

#### **USRCOM**

```
SEG 10/(USRNO-1)*'505 ...
+1 UNITAB:
                  Ø < UNIT < '21
        +UNIT*17 +0 VSTAT
                 +1 VBRA (2)
                 +3 VDVNO
                 +4 VDCRA(2)
                 +6 VDRWP
                 +10 VCRA(2)
                 +12 VRWP
                 +13 VPRIV
                 +14 VPOPRA(2)
                 +16 VPOPRW
+417 CUFD:
     +0 CURRENT UFD NAME
    +20 CFDBRA(2)
    +22 CFDDEV
    +23 CFDPOP (2)
    +25 CFDOWN
    +26 CFDLEN
    +27 CFDPRA(2)
    +31 CFDPRW
+451 HOMUFD:
    +Ø HOME UFD NAME
    +20 HOMBRA (2)
    +22 HOMDEV
    +23 HOMPOP (2)
    +25 HOMOWN
```

+26 HOMLEN +27 HOMPRA(2) +31 HOMPRW

+503 LOGNAM(3)

#### MTUQV

1	1 MIN.	1 MINUTE (UPDATE, LOGEV2, ETC.
2	16.5 MSEC	PUNCH DIM
3		DIGITAL INPUT
4	112 MSEC	ASR DIM
5	100 MSEC	TENTH SECOND (STIMER QUEUES)
6		UNUSED
7		UNUSED
8		UNUSED
9		UNUSED
10	1/2 SEC	SMLCEX ALARM
11	10 SEC	NETWORK GROSS TIMER
12	1 SEC	IPC PROTOCOL TIMER
13	1/2 SEC	REMOTE USER POLL

#### 9 SVC INFORMATION

#### SVC CALLING SEQUENCES

\* => Also Direct Entrance Call.

### ATCH\$\$ - Attach to UFD

CALL ATCH\$\$ (UFDNAM, NAMLEN, LDISK, PASSWD,

KEY, CODE)

(\*1500)

CALL ATTAC\$ (UFDNAM, NAMLEN, LDISK, PASSWD,

KEY, LOC(CODE))

(1400)

CALL ATTACH (UFDNAM, LDISK, PASSWD, KEY,

ALTRIN)

(0100)

#### \*\*\*\*\* KEY \*\*\*\*\*

K\$IMFD = :0 UFD IS IN MFD

K\$ICUR = :2 UFD IS IN CURRENT UFD

\*\*\*\*\* KEYMOD \*\*\*\*\*

K\$SETC = :0 SET CURRENT UFD (DO NOT SET HOME)

K\$SETH = :1 SET HOME UFD (AS WELL AS CURRENT)

\*\*\*\*\* UFDNAM \*\*\*\*\*

K\$HOME = :0 RETURN TO HOME UFD (KEY=K\$IMFD)

\*\*\*\*\* LDISK \*\*\*\*\*

K\$ALLD = :100000 SEARCH ALL DISKS

K\$CURR = :177777 SEARCH MFD OF CURRENT DISK

\*\*\*\*\* CODES \*\*\*\*\*

E\$NATT

E\$MTUD

E\$FMTF E\$PTRM E\$DISK E\$BPAS

ESBKEY

EŞBUFD

#### BREAK\$ -- Disable/Enable Quits

CALL BREAK\$ (KEY)

(\*0507)

\*\*\*\*\* KEY \*\*\*\*\*

= :0 INHIBITS QUITS

= :1 ENABLE QUITS

#### Clin -- Read Character from Command Stream

CALL Clin (CHAR)

(\*0601)

#### CMREAD -- Read Last Command Line

CALL CMREAD (BUFF (18))

(0602)

BUFF(1-3) First name or blanks

BUFF(4-6) Second name or blanks

BUFF(7-9) Third name or blanks

BUFF(10) First octal parameter or 0

• • •

BUFF(18) Ninth octal parameter or Ø

#### CNAM\$\$ -- Change a Filename

CALL CNAM\$\$ (OLDNAM, OLDLEN, NEWNAM, NEWLEN,

CODE)

(\*1515)

CALL CNAMES (OLDNAM, OLDLEN, NEWNAM, NEWLEN,

LOC (CODE))

(1415)

CALL CNAME (OLDNAM, NEWNAM, ALTRIN) (0113)

\*\*\*\*\* CODES \*\*\*\*\*

E\$BNAM E\$FNTF E\$BUFD E\$IREM E\$EOF E\$NRIT

**E\$EXST** 

#### CNIN\$ -- Raw Data Input from Command Stream

CALL CNIN\$ (BUFF, CHARCNT, STATV(3))

(\*0604)

#### COMANL -- Read Command Line

CALL COMANL

(\*Ø6ØØ)

(0603)

### COMISS -- Switch Command Input Stream

CALL COMINP (FILNAM, UNIT, ALTRIN)

CALL COMISS (FILNAM, NAMLEN, UNIT, CODE) (\*1516)
CALL COMINS (FILNAM, NAMLEN, UNIT, LOC(CODE)) (1416)

\*\*\*\*\* CODES \*\*\*\*\*\*

E\$BDAM E\$NATT

E\$BUFD E\$NRIT

E\$DIRE E\$PTEM

E\$FIUS E\$UIUS

E\$FNTF E\$UNOP

### COMO\$\$ -- Control Routing of Terminal Output

CALL COMO\$\$ (KEY, FILNAM, NAMLEN, Ø, CODE) (\*1523)

#### \*\*\*\*\* KEY \*\*\*\*\*

- :1 TURN TTY OUTPUT OFF
- :2 TURN TTY OUTPUT ON
- :4 (RESERVED)
- :10 TURN FILE OUTPUT OFF
- :20 TURN FILE OUTPUT ON
- :40 APPEND IF TURNING ON, CLOSE IF TURNING OFF
- :100 TRUNCATE IF TURNING ON

\*\*\*\*\* CODES \*\*\*\*\* E\$EOF

#### CONECT -- Connect Logical Unit Number

CALL CONECT (TGTNAM, TGTUSR, LUN, DATA, STATV, LINTYP) (0401)

### CREA\$\$ -- Create New UFD in Current UFD

CALL CREA\$\$ (UFDNAM, NAMLEN, OPASS, NPASS, CODE) (\*1501)

CALL CREAT\$ (UFDNAM, NAMLEN, OPASS, NPASS, LOC(CODE)) (1401)

#### D\$INIT -- Initialize Disk Devices

CALL D\$INIT (PDEV) (0506)

### DISCON -- Disconnect Logical Unit Number

CALL DISCON (LUN, DATA, STATV) (0410)

### DUPLXS -- Set/Return Terminal Characteristics

Integer = DUPLX\$ (LWORD)

#### LWORD Contents:

bit		Meaning when on
1	100000	Half duplex
2	040000	No LF after CR
3	020000	XOFF/XON Recognition
4	010000	XOFF Received
5–8	007400	Reserved
9-10	000377	user number

LWORD = -1 => no update of LWORD. Integer set to new LWORD setting. ERKL\$\$ -- Read/Set Kill and Erase Character

CALL ERKL\$\$ (KEY, ERASEC, KILLC, CODE)

(\*1524)

\*\*\*\*\* KEYS \*\*\*\*\*
see COMO\$\$

\*\*\*\*\* CODES \*\*\*\*\* E\$BKEY E\$BPAR

ERRPR\$ -- Print Standard System Error Messages

CALL ERRPR\$ (KEY, CODE, TEXT, TXTLEN, NAME, NAMLEN) (\*1402)

\*\*\*\*\* KEY \*\*\*\*\*

K\$NRTN = :0 NEVER RETURN TO USER

K\$SRTN = :1 RETURN AFTER START COMMAND

K\$IRTN = :2 IMMEDIATE RETURN TO USER

\*\*\*\*\* CODES \*\*\*\*\* E\$EOF E\$LAST

ERRRIN -- Return Error Code

CALL ERRRIN (ALTRIN, NAME, MSG, MSGLEN) (0106)

ERRSET -- Handle Error Messages

CALL ERRSET (ALTVAL, ALTRIN, NAME, MSG,
MSGLEN) (Ø114)

EXIT -- Return to PRIMOS Command Level

CALL EXIT (\*0105)

FAMSVC -- (Called by FAM only)

CALL FAMSVC (Al, A2, A3, A4, A5, A6, ALTRIN) (0400)

FORCEW -- Update Runit to Disk

CALL FORCEW (KEY, UNIT)

(\*0115)

GETCON -- Get Pending Connect Information

CALL GETCON (TARGET, USER, DATA, STATV) (0402)

GETERR -- Get Error Messages

CALL GETERR (BUFF, NW)

(0110)

GETERR is used after a return from PRWFIL:

On an alternate return:

ERRVEC(1) Error code

On a normal return:

PRWFIL:

FPRVEC(3) Record normal

ERRVEC(3) Record number ERRVEC(4) Word number

ERRVEC(2) Alternate Key of read/write value convenient:

ERRVEC(2) No. of words transferred

SEARCH:

ERRVEC(2) File type

GINFO -- Return Operating System Information

CALL GINFO (BUFF, NW)

Return infomation for PRIMOS II:

BUFF(1) Low bound of PRIMOS II and buffers (77777 octal if 64K PRIMOS II).

2 High bound of PRIMOS II (77777 octal if 64K PRIMOS II).

(not valid)

3 (not valid)

4 (not valid)

Low bound of PRIMOS II and buffer (64K PRIMOS II only).

6 High bound of 64K PRIMOS II.

Returned information for PrIMOS III, IV, and V:

GPAS\$\$ Obtain UFD Passwords		PRWF\$\$ — Read-Write-Position SAM/DAM File
CALL GPAS\$\$ (UFDNAM, NAMLEN, OPASS, NPASS, CODE)	(*1504)	CALL PRWF\$\$ (KEY, FUNIT, LOC(BUFF), BUFLEN, POS32, RNW, CODE) (*1506
CALL GPASS\$ (UFDNAM, NAMLEN, OPASS, NPASS, CODE)	(1404)	CALL PRWFL\$ (KEY, UNIT, LOC(BUFF), NW, POS, RNW, LOC(CODE)) (1406)
****** CODES		CALL PRWFIL (KEY, UNIT, LOC (BUFF), NW, POS, ALTRIN) (0300)
E\$FNTF E\$DISK E\$PTRM E\$BUFD E\$UIUS (bounce package only)		KEY = RWKEY + POSKEY + MODE
E\$FIUS (bounce package only)		***** RWKEY *****  K\$READ = :1
NETLNK —		K\$POSN = :3 POSITION ONLY K\$TRNC = :4 TRUNCATE K\$RPOS = :5 READ CURRENT POSITION
CALL NETLNK (STATV)	(Ø412)	****** POSKEY ******  K\$PRER = :0 PRE-POSITION RELATIVE  K\$PREA = :10 PRE-POSITION ABSOLUTE
NETWAT Put User to Sleep		K\$POSR = :20 POST-POSITION RELATIVE K\$POSA = :30 POST-POSITION ABSOLUTE  ****** MODE ******
CALL NETWAT	(0406)	K\$CONV = :400 CONVENIENT NUMBER OF WORDS
NTSTAT Get Network Status		****** CODES *****  E\$EOF E\$NOF
CALL NISTAT (KEY, Pl, P2, ARRAY)	(Ø4Ø7)	E\$UNOP E\$DKFL E\$DISK E\$PTRM E\$BUNT E\$BOF E\$IREM E\$NTUD
PRERR — Prints Error Messages		E\$IREM E\$NTUD E\$NTSD

(Ø111)

CALL PRERR

RDLIN\$ -- Read Line of Characters from ASCII File

## RDEN\$\$ -- Read UFD Entry

E\$BFTS

CALL RDEN\$\$ (KEY, FUNIT, BUFF, BUFLEN, RNW, NAM32, NAMLN, CODE) (*150	•	:1506)
CALL DUCKING AREA TRILIN DUDG DUCKER DATA	CALL RDLIN (UNIT, LINE, NW, ALTRIN) (0202)	
CALL RDENT\$ (KEY, UNIT, BUFF, BUFLEN, RNW, NAME32, NAMELEN, LOC(CODE)) (1407)	RDTK\$\$ Read Token from Command Line	.406)
Entry Format:		300)
Ø   ECW	CALL RDTK\$\$ (KEY, INFO(8), BUFF, BUFLEN, CODE) (*1517)	(ששכי
1  F	CALL RDTKN\$ (KEY, INFO(8), BUFF, BUFLEN, LOC(CODE)) (1417)	
	INFO(1):	
FILENAME (BLANK PADDED)	l normal token	
A	2 register setting parameter	
і м і	5 null token	
l E i	6 end of line	
17   PROTEC   PROTECTION (OWNER/NONOWNER)		
18 RESERVED RESERVED FOR FUTURE USE	INFO(2): len in chars of token; $null = \emptyset$ len	
19 FILTYP   FILETYPE < END OF ENTRY FOR TYPE=1		
20   DATMOD   DATE LAST MODIFIED	INFO(3): further info about token	
21   TIMMOD   TIME LAST MODIFIED	bit 1 (:100000) dec conversion successful,	
22 RESERVED RESERVED FOR FUTURE USE	value returned in INFO(4).	
23 RESERVED RESERVED FOR FUTURE USE	bit 2 (:040000) oct conversion successful,	
	value returned in INFO(5).	
DATMOD = YYYYYYYMMMMDDDDD where:	This bit always set when	
YYYYYYY is the year module 100	token type is 2.	
MMMM is the month	bit 3 (:020000) token begins with unquoted	
DDDDD is the day	minus sign.	
	bit 4 (:010000) explicit postion for register	
DATMOD is held in binary seconds-since-midnight	setting given, value returned	
divided by 4.	in INFO(4).	
	bits 5 - 16: reserved for future use.	
***** KEY ****	TMEO(A). depend on floor and in TMEO(2)	
4.	<pre>INFO(4): depend on flags set in INFO(3)</pre>	
K\$READ = :1 READ NEXT ENTRY	TNIDO (E) . demand on Elema in TNIDO (2)	
K\$RSUB = :2 READ NEXT SUB-ENTRY	INFO(5): depend on flags in INFO(3)	
K\$GPOS = :3 RETURN CURRENT POSITION IN UFD K\$UPOS = :4 POSITION IN UFD	TNEO(6) - TNEO(0) - recovered for fulling use	
	INFO(6) - INFO(8): reserved for future use	
K\$NAME = :5 READ ENTRY SPECIFIED BY NAME		
	***** CODES *****	
***** CODES *****	E\$BKEY	
E\$EOF E\$NOF	E\$BPAR	
E\$UNOP E\$DISK	E\$BFTS	
E\$PTRM E\$BKEY	will man wan	
E\$BUNT E\$BUFD		
E CDEUC		

RECEIV Receive Message from Remote System		SATR\$\$ Set Attributes in UFD Entry	
CALL RECEIV (LUN, LOC(BUFF), NW, STATV)	(Ø4Ø4)	CALL SATR\$\$ (KEY, NAME, NAMLEN, ARRAY, CODE) (*1510)	Ø6)
RECYCL Cycle to Next User		CALL SATTR\$ (KEY, NAME, NAMLEN, ARRAY, LOC(CODE)) (1410)	6)
CALL RECYCL	(*0505)	***** KEY ***** K\$PROT = :1 SET PROTECTION	Ø)
REST\$\$ Restore Memory Image from File		K\$DTIM = :2 SET DATE/TIME MODIFIED  K\$DMPB = :3 SET DUMPED BIT  K\$RWLK = :4 SET PER FILE READ/WRITE LOCK	
CALL REST\$\$ (RVEC, NAME, NAMLEN, CODE)	(*1520)		
CALL RESTO\$ (RVEC, NAME, NAMLEN, LOC(CODE))	(1420)	***** CODES ***** E\$NATT E\$FNTF E\$DISK E\$PTRM	
CALL RESTOR (RVEC, NAME, ALTRIN)	(0103)	E\$BKEY E\$BUFD	
****** CODES ***** see SRCH\$\$		E\$UIUS (bounce package only) E\$FIUS (bounce package only) E\$OLDP (old partition only)	
RESU\$\$ — Resume Memory Image from File		SAVE\$\$ Save P300 Memory Image as a File	
CALL RESU\$\$ (NAME, NAMLEN)	(*1521)		
CALL RESUM\$ (NAME, NAMLEN)	(1420)	CALL SAVE\$\$ (RVEC, NAME, NAMLEN, CODE) (*1522)	
CALL RESUME (NAME)	(0104)	CALL SAVE\$ (RVEC, NAME, NAMLEN, LOC(CODE)) (1422)	
***** CODES *****  see SRCH\$\$	(,	CALL SAVE (RVEC, NAME) (Ø102)	
bee blengy		SEM\$DR Drain Semaphore	
RJCON Reject Pending Connect		CALL SEM\$DR (SEMNUM, CODE) (*)	
CALL RJCON (TARGET, USER, STATV, NUMTYP)	(0403)	***** CODES *****	
RREC Read Record From Disk to Memory		E\$BPAR	
		SEM\$NF Notify User Semaphore	
CALL RREC (LOC(BUFF), BUFLEN, N, RA, PDEV, ALTRIN)	(0500)	CALL SEM\$NF (SEMNUM, CODE) (*)	
CALL RRECL (LOC (BUFF), BUFLEN, N, RA32, PDEV, ALTRIN)	(Ø516)		
PHINTY	(2210)	****** CODES ***** E\$BPAR E\$SEMO	

SLEEP\$ Suspend Execution	
CALL SLEEP\$ (INT32) (*)	6)
INT32 = number of milliseconds to delay	·
SPAS\$\$ Set UFD Passwords	)
CALL SPAS\$\$ (OPASS, NPASS, CODE) (*1513)	)
CALL SPASS\$ (OPASS, NPASS, LOC(CODE)) (1413)	
****** CODES *****  E\$EOF E\$NOF  E\$UNOP E\$DKFL  E\$NRIT E\$FNTS  E\$EXST E\$DISK  E\$PTRM E\$BKEY	
SRCH\$\$ Open or Close a File	
CALL SRCH\$\$ (KEY, NAME, NAMLEN, UNIT, TYPE, CODE) (*1511)	
CALL SEARC\$ (KEY, NAME, NAMLEN, UNIT, TYPE, LOC(CODE)) (1411)	
CALL SEARCH (KEY, NAME, UNIT, ALTRIN) (0101)	
KEY = ACTION + REF+ NEWFIL	
K\$READ = :1 OPEN FOR READ  K\$WRIT = :2 OPEN FOR WRITE  K\$RDWR = :3 OPEN FOR READING AND WRITING  K\$CLOS = :4 CLOSE FILE UNIT  K\$DELE = :5 DELETE FILE  K\$EXST = :6 CHECK FILE'S EXISTENCE  ******* REF ******  K\$IUFD = :0 FILE ENTRY IN UFD  K\$ISEG = :100 FILE ENTRY IN SEGMENT DIRECTORY  K\$CACC = :1000 CHANGE ACCESS  ****** NEWFIL *****  K\$NSAM = :0 NEW SAM FILE  K\$NDAM = :2000 NEW DAM FILE  K\$NSGS = :4000 NEW DAM SEGMENT DIRECTORY  K\$NSGD = :6000 NEW DAM SEGMENT DIRECTORY  K\$CURR = :177777 CURRENTLY ATTACHED UFD	
	CALL SLEEP\$ (INT32) (*)  INT32 = number of milliseconds to delay  SPAS\$\$ Set UFD Passwords  CALL SPAS\$\$ (OPASS, NPASS, CODE) (*1513)  CALL SPAS\$\$ (OPASS, NPASS, LOC(CODE)) (1413)  ******* CODES ******  ESEOF ESNOF ESUNOP ESDKFL ESNRIT ESFNIS ESEXST ESDISK ESPIRM ESBKEY ESBUNT  SRCH\$\$ Open or Close a File  CALL SRCH\$\$ (KEY, NAME, NAMLEN, UNIT, TYPE, CODE) (*1511)  CALL SEARC\$ (KEY, NAME, NAMLEN, UNIT, TYPE, LOC(CODE)) (1411)  CALL SEARC\$ (KEY, NAME, UNIT, ALTRIN) (0101)  KEY = ACTION + REF+ NEWFIL  ****** ACTION ******  KSREAD = :1 OPEN FOR READ K\$WRIT = :2 OPEN FOR WRITE K\$REAT = :1 OPEN FOR READ K\$WRIT = :2 OPEN FOR READ K\$WRIT = :2 OPEN FOR READ K\$SUGE = :4 OLCSE FILE UNIT K\$DELE = :5 DELETE FILE K\$EXST = :6 CHECK FILE'S EXISTENCE  ****** REF ****** K\$IUFD = :0 FILE ENTRY IN UFD K\$ISEG = :100 FILE ENTRY IN SEGMENT DIRECTORY K\$CACC = :1000 CHANCE ACCESS  ******** NEWFIL ****** K\$NSAM = :0 NEW SAM FILE K\$NDAM = :2000 NEW DAM FILE K\$NDAM = :2000 NEW DAM SEGMENT DIRECTORY K\$NSCS = :6000 NEW DAM SEGMENT DIRECTORY K\$NSCS = :6000 NEW DAM SEGMENT DIRECTORY

***** CODES *****		T\$SLC Performs I/O Over SMIC Lines	
E\$NATT E\$DKFL E\$NRIT E\$FDEL E\$NTUD E\$NTSD E\$FNTF E\$BNAM E\$DNTE E\$DISK E\$BDAM E\$PTRM E\$BKEY E\$BUNT E\$BSUN		CALL T\$SIC (KEY, LINE, LOC(BUFF), NW)	(1001)
EŞSUNO EŞBUFD EŞUNOP (for KŞCACC KEY only) EŞUIUS (not for EŞEXST KEY)		TIMDAT —	6)
E\$FDFL (old partition only) E\$FIUS (not for E\$EXST KEY)		CALL TIMDAT (BUFF, BUFLEN)	(*Ø5Ø2) Ø)
T\$AMIC AMIC Reveive/Transmit  CALL T\$AMIC (LINE, LOC(BUFF), NW, INST, STATV)	( <b>*</b> Ø513)	TIMDAT returns information in BUFF as follows: BUFF(1) Two ASCII characters representing (2) Two ASCII characters representing (3) Two ASCII characters representing (4) Integer time in minutes since midr (5) Integer time in seconds. (6) Integer time in ticks. (7) Integer CPU time used in seconds. (8) Integer CPU time used in in seconds. (9) Integer disk I/O time used in seconds.	month day. year. night.
T\$CMPC Move Card of Info to User's Space  CALL T\$CMPC (UNIT, LOC(BUFF), NW, INST, STATV)	(*Ø512)	(10) Integer disk I/O time used in tick (11) Integer number of ticks per second (12) user number. (13) 6-character login name, left justi (14) (15)	3.
T\$IMPC Print Data		TNOU Output CHARCNT Chars with CR and LF	
CALL T\$LMPC (UNIT, LOC(BUFF), NW, INST, STATV)	(*0511)	CALL TNOU (MSG, CHARCNT)	(*0702)
T\$PMPC Punch Data		TNOUA Output CHARCNT Characters	
CALL T\$PMPC (UNIT, LOC(BUFF), NW, INST, STATV)	(*0515)	CALL TNOUA (MSG, CHARCNT)	(*0703)
T\$MT Move Raw Data from Magtape		TRNMIT Transmit Message to a Remote Machine	
CALL T\$MT (UNIT, LOC(BUFF), NW, INST, STATV)	(*0510)	CALL TRNMIT (LUN, LOC(BUFF), CNT, STATV)	(Ø4Ø5)
T\$VG Print Data on Versatec Printer		UNLINK Disconnect All Logical Unit Numbers	
CALL T\$VG (UNIT, LOC(BUFF), NW, INST, STATV)	(*0514)	CALL UNLINK	(0411)

### WREC -- Write Record from Memory to Disk

CALL WREC (LOC(BUFF), BUFLEN, NW, RA, PDEV, ALTRIN)	(0501)
CALL WRECL (LOC(BUFF), BUFLEN, NW, RA32, PDEV, ALTRIN)	(Ø517
WTLIN\$	
CALL WILIN\$ (UNIT, LINE, NW, CODE)	(*1526)
CALL WILIN (UNIT, LINE, NW, ALTRIN)	(0203)

### SVC NUMBERS

* => P	CLable			
*	SEM\$DR	<b>*</b> Ø6ØØ	COMANL	Ø6)
*	SEMSNF	*0601		
*	SEMSTN	0602	CMREAD	
*	SEMSTS	0603	COMINP	6)
*	SEMSWT	*0604	CNIN\$	
*	SLEEP\$			
		*0702	TNOU	Ø)
0100	ATTACH	<b>*</b> Ø7Ø3	TNOUA	
0101	SEARCH	*0705	DUPLX\$	
0102	SAVE			
Ø103	RESTOR	1001	T\$SLC	
0104	RESUME			
*0105	EXIT	1400	ATTAC\$	
Ø1Ø6	ERRRTN	1401	CREAT\$	
0110	<b>GETERR</b>	*1402	ERRPR\$	
Ø111	PRERR	1404	GPASS\$	
Ø112	GINFO	1406	PRWFL\$	
Ø113	CNAME	1407	RDENT\$	
Ø114	ERRSET	1410	SATTR\$	
<b>*</b> Ø115	FORCEW	1411	SEARC\$	
		1412	SEGDR\$	
0202	RDLIN	1413		
0203	WTLIN	1415	CNAME\$	
		1416	COMIN\$	
Ø3ØØ	PWRFIL	1417	RDTKN\$	
		1420	RESTO\$	
0400	FAMSVC	1421	RESUM\$	
0401	CONECT	1422	SAVE\$	
0402	GETCON			
0403	RJCON	*1500	ATCH\$\$	
0404	RECEIV	*1501		
0405	TRMMIT	*1504		
0406	NETWAT	*1506		
0407	NTSTAT	*1507	RDEN\$\$	
0410	DISCON	*1510	SATR\$\$	
0411	UNLINK	*1511		
0412	NETLNK	*1512		
~-~~		*1513		
0500	RREC	*1515	CNAM\$\$	
0501	WREC	*1516		
*0502	TIMDAT	*1517		
*0505	RECYCL		REST\$\$	
Ø5Ø6	D\$INIT	*1521	RESU\$\$	
*0507	BREAK\$	*1522	SAVE\$\$	
*Ø51Ø	T\$MT		COMO\$\$	
*0511 *0512	T\$LMPC	*1524	ERKL\$\$	
*Ø512 *Ø513	T\$CMPC	*1525	RDLIN\$	
	T\$AMLC	*1526	WTLIN\$	
*Ø514	T\$VG			
*Ø515 Ø516	T\$PMPC			
Ø517	RRECL			
/ דכמ	WRECL			

06)

6)

Ø)

### ERROR MESSAGES AND CODES (SYSCOM>ERRD.F)

E\$EOF≃ 1	END OF FILE	PE
E\$BOF= 2	BEGINNING OF FILE	PG
E\$UNOP= 3	UNIT NOT OPEN	PD,SD
E\$UIUS= 4	UNIT IN USE	SI
E\$FIUS= 5	FILE IN USE	SI
E\$BPAR= 6	BAD PARAMETER	SA
E\$NATT= 7	NO UFD ATTACHED	SL,AL
E\$FDFL= 8	UFD FULL	SK
E\$DKFL= 9	DISK FULL	DJ
E\$NRIT=10	NO RIGHT	SX
E\$FDEL=11	FILE OPEN ON DELETE	SD
E\$PDEL=11 E\$NTUD=12	NOT A UFD	AR
E\$NTSD=13	NOT A SEGDIR	
EŞDIRE=14	IS A DIRECTORY	
E\$FNTF=15	(FILE) NOT FOUND	SH,AH
E\$FNTS=16	(FILE) NOT FOUND IN SEGDIR	SQ
E\$BNAM=17	ILLEGAL NAME	CA
E\$EXST=18	ALREADY EXISTS	CZ
E\$DNTE=19	DIRECTORY NOT EMPTY	
E\$SHUT=20	BAD SHUTDN (FAM ONLY)	BS
E\$DISK=21	DISK I/O ERROR	WB
E\$BDAM=22	BAD DAM FILE (FAM ONLY)	SS
E\$PTRM=23	PTR MISMATCH (FAM ONLY)	PC,DC,AC
E\$BPAS=24	BAD PASSWORD (FAM ONLY)	AN
E\$BCOD=25	BAD CODE IN ERRVEC	
E\$BTRN=26	BAD TRUNCATE OF SEGDIR	
E\$OLDP=27	OLD PARTITION	
E\$BKEY=28	BAD KEY	
E\$BUNT=29	BAD UNIT NUMBER	
E\$BSUN=30	BAD SEGDIR UNIT	SA
E\$BSUN=30 E\$SUNO=31	SEGDIR UNIT NOT OPEN	
E\$NMLG=32	NAME TOO LONG	
E\$NMLG=32 E\$SDER=33 E\$BUFD=34	SEGDIR ERROR	SQ
E\$BUFD=34	BAD UFD	
E\$BFTS=35	BUFFER TOO SMALL	
E\$FITB=36	FILE TOO BIG	
E\$NULL=37	(NULL MESSAGE)	
E\$IREM=38	ILL REMOTE REF	
EŞDVIU=39	DEVICE IN USE	
E\$RLDN=40	REMOTE LINE DOWN	
E\$FUIU=41	ALL REMOTE UNITS IN USE	
E\$DNS=42	DEVICE NOT STARTED	
EŞTMUL=43	TOO MANY UFD LEVELS	
E\$FBST=44	FAM - BAD STARTUP	
E\$BSGN=45	BAD SEGMENT NUMBER	
E\$FIFC=46	INVALID FAM FUNCTION CODE	
E\$TMRU=47	MAX REMOTE USERS EXCEEDED	
E\$NASS=48	DEVICE NOT ASSIGNED	
E\$BFSV=49	BAD FAM SVC	
E\$SEMO=50	SEM OVERFLOW	
E\$NTIM=51	NO TIMER	
E\$FABT=52	FAM ABORT	
E\$FONC=53	FAM OP NOT COMPLETE	-

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### 10 APPENDICES

## ASCII CHARACTER SET

F => Valid file name char R => Rsrved cmd line char ^ => ctrl key depressed

8-Bi Octa Code	-		Code in Left Byte	8-Bit Octal Code Char	Code in Left Byte
200 201 202 203 204 205 206 207 210 211 212 213 214 215 221 222 223 224 225 227 231 232	Char NULL SOHX SETXX ECXX BEL BS HIL VT FF CRS BCP HLF NAXN ETAN STB CAM SUB	ABCDEFGHIJKLMNOPQRSTUVWXYZ	Left Byte  1000 1004 1010 1014 1020 1024 1030 1034 1040 1044 1050 1054 1060 1064 1070 1100 1104 1110 1114 1120 1124 1130 1134 1140 1144 1150	Code Char  240 Sp 241 ! 242 " 243 # 244 \$ 245 % 246 & 247 ' 250 ( 251 ) 252 * 253 + 254 , 255 - 256 . 257 / 260 0 261 1 262 2 263 3 264 4 265 5 266 6 267 7 270 8 271 9 272 :	1200 1204 R 1210 R 1214 F 1220 F 1224 R 1234 R 1240 R 1250 F 1254 1260 R 1264 FR 1270 F 1374 F 1310 F 1314 F 1320 F 1324 F 1330 F 1334 F 1340 F 1344 F 1344 F
233 234	ESC FS	^UP-K ^UP-L	1154 1160	272 : 273 ; 274 <	1350 R 1354 R 1360
235 236 237	GS RS US	^UP-M ^UP-N ^UP-O	1164 1170 1174	275 = 276 > 277 ?	1364 R 1370 1374

8-Bit	:		8-Bit	-	
Octal	_	Code in	Octal	_	Code in
Code	Char	Left Byte	Code	Char	Left Byte
300		1 <b>400</b> R	340		1600 R
	A	1404 F	341	a	1604
3Ø2	В	1410 F	342	b	1610
3Ø3	C	1414 F	343	С	1614
304	D	1420 F	344	đ	1620
3Ø5	E	1424 F	345	e	1624
3Ø6	F	1430 F	346	f	1630
3Ø7	G	1434 F	347	g	1634
	H	1440 F	35Ø	h	1640
311	I	1444 F	351	i	1644
312	J	1450 F	352	j	1650
313	K	1454 F	353	k	1654
314	L	1460 F	354	1	1660
315	M	1464 F	355	m	1664
316	N	1470 F	356	n	167Ø
317	0	1474 F	357	0	1674
320	P	1500 F	360	р	1700
321	Q	1504 F	361	q	1704
	R	1510 F	362	r	171Ø
323	S	1514 F	363	s	1714
324	T	1520 F	364	t	1720
325	Ū	1524 F	365	u	1724
326	V	1530 F	366	v	173Ø
327	W	1534 F	367	W	1734
330	X	1540 F	37Ø	x	1740
331	Y	1544 F	371	У	1744
332	Z	1550 F	372	z	1750
333	[	1554 R	373	{	1754 R
334 \		1560 R	374	1	1760
335	j	1564 R	375	}	1764 R
336	•	1570 R	376	~	1770 R
337		1574 F	377	DEL	1774

### CONVERSION TABLES

OCTAL	DECIMAL	CONVERSION	TABLE

		0011111	DECIM	THE COLV	ALIMIO	IN TABL	<u> </u>	
Ø	Ø	1	2	3	4	5	6	7
10	8	9	10	11	12	13	14	15
20	16	17	18	19	20	21	22	23
30	24	25	26	27	28	29	3Ø	31
40	32	33	34	35	36	37	38	39
50	40	41	42	43	44	45	46	47
60	48	49	50	51	52	53	54	55
70	56	57	58	59	6Ø	61	62	63
100	64	65	66	67	68	69	70	71
110	72	73	74	75	76	77	78	79
120	8Ø	81	82	83	84	85	86	87
130	88	89	90	91	92	93	94	95
140	96	97	98	99	100	101	102	103
150	104	105	106	107	108	109	110	111
160	112	113	114	115	116	117	118	119
170	120	121	122	123	124	125	126	127
200	128	129	130	131	132	133	134	135
210	136	137	138	139	140	141	142	143
220	144	145	146	147	148	149	150	151
230	152	153	154	155	156	157	158	159
240	160	161	162	163	164	165	166	167
250	168	169	170	171	172	173	174	175
260  270	176	177	178	179	180	181	182	183
300	184 192	185	186	187	188	189	190	191
310		193	194	195	196	197	198	199
320	200 208	201 209	202	203	204	205	206	207
3301	216	209 217	210	211	212	213	214	215
340	224	225	218	219	220	221	222	223
350	232	233	226 234	227 235	228	229	230	231
360	240	233	242	243	236	237	238	239
370	248	241	250	243 251	244 252	245	246	247
400	256	257	258	259	260	253 261	254	255
410	264	265	266	267	268	269	262	263
420	272	273	274	275	276		270	271
4301	280	281	282	283	284	277 285	278 286	279
440	288	289	290	291	292	293	200 294	287 295
450	296	297	298	299	3ØØ	293 301	302	295 303
460	304	305	306	3Ø7	308	309	310	311
470	312	313	314	315	316	317	318	319
500	320	321	322	323	324	325	326	327
510	328	329	33Ø	331	332	333	334	335
520	336	337	338	339	340	341	342	343
530	344	345	346	347	348	349	35Ø	351
540	352	353	354	355	356	357	358	359
								555

Ø	Ø	1	2	3	4	5	6	7
550		361	362	363	364	365	366	367
560		369	370	371	372	373	374	375
570		377	378	379	380	381	382	383
600		385	386	387	388	389	390	391
610		393	394	395	396	397	398	399
620		401	402	403	404	405	406	407
630		409	410	411	412	413	414	415
640		417	418	419	420	421	422	423
650   660		425 433	426 434	427 435	428 436	429 437	43Ø 438	431 439
670		441	442	443	444	445	446	447
700		449	450	451	452	453	454	455
710		457	458	459	460	461	462	463
720		465	466	467	468	469	470	471
730		473	474	475	476	477	478	479
740	480	481	482	483	484	485	486	487
750	488	489	490	491	492	493	494	495
760	496	497	498	499	500	5Ø1	502	503
770		505	506	5Ø7	5Ø8	509	51Ø	511
1000		513	514	515	516	517	- 518	519
1010		521	522	523	524	525	526	527
1020		529	53Ø	531	532	533	534	535
1030		537	538	539	54Ø	541	542	543
1040		545	546	547	548	549	55Ø	551
1050		553	554	555	556	557	558	559
1060		561	562	563	564	565	566	567
1070		569	570	571	572	573	574	575
1100		577	578	579	58Ø	581	582	583
1110		585	586	587	588	589	590	591
1120		593	594	595	596	597	598	599
1130	600	601	602	603	604	605	606	607
1140	6Ø8	609	61Ø	611	612	613	614	615
1150		617	618	619	620	621	622	623
1160		625	626	627	628	629	63Ø	631
1170		633	634	635	636	637	638	639
1200		641	642	643	644	645	646	647
1210		649	65Ø	651	652	653	654	655
1220		657	658	659	66Ø	661	662	663
1230		665	666	667	668	669	67Ø	671
1240		673	674	675	676	677	678	679
1250		681	682	683	684	685	686	687
1260		689	690	691	692	693	694	695
1270		697	698	699	700	701	702	703
1300		705	706	707	708	709	710	711
1310		713	714	715	716	717	718 726	719 727
1320		721	722	723	724	725		=
1330		729	730	731	732	733	734	735
1340		737 7 <b>4</b> 5	738	739 747	740	741 740	742	743
1350 1360		7 <del>4</del> 5 753	746 754	755	748 756	749 757	75Ø 758	751 759
1370	-	761	762	763	764	765	766	767
1400		769	702 77Ø	703 771	772	773	774	775
1410		777	778	779	78Ø	781	782	783
1420		785	786	787	788	789	79Ø	791
1430	•	793	794	795	796	797	798	799
1440		801	802	803	804	805	8Ø6	8Ø7

Ø	Ø	1	2	3	4	5	6	7
1450	808	809	810	811	812	813	814	815
1460	816	817	818	819	820	821	822	823
1470	824	825	826	827	828	829	83Ø	831
1500	832	833	834	835	836	837	838	839
1510	840	841	842	843	844	845	846	847
1520	848	849	85Ø	851	852	853	854	855
1530	856	857	858	859	860	861	862	863
1540	864	865	866	867	868	869	870	871
1550	872	873	874	875	876	877	878	879
1560	880	881	882	883	884	885	886	887
1570	888	889	8 <b>9</b> Ø	891	892	893	894	895
1600	896	897	898	899	900	9Ø1	902	903
1610	904	905	906	907	9Ø8	909	910	911
1620	912	913	914	915	916	917	918	919
1630	920	921	922	923	924	925	926	927
1640	928	929	930	931	932	933	934	935
1650	936	937	938	939	940	941	942	943
1660	944	945	946	947	948	949	95Ø	951
1670	952	953	954	955	956	957	958	959
1700	960	961	962	963	964	965	966	967
1710	968	969	97Ø	971	972	973	974	975
1720	976	977	978	979	98Ø	981	982	983
1730	984	985	986	987	988	989	99Ø	991
1740	992	993	994	995	996	997	998	999
1750	1000	1001	1002	1003	1004	1005	1006	1007
1760	1008	1009	1010	1011	1012	1013	1014	1015
1770	1016	1017	1018	1019	1020	1021	1022	1023

### POWERS OF TWO

### POSITIVE POWERS OF TWO

n   2 1   2		
2   4 3   8		
3   8		
4   16		
5   32		
6   64		
7   128		
8   256		
9   512		
10   1024		
11   2048		
12   4096		
13   8192		
14   16384		
15   32768		
16   65536		
17   13107	2	
18   26214	4	
19   52428	8	
20   10485	76	
21   20971	52	
22   41943	Ø4	
23   83886	Ø8	
24   16777	216	
25   33554	432	
26   67108	864	
27   13421	7728	
28   26843	5456	
29   53687	Ø912	
30   10737	41824	
31   21474	83648	
32   42949	67296	
33   85899	34592	
34   17179	86918	4
35   34359	73836	8
36   68719	47673	6
37   13743	89534	72
38   27487	79069	44
39   54975	58138	88
40   10995	11627	776
41   21990	23255	552
42   43980	46511	104
43   87960	93022	208
44   17592	18604	4416
45   35184	37208	8832
46   70368	74417	7664
47   14073	74883	55328
48   28147	49767	10656

### NEGATIVE POWERS OF TWO

n						
Ø						
1	Ø <b>.</b> 5					
2	Ø.25					
3						
4	0.0625					
5	0.03125					
6	0.01562	5				
7	0.00781	25				
	0.00390	625				
9	0.00195	3125				
10	0.00097	65625				
	0.00048	82812	5			
12	0.00024	41406	25			
13	0.00012	20703	125			
14	0.00006	10351	5625			
15	0.00003	Ø5175	78125			
16	0.00001	52587	89062	5		
	0.00000	76293	94531	25		
18	0.00000	38146	97265	625		
	0.00000	19073	48632	8125		
	0.00000	09536	74316	40625	_	
	0.00000	Ø4768	37158	20312	5	
	0.00000	Ø2384	18579	10156	25	
	0.00000	Ø1192	Ø9289	55078	125	
	0.00000	00596	04644	77539	Ø625	
	0.00000	ØØ298	Ø2322	38769	53125	_
	0.00000	00149	01161	19384	76562	5 25
	0.00000	00074	50580	59692	38281 19140	625
	0.00000	00037	25290	29846		3125
	0.00000	00018 00009	62645 31322	14923 57461	Ø957Ø 54785	15625
	0.00000   0.00000	00004	65661	28730	77392	57812
		00004	32830	64356	28696	28906
	0.00000   0.00000	00001	16415	32182	69348	14453
	0.00000	00000	58207	66091	34674	Ø7226
	0.00000	00000	29103	83045	67337	Ø3613
	0.00000	00000	14551	91522	83668	51806
	0.00000	00000	Ø7275	95761	41834	25903
	0.00000	00000	Ø3637	97880	70917	12951
	0.00000	00000	Ø1818	98940	35458	56475
	0.00000	00000	00909	49470	17629	28237
	0.00000	00000	00454	74735	Ø8864	64118
	0.00000	00000	ØØ227	37367	54432	32059
	0.00000	00000	ØØ113	68683	77216	16029
	0.00000	00000	00056	84341	886Ø8	08014
• •	, ~.~~	2222	2220			

#### 11 GLOSSARY

ALTRIN

Alternate return.

BUFF

Buffer (usually INTEGER\*2 array).

AΡ

argument pointer.

code

a value returned by a routine indicating either the success of or the reason for failure to accomplish the requested action.

command

a program called from command level. (See internal command and external command, below.)

command level

the process state in which input lines are interpreted by Primos as commands. A process is at command level when a user logs in and when a command either completes, encounters an error, or stops after a quit signal is issued.

crash

an unplanned interruption of system availability caused by problems in hardware and/or software.

CRASH ADDR

displacement in hardware register save area (pointed to by RSAVPTR -- R37).

CRS

Current register set.

DAM file

a direct access method file.

directory

a catalog of files and other subordinate directories. See MFD, UFD, and segment directory.

DSW

diagnostic status word.

DTAR

descriptor table address register.

DVNO

physical device number.

ECB

Entry Control Block.

external command

a command that executes in user address space.

FADDRH

fault address, high.

FADDRL

fault address, low.

fault

a hardware or softwre condition that causes system failure.

FCODE

fault code.

file

a sam or dam file.

filename

a name given to an item in a directory.

FUNIT

A file system unit number 0-'21.

internal command

a command that executes in the address space occupied by the Primos operating system.

interrupt

A signal received from a device in the external world (including clocks) indicating that the device either needs to be serviced or has completed an operation.

LB

linkage base register.

login name

A 6-character name by which each logged-in user is known to PRIMOS.

LSB

Least Significant Bit.

MFD

master file directory. An MFD contains information about each UFD on the disk.

MSB

Most Significant Bit.

NODE

the name of a system connected to PRIMENET.

NW

Number of words.

non-owner, NPASS

Non-owner password.

owner, OPASS

Owner password.

page

a 1024 16-bit word block of data within a segment.

page control

the routines that manage the transfer of pages between secondary storage and main memory frames.

paged address space

nonvisible memory; allocation of physical memory.

password

a character string supplied by the user that controls his access to various files and directories. See owner and non-owner.

PB

procedure base register.

PBH

procedure base register, high side.

PBL

procedure base register, low side.

PCB

see process control block.

**PDEV** 

physical device number.

physical volume

a disk pack.

PMNT

page map entry.

pointer

an address value either 16-bits or 32-bits in length.

procedure control block

64-word block describing current state of a process.

process

an address space and an execution point. Each logged-in user has his own process.

register file

128 32-bit registers partitioned into 4 32-register blocks. The first block is reserved for microcode use, the second block is used for DMA channels, the third and fourth blocks are process register sets.

RFIL, RFILE

Register file address.

ring

a level of privilege at which programs execute. Supervisor programs run in ring  $\emptyset$ ; most user programs run in ring 3.

SAM file

a sequential access method file.

SB

stack base pointer.

SDW

Segment Descriptor Word.

segment directory

a directory that contains nothing but pointers to the first record of each file cataloged in it.

semaphore

a special purpose integer variable allocated in the universe in which the processes are embedded, to perform explicit mutual synchronization of parallel sequential processes.

segmented address space

visible memory; allocation of user's data/procedures.

stack

a pushdown list where active procedures maintain private regions used for temporary variables.

STATV

A three word vector used by the T\$xxxx routines. STATV(1) set to  $\emptyset$  when operation completes, STATV(2) = device status, STATV(3) = number words transferred.

TRAP

An asynchronous interruption of sequential microcode execution.

treename, tree

a character string that specified a file by its position in the file system hierarchy. Valid treenames are of the form:

[<ldev>]ufdname[ password][>ufdname ...]>filename

Use of < and > in a treename are literal and must be typed as shown. If the treename includes embedded blanks and is entered at PRIMOS command level, it should be surrounded by apostrophes.

UFD

User File Directory. An UFD contains information about the location and content of each file cataloged in it.

UNIT

See FUNIT.

wired page

a page that remains in main memory at all times.

word

a unit of information that is 16 bits in length.

 $X\!B$ 

temporary base register.

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